VIT ®
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

ic-ETITE'24

Second International Conference on

EMERGING TRENDS IN INFORMATION TECHNOLOGY AND ENGINEERING

Organized by
School of Information Technology and Engineering

22 & 23 February 2024
Vellore Institute of Technology
Vellore, India.

ic-ETITE'24

About ic-ETITE

The Second International Conference on Emerging Trends in Information Technology and Engineering (ic-ETITE'24). This event will take place at Vellore Institute of Technology, Vellore, India on 22nd & 23rd February 2024. The purpose of this conference is to enhance the research in Information Technology, Computer Engineering, Communication Engineering, Electronics Engineering and to afford an international platform for researchers, academicians, engineers, industrialists and students around the world to share their research findings with the global experts in the field of Science and Technology. The primary goal of the conference is to help the delegates to launch their research or business relations and to associate for future collaborations in their career path. Original, unpublished papers highlighting specific research domains from all subject perspectives are invited from delegates worldwide. In this concern, the conference aims to deliver, coordinate and publish research and resources to enrich the impact and growth of education allied.

The Theme of the Conference

ic-ETITE'24 expresses the importance of upgrading the research in Information Technology and Engineering. It motivates to provide a worldwide platform to researchers far and widespread by exploring their innovations in the field of science and technology. The mission is to promote and improve the research and development related to Information Technology and Engineering. The essential objective of the conference is to assist the researchers in discovering the global linkage for future joint efforts in their academic outlook.

ic-ETITE'24

The previous International Conference on "Emerging Trends in Information Technology and Engineering" was organized on 24th & 25th February 2020.

ic-ETITE'20 was technically Co-sponsored by IEEE Computer Society Madras Chapter, IEEE Communications Society Madras Chapter and supported by ACM-Madras Chapter.

All the presented papers were published in the IEEE Xplore by the IEEE.
(https://ieeexplore.ieee.org/xpl/conhome/9070069/proceeding)
Electronic ISBN:978-1-7281-4142-8
USB ISBN:978-1-7281-4141-1

ic-ETITE'20 had 21 technical and 17 keynote sessions.

A hackathon titled, "Breakthrough on Locked Technology" BOLT was conducted with 500+ participants and gave
Rs. 1,00, 000 as prize money.

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About VIT

Vellore Institute of Technology was established under Section 3 of the University Grants Commission (UGC) Act, 1956, and was founded in 1984 as Vellore Engineering College. The Union Ministry of Human Resources Development conferred University status on Vellore Engineering College in 2001. The University is headed by its founder and Chancellor, Dr. G. Viswanathan, a former Parliamentarian and Minister in the Tamil Nadu Government. It persistently seeks and adopts innovative methods to improve the quality of higher education consistently. The campus has a cosmopolitan atmosphere with students from all corners of the globe. Experienced and learned teachers are strongly encouraged to nurture the students. The diversity of faculty members and their unrelenting focus on teaching and research enable to realise the high requirements of modern day teaching-learning process. The Institution has established and sustains a strong support structure for the successful progression of students during their student days. The institution is committed towards its vision of "Transforming life through excellence in education and research" and adopts professional approach in governance.

Ranking & Accreditation

VIT has emerged as one of the best institutes in India and is aspiring to become a global leader. Quality in teaching-learning, research and innovation make VIT unique.

- Ranked among the top 601-700 Universities of the world and one among the top 3 Institutions in India (Shanghai ARWU Ranking 2022)
- The 9th best University, the 10th best research institution and the 12th best engineering institution in India (NIRF Ranking, Govt. of India 2022)
- The engineering and Technology subject areas of VIT are the 346th best in the World and the 9th best in India as per QS World University Rankings by Subject 2022
- NAAC Accreditation with A++ grade in the 4th cycle
- Ranked within the top 200 Universities in Asia (QS - Asia University Rankings 2022)

iC-ETITE'24

About the School of Information Technology and Engineering

The School of Information Technology and Engineering (SITE) offers B.Tech(IT), M.Tech (Software Engineering), MCA, BCA, B.Sc (Computer Science), M.Tech(by Research) and Ph.D programs in the domain of Information Technology and Engineering. Its focus is on holistic learning to help students to make significant contributions to the Information Technology industry and to serve society at large. The school has more than 4,800 students and 180 committed faculty members, apart from many visiting professors, working professionals from the industry and R&D organizations. The School has State-of-the-art infrastructure for teaching-learning, research and consultancy. The School has strong linkages with leading IT companies and research organizations. It has many industry-supported Laboratories and Centres of Excellence. It has a track record in organizing many Seminars, Workshops, Symposia and Conferences in the emerging and technical areas of Information Technology and Engineering.

iC-ETITE'24

- All submitted papers will go through a plagiarism check process.
- The manuscript should not contain embedded links, scanned images, header and footer.
- Email submission will not be accepted.
- All queries related to the conference shall be addressed to icetiteconference@gmail.com.
- Original Contributions are solicited on topics covered under broad areas not restricted to.

Important Dates:

Full Paper Submission : 05 November 2023
Notification of Acceptance : 04 December 2023
Camera Ready Paper Submission with Registration : 06 January 2024
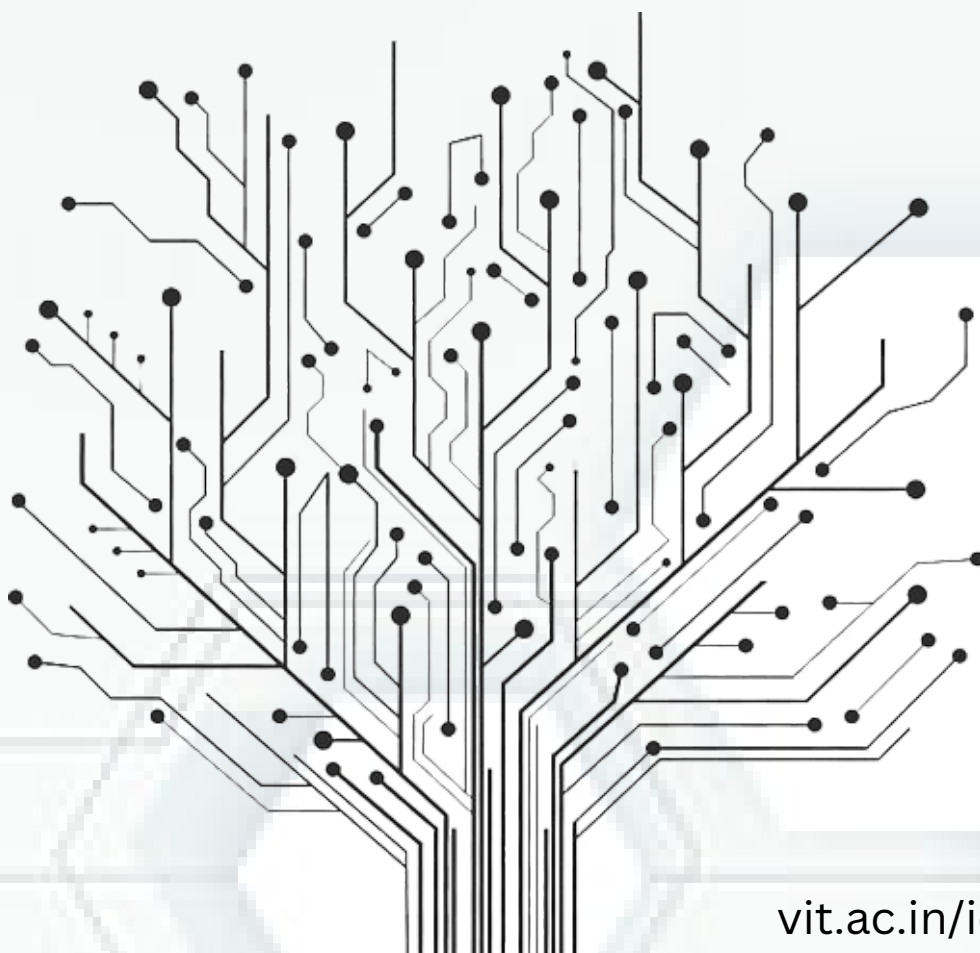Conference Dates : 22 & 23 February 2024

Registration Details

Category	Indian Authors & Delegates	Foreign Authors & Delegates
Industry Professionals	Rs. 10000	300$
Faculty / Academicians	Rs. 8000	250$
Faculty / Academicians (IEEE Members)	Rs. 7000	225$
Research Scholars, UG and PG Students	Rs. 6000	200$
Research Scholars, UG and PG Students (IEEE Members)	Rs. 5000	175$
VIT (internal)	Rs. 4000	-
Co-Authors / Participants (non-authors)	Rs. 3000	100$

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vit.ac.in/icetite

Information Technology

- Information Systems
- Information Retrieval
- Cloud Computing
- Cloud Security
- Knowledge Management
- Molecular Information Systems
- Data Management & Visualization
- Cyber-Physical Systems
- Digital Society
- Big Data Analytics
- Data Mining and Sentiment Analysis
- Social Network Analytics
- Data Sources and Integration
- Distributed, Pervasive, and Embedded Systems Security
- Deep Learning
- E-learning

- Multimedia and Internet Security
- Image And Video Retrieval
- Web-based Application and Service Security
- Cyber Security
- Network security and Information Security
- Digital Forensics
- Biometric Security
- Quantum Cryptography
- Malware Analysis and detection
- Secure IoT
- Web Ontology
- Web Intelligence
- Search Engines
- Innovative Information Security

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Communication Engineering

- Wireless & Sensor Systems
- Social Networking
- Communication Signal processing
- Wireless Communications
- Mobile Communication
- Communication Theory and Techniques
- Ad-Hoc Networks
- Innovative Networking and Communication Techniques
- Data Communication
- TV And Sound Broadcasting
- Communication IC Design
- Communication Protocols
- Optical Communications
- Telecommunication Networks
- Radio And Satellite Communications
- Radar, Sonar and Navigation Systems
- Space Technologies
- Zigbee Technology

- Human Area Network
- Next Generation Protocols
- Software Defined Network
- GPRS
- HSPA
- Quantum Communication
- Fibre Optic Communication
- Modelling and Simulation
- Sensor & Micro-Machines
- Wireless Video
- Intelligent Control Systems
- Internetworking of WLAN & Cellular Networks
- 4G & 5G
- Antenna & EMC/EMII
- Aerospace Applications
- Speech Processing

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Computer Engineering

- Computer Architecture
- Computing for Development
- Cognitive Analysis
- Fuzzy Systems
- Granular Computing
- Modern Operating Systems
- Rough Sets
- Storage Techniques
- User Interfaces
- Next Generation Computing Technologies
- Virtualization
- Pervasive Computing
- Quantum Computing
- Human Computer Interaction & Accessible Technology
- Machine Learning
- Computation Intelligence
- Natural Language Processing
- Programming Languages & Software Engineering
- Systems & Networking
- Theory of Computation
- Ubiquitous Computing
- Artificial Intelligence
- Augmented & Virtual Reality
- Computer Graphics, Vision, Animation, & Game Science

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- Fabrication
- Robotics
- Digital Image/Signal Processing
- VLSI/Embedded systems
- Microprocessor Based Technologies
- Applied Electromagnetics and RF Circuits
- Control Systems
- MEMS and Microsystems
- Optics and Photonics
- Plasma Science and Engineering
- Power and Energy
- Quantum Science and Technology
- Solid-State Devices and Nanotechnology
- Antenna and Wave Propagation
- Intelligent Systems Architectures And Applications
- Microelectronics
- Nano-electronics
- Power Electronics

- Signal and System Theory
- Solar Technology
- Network Theory
- Modulation Techniques
- Source and Channel Coding
- Switching Theory and Techniques
- Microwave Theory and Techniques
- Wave Propagation
- Measurement and Instrumentation
- Circuit Design
- Simulation and Cad
- Microwaves, Antennas and Radio Propagation
- Optoelectronics
- Electromagnetic Compatibility
- Digital Governance

Extended version of selected papers will be recommended for publication in Scopus indexed journals with impact factor

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BOLT 2.0

BOLT Hackathon

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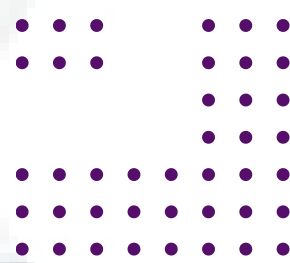
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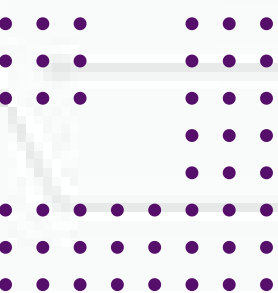
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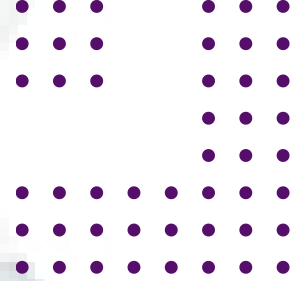
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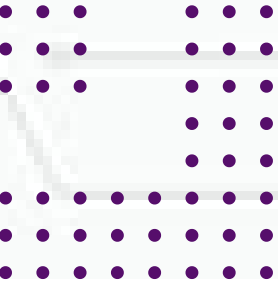**Dr. Surya Prakash**, Department of Computer Science and Engineering, Indian Institute of Technology, Indore.

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