# COL331 Assignment 3 (EASY) Report

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## 1 Buffer Overflow Attack in XV6

We used pwndbg to find the location of foo for memory addresses.

Figure 1: Location of foo

Thus foo is stored at address 0x0

```
pwndbg> p $ebp
$4 = (void *) 0x2f38
pwndbg> p &buffer
$5 = (char (*)[4]) 0x2f2c
pwndbg> p/d 0x2f38-0x2f2c
$6 = 12
pwndbg>
```

Figure 2: Content of eb

We can see that the \$ebp has the location 0x2f38 stored in it which points to the location of the stack frame. And the location of the buffer (starting) is 0x2f2c, if we take the difference we get an offset of 12, thus we

know that the return address is stored at location 16 away(offset).

If we have to generalize for the arbitrary length of the buffer, we know that each character is stored at 1 address location this means that the offset is buffer size + x which we found to be 16. In this case, the buffer size was 4. Therefore x = 12.

Hence to find the location of the return address in the stack all we have to do is buffer size + 12.

```
import sys

buffersize = int(sys.argv[1])
content = "x" * (buffersize+12) + "\x00\x00\x00\x00"

with open('payload', 'w') as f:
    f.write(content)
f.close()
```

```
$ buffer_overflow
SECRET_STRING:COLOS
pid 3 buffer_overflow: trap 14 err 4 on cpu 0 eip 0x2fce addr 0x53e58955--kill c
$
```

Figure 3: Before enabling ASLR, buffer overflow attack

# 2 Address Space Layout Randomization

If the Address space for the base address of the heap, stack, etc remains the same every time then it is easy to exploit the system by first calculating/finding the return of the important function (which may be inaccessible if we try to execute it)

Therefore changing the base addresses of the heap, stack, etc helps to prevent buffer overflow attacks because the addresses of the functions will change every time the program executes.

#### 2.1 Generating Random integer

We generate a random number between between lower and upper which uses ticks, which are random at each instant. Remainder of ticks \* ticks - ticks is computed with range and added to lower to bring randomness.

The below function is created in exec.c, where it is used to generate offset.

```
int random(int lower, int upper){
  int num = (((ticks*ticks)-ticks) % (upper - lower + 1)) + lower;
  return num;
}
```

#### 2.2 Checking aslr\_flag

aslr\_flag file is read and aslr\_flag variable is set to 1 incase the file contains 1.

```
int aslr_flag = 0;
char c[2] = {0};

if ((ip = namei("aslr_flag")) == 0) {
   cprintf("unable to open aslr_flag file, default to no randomize\n");
} else {
```

```
ilock(ip);
if (readi(ip, (char*)&c, 0, sizeof(char)) != sizeof(char)) {
    cprintf("unable to read aslr_flag flag, default to no randomize\n");
} else {
    aslr_flag = (c[0] == '1')? 1 : 0;
}
iunlockput(ip);
}
```

#### 2.3 Calculating load offset

If aslr\_flag is 1, offset, a random number is computed as follows using the random function (random number generator):

```
int offset = (aslr_flag)? random(0, 1000) * 16 + 1 : 0;
```

#### 2.4 Changes in exec.c incorporating offset

This offset is used while calling the allocuvm and loaduvm functions in exec.c to modify the loading location of program segment in page table and allocating page tables and physical memory.

```
sz = allocuvm(pgdir,0,offset);
    for(i=0, off=elf.phoff; i<elf.phnum; i++, off+=sizeof(ph)){</pre>
       if(readi(ip, (char*)&ph, off, sizeof(ph)) != sizeof(ph)){
         goto bad;
6
       if (ph.type != ELF_PROG_LOAD)
         continue;
11
       if(ph.memsz < ph.filesz){</pre>
12
         goto bad;
13
14
       if (ph.vaddr + ph.memsz < ph.vaddr){</pre>
15
16
         goto bad;
17
18
19
       if((sz = allocuvm(pgdir, sz, ph.vaddr + ph.memsz+offset)) == 0){
20
21
         goto bad;
22
23
       // if(ph.vaddr % PGSIZE != 0)
24
      // goto bad;
25
26
       if(loaduvm(pgdir, (int)ph.vaddr+offset, ip, ph.off, ph.filesz) < 0){</pre>
         goto bad;
27
```

#### 2.5 Changes in loadurm function in vm.c

Finally, we also modify vm.c file's loaduvm function computed page offset from beginning of page, then manually filled the first page which might not be page aligned, filled remainder of the pages and then used for loop for subsequent pages.

```
int
loaduvm(pde_t *pgdir, int va, struct inode *ip, uint offset, uint sz)
{
```

```
uint i, n;
         int pa;
         pte_t *pte;
6
         // get the positive page offset of the va from the beginning of the page
         int first_va = PGROUNDDOWN(va);
         int pg_offset = va - first_va;
9
10
         // manually fill the first page, which might not be page aligned
11
         pte = walkpgdir(pgdir, (char*)first_va,0);
         pa = PTE_ADDR(*pte);
13
         if (pa == 0)
14
           panic("loaduvm: address should exist");
15
16
         // fill the remainder of the page or until there are no bytes left to write
17
        n = (sz < PGSIZE - pg_offset)? sz : PGSIZE - pg_offset;</pre>
18
         if(readi(ip, P2V(pa) + pg_offset, offset, n) != n)
19
20
           return -1:
         offset += n;
21
         sz -= n;
22
23
24
         // use for loop for subsequent pages
         for(i = PGSIZE; sz > 0; i += PGSIZE){
25
           pte = walkpgdir(pgdir, (char*)first_va + i,0);
26
          pa = PTE_ADDR(*pte);
27
          if(pa == 0)
28
             panic("loaduvm: address should exist");
29
           // fill the page or until there are no bytes left to write
30
          n = (sz < PGSIZE)? sz : PGSIZE;</pre>
31
          if(readi(ip, P2V(pa), offset, n) != n)
32
             return -1;
33
           offset += n;
34
35
          sz -= n;
36
37
        return 0;
38
```

```
Booting from Hard Disk..xv6...

cpu0: starting 0

sb: size 1000 nblocks 941 ninodes 200 nlog 30 logstart 2 inodestart 32 bmap start 58 init: starting sh

$ buffer_overflow

pid 3 buffer_overflow: trap 14 err 4 on cpu 0 eip 0x610 addr 0x78787801--kill proc

$ $ $ $
```

Figure 4: After enabling ASLR

#### 2.6 Challenges faced

Since there were very few resources, figuring out what needs to be done exactly even after thoroughly understanding what ASLR means was really difficult and time taking.

Intricacies of xv6 and understanding how it implements everything relating to stacks and virtual memory was most cumbersome as there are a huge number of files and studying each was not possible in a very short time.

We got panic most of the time and trying to figure out where the error is was another major challenge.

## 2.7 Resolutions

We tried to follow the github repository of the risc implementation of xv6 ASLR provided on piazza step by step. Debugging was primarily done through print statements in every two lines of the code of exec.c and vm.c which are the two files where major changes were made.

The following link was our primary resource for ASLR implementation:

https://github.com/TypingKoala/xv6-riscv-aslr