



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fifth Semester January 2014
Electrical & Electronics Engineering

J
13 690383

Name of the Student : RAJATH.T.G

Father's / Mother's Name : GUBBIYAPPA.T.M

Seat No. : 10X11EE063

Name of the College : OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10AL51	Management & Entrepreneurship	100	35	77	25	-	23	125	50	100	Pass
2	10EE52	Signals & Systems	100	35	58	25	-	25	125	50	81	Pass
3	10EE53	Transmission & Distribution	100	35	77	25	-	24	125	50	101	Pass
4	10EE54	D. C. Machines & Synchronous Machines	100	35	35	25	-	22	125	50	57	Pass
5	10EE55	Modern Control Theory	100	35	65	25	-	19	125	50	84	Pass
6	10EE56	Linear IC's & Applications	100	35	69	25	-	24	125	50	93	Pass
7	10EEL57	Measurements & Circuit Simulation Lab.	50	20	48	25	12	24	75	32	72	Pass
8	10EEL58	Transformers & Induction Machines Lab.	50	20	45	25	12	24	75	32	69	Pass
GRAND TOTAL									900	364	857	

Total Marks Obtained (In words) : SIX HUNDRED AND FIFTY SEVEN ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE : MAR 23, 2014

A - Absent NE - Not Eligible



Registrar (Evaluation)