



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Seventh Semester January 2015

Electrical & Electronics Engineering

J
15 021108

Name of the Student : RAJATH.T.G

Father's / Mother's Name : GUBBIYAPPA.T.M

Seat No. : 10X11EE063

Name of the College : OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EE71	Computer Techniques in Power System Analysis	100	35	45	25	-	25	125	50	70	Pass
2	10EE72	Electrical Power Utilization	100	35	82	25	-	25	125	50	107	Pass
3	10EE73	High Voltage Engineering	100	35	67	25	-	24	125	50	91	Pass
4	10EE74	Industrial Drives & Applications	100	35	53	25	-	25	125	50	78	Pass
5	10EE756	Testing & Commissioning of Electrical Equip.	100	35	76	25	-	18	125	50	94	Pass
6	10EE764	VLSI Circuits & Design	100	35	69	25	-	23	125	50	92	Pass
7	10EEL77	Relay & High Voltage Lab.	50	20	47	25	12	25	75	32	72	Pass
8	10EEL78	Power System Simulation Lab.	50	20	48	25	12	25	75	32	73	Pass
GRAND TOTAL									900	364	677	

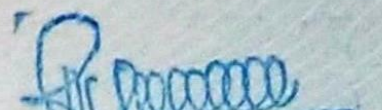
Total Marks Obtained (in words) : SIX HUNDRED AND SEVENTY SEVEN ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE: APR 11, 2015

A - Absent NE - Not Eligible




Registrar (Evaluation)