

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

13 483560

STATEMENT OF MARKS

BE Fourth Semester July 2013 Electrical & Electronics Engineering

Name of the Student

RAJATH T.G.

Father's / Mother's Name :

GUBBIYAPPA.T.M

Seat No.:

10X11EE063

Name of the College

OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

SI. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	Result
1	10MAT41	Engineering Mathematics - IV	100	35	84	25	-	24	125	50	108	Pass
2	10ES42	Microcontrollers	100	35	66	25	-	22	125	50	88	Pass
3	10ES43	Control Systems	100	35	66	25	-	24	125	50	90	Pass
4	10EE44	Field Theory	100	35	67	25	-	21	125	50	88	Pass
5	10EE45	Power Electronics	100	35	53	25	-	15	125	50	68	Pass
6	10EE46	Transformers and Induction Machines	100	35	35	25	-	16	125	50	51	Pass
7	10ESL47	Microcontrollers Lab	50	20	47	25	12	23	75	32	70	Pass
8	10EEL48	Power Electronics Lab	50	20	46	25	12	23	75	32	69	Pass
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GRAND TOTAL									900	364	632	

Total Marks Obtained (in words) : SIX HUNDRED AND THIRTY TWO ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE: OCT 05.2013





Registrar (Evaluation)

A - Absent NE - Not Eligible