



ವಿಶ್ವವರಯ, ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Sixth Semester July 2014  
Electrical & Electronics Engineering

J 424184  
14

Name of the Student : RAJATH.T.G

Father's / Mother's Name : GUBBIYAPPA.T.M

Seat No. : 10X11EE063

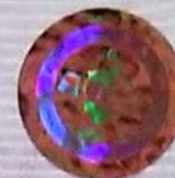
Name of the College : OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10EE61	Power System Analysis & Stability	100	35	82	25	-	24	125	50	106	Pass
2	10EE62	Switchgear & Protection	100	35	72	25	-	24	125	50	96	Pass
3	10EE63	Electrical Machine Design	100	35	62	25	-	22	125	50	84	Pass
4	10EE64	Digital Signal Processing	100	35	51	25	-	24	125	50	75	Pass
5	10EE65	Computer Aided Electrical Drawing	100	35	92	25	-	25	125	50	117	Pass
6	10EE661	Operations Research	100	35	80	25	-	22	125	50	102	Pass
7	10EEL67	D.C. Machines & Synchronous Machines Lab.	50	20	38	25	12	25	75	32	63	Pass
8	10EEL68	Control Systems Laboratory	50	20	47	25	12	25	75	32	72	Pass
GRAND TOTAL									900	364	715	

Total Marks Obtained (In words) : SEVEN HUNDRED AND FIFTEEN ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE : SEP 17, 2014



*[Signature]*  
Registrar (Evaluation)

A - Absent NE - Not Eligible