



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Third Semester January 2013
Electrical & Electronics Engineering

J 658609
12

Name of the Student : RAJATH.T.G

Father's / Mother's Name : GUBBIYAPPA.T.M

Seat No. : 10X11EE063

Name of the College : OXFORD COLLEGE OF ENGINEERING, J.P. NAGAR, BANGALORE

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	10MAT31	Engineering Mathematics-III	100	35	74	25	-	25	125	50	99	Pass
2	10ES32	Analog Electronic Circuits	100	35	41	25	-	22	125	50	63	Pass
3	10ES33	Logic Design	100	35	52	25	-	24	125	50	76	Pass
4	10ES34	Network Analysis	100	35	37	25	-	22	125	50	59	Pass
5	10EE35	E & E Measurements & Instrumentation	100	35	46	25	-	23	125	50	69	Pass
6	10EE36	Electric Power Generation	100	35	43	25	-	25	125	50	68	Pass
7	10ESL37	Analog Electronics Lab	50	20	40	25	12	23	75	32	63	Pass
8	10ESL38	Logic Design Lab	50	20	44	25	12	24	75	32	68	Pass
GRAND TOTAL									900	364	565	

Total Marks Obtained (in words) : FIVE HUNDRED AND SIXTY FIVE ONLY

Result of the Semester : FIRST CLASS

DATE : APR 26, 2013

A - Absent NE - Not Eligible



(Signature)

Registrar (Evaluation)