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# **Department of Sciences and Humanities**



Course Name:	Elements of Electrical and Electronics Engineering Laboratory	Semester:	I
<b>Date of Performance:</b>	12/11/2024	Batch No:	<b>C5-3</b>
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<b>Faculty Sign &amp; Date:</b>		Grade/Marks:	/ 20

# **Experiment No: 5**

**Title:** Power factor improvement (series)

#### **Aim and Objective of the Experiment:**

To improve power factor of a single phase inductive AC circuit using capacitor in series with it.

#### **Requirements:**

Inductor box, 1 K $\Omega$ -3W Resistor, Capacitor box, AC Ammeter and AC Voltmeter.

#### COs to be achieved:

**CO2:** Demonstrate and analyze steady state response of single phase and three phase circuits

#### Theory:

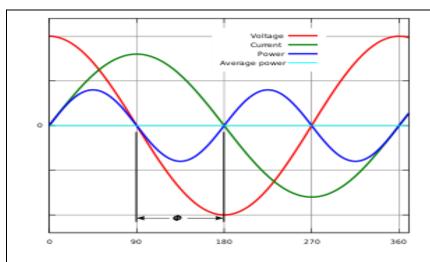
When we need to convert electrical energy to mechanical energy, electric motors are used for it. These AC motors converts electric energy in two forms namely mechanical energy in the form of rotary motion and other is magnetic field. Magnetizing currents are lagging to the supply voltage. This magnetic energy is not a mechanical energy so it is kind of wastage, but without which motor will not run and convert electric energy into mechanical energy. Such form of energy is called as reactive power. Reactive power must be as less as possible so that the load will utilize maximum power and current requirement will be less for the same amount power. As the current requirement is less, so wire thickness will be small in diameter. Installation cost and energy cost will be also reduced. To reduce reactive power of the circuit, different power factor improvement methods are used. One of the most familiar methods is the use of capacitor bank. We can use capacitor in series with the load or across the load. Following diagrams are illustrating effect of PF on active power.



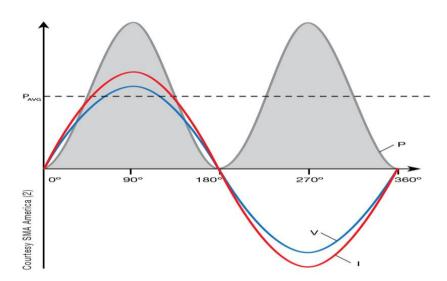
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In the above figure instantaneous and average power calculated from AC voltage and current with a zero power factor. The blue line shows all the power is stored temporarily in the load during the first quarter cycle and returned to the grid during the second quarter cycle, so no real power is consumed by the load which is shown by sky-blue colour line.



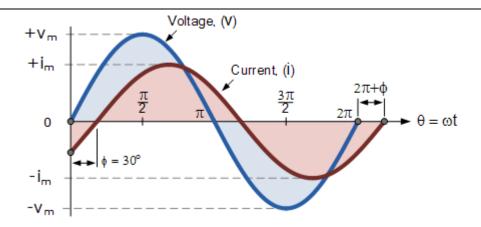
In the above figure instantaneous and average power calculated from AC voltage and current with a unity power factor. The gray part shows all the power is absorbed in the load during the first half cycle as well as the second half cycle, so real power is fully consumed.



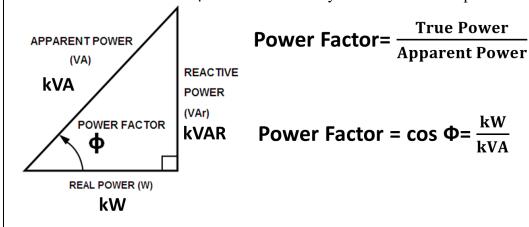
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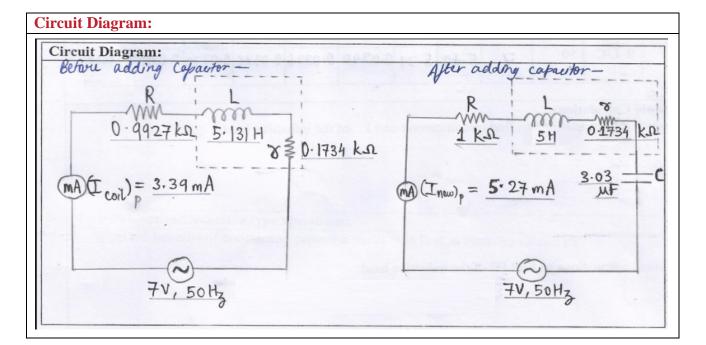
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When power factor is between zero and unity, then real power consumed by the load depends upon PF of the circuit. Greater the power factor is always better to consume power.







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#### **Stepwise-Procedure:**

- 1. Connect series R and L circuit across 230V, 1ø, 50 Hz AC supply and note down circuit voltage and current.
- **2**. Calculate practical value of circuit power factor by taking ratio of active power (P) and apparent power (S).
- **3**. Connect required value of capacitor in series with R-L load and switch on power supply to note circuit current.
- **4**. Calculate practical value of circuit power factor by taking ratio of active power (P) and apparent power (S).
- **5.** Compare theoretical and practical values of PF before connecting the capacitor and after connecting capacitor.

#### **Observation Table:**

Sr No	Type of load	Voltage (V)		Current (mA)		P (W)		S (VA)		Power factor	
		Th	Pr	Th	Pr	Th	Pr	Th	Pr	Th	Pr
	R-L	7	7	3.51	3-39	0.01436	0.13340	0.02457	0.02373	0.5846	0.5621
	R-L-C	7	7	5.40	5.29	0.0340	0.0326	0.0378	0.0370	0 -8990	0.8810



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# **Sample Calculations:** 0.9927 KD 5.131 H 7= 0.173 K2 3.39 mA ( Practical) find circuit guvent $0.173) + i(2 \pi \times 50 \times 5.131)$ Theoretical) 1989 CAS (54-12) 0.5860 Practical Calculations find inductive Power factor: S=VT ( Practical) =7 x 3.39 x 10-3 ( Practical) (R+V) 3-39 x 10-3) x 1166 0-01334 (Practical) = 0.01334 =0-02373 Down factor (cos d) = 0-5621 (Practical)



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Consideration of the second of
(2) Permitador: S = VI
(Theorifical) = $7 \times 3.51 \times 10^{-3} \text{ VA}$
(The pritical) = $7 \times 3.51 \times 10^{-3} \text{ VA}$ $\Rightarrow S = 0.02457 \text{ VA}$ (Theoritical)
a. 1 set 0 = 8
$\rho = I^2(R+\delta)$
$=(3.51 \times 10^{-3}) \times 1166$
$P = I^{2}(R+\delta)$ = $(3.51 \times 10^{-3})^{2} \times 1166$ $\Rightarrow P = 0.01436 \text{ W (Theorifical)}$
A TABLE OF TAXABLE AND
QS = P = 0.5846  (Theorifical)
(II) Calculations to find value of capacitor to be connected with load
WIN waa
7 = (l + x) = 1166 = 1905.5
$Z_n = \frac{(R+8)}{CW \phi_n} = \frac{1166}{0.9} = \frac{1295.5}{0.9}$
$X - X_{\Gamma} = Z_{\Gamma}$
$\frac{1}{2} \frac{1}{2} \frac{1}$
-: X = 2nfL (4)+2) M = MANTE MANTE (6)
= 27 x 50 x 5.131
$X_{L} = 1612\Omega$
MORPH THE WARRENCE STATE OF WHAT ON TO SHEET (TILL)
$\frac{1}{1612 - X_c} = 1295.5$
Sin (25.84)
$-1. X_{c} = -[1295.5 \times sin(25.84)] + 1612$
Xc = 1048-D
2. C = 1 = 3.03.1F
$2\pi f X_{c}$ $= \frac{3.03\mu F}{2\pi \times 50 \times 1048}$
2113 1 C 211 N 50 X 1078



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	,
$\rightarrow I_{N} = V = 7 = 5.40 \text{ mA}$ (7) $E_{N}$ 1295	heoritical)
-> IN = 5.29 mA ( Practical)	
a) $S = V \cdot I$ .	
$= 7 \times 5.40 \times 10^{-3} \text{ VA}$	
⇒ S = 0.0378 VA (Theoritical)	
b) S = V.I.	
= 7 x 5-29 x 10-3 VA	
⇒ S = 0.0370 VA (Praetical)	
a) P= I2(R+8)	
a) $P = I^{2}(R+8)$ = $(5.29 \times 10^{-3})^{2} \times 1166$	
P= 0.0326 W (Theoretical)	
b) $P = I^2(R+8)$	
$= (5.40 \times 10^{-3})^2 \times 1166$	
P= 0.0340 W (Practical)	
a) Pomer factor = P = 0.0340 = 0.899	(Theoritical)
\$ 0.0378	



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#### Post Lab Subjective/Objective type Questions:

1. What are benefits of connecting capacitor across the load to improve circuit PF?

#### Ans:

- **a) Reduced Power Losses:** By improving the power factor, the reactive power in the circuit is reduced, which minimizes energy losses in the transmission lines.
- **b)** Lower Apparent Power: With the help of improved power factor, the apparent power drawn by the load, which allows for efficient utilization of the electrical supply system.
- **c**) **Improved Voltage Regulation:** Adding a capacitor reduces the voltage drop across the supply lines, this maintains stable voltage levels for the load.
- **d) Increased System Capacity**: With reduced reactive power, we can utilize the capacity of the existing system more effectively so that we can power additional loads.
- **e**) **Reduced Electricity Costs:** Utilities often charge penalties for poor power factors. Improving PF leads to significant cost savings with reduced energy consumption.
- **f)** Enhanced Equipment Lifespan: Reducing stress on generators, transformers, and other components decrease the likelihood of the equipment's overheating; this extends equipment life.

#### **Conclusion:**

The above experiment successfully demonstrates the significance of power factor improvement in an electrical circuit. Initially, the circuit exhibited a low power factor, this resulted in higher apparent power, increased current, and inefficient energy utilization. By using a capacitor in series to the load, the reactive power was significantly reduced, leading to a significant improvement in the power factor. This enhancement in power factor directly contributed to reduced apparent power, lower current flow, and minimized power losses in the circuit. Also, the addition of the capacitor stabilized the voltage levels, which helped in reducing the burden on the power supply and optimizing the overall efficiency of the system. Practically, this experiment emphasizes the importance of capacitors in industrial and household applications, ensuring energy savings, cost efficiency, and extended equipment lifespan. It displays the critical role of power factor correction in efficient energy management for household and industrial applications.

**Signature of faculty in-charge with Date:**