Document Revision History

Date	Version	Article
11/18/2021	0.1	Initial version.

Contents

1. The OZ711LV2 PCIe registers, 0x234, 0x248, and 0x24C only support DWORD access.

<Scope>

This Application Note illustrates how to access the three registers:
PCR 0x234 (LTR MAX Snoop Latency Register and MAX No-Snoop Latency Register),
PCR 0x248 (L1 PM Substates Control 1 Register), and
PCR 0x24C (L1 PM Substate Control 2 Register).

<Description>

PCR 0x234, PCR 0x248 and PCR 0x24C can only be DWORD Config Write.

If Software needs to BYTE or WORD Config Write them, it needs to follow the below steps to BYTE or WORD write:

Step1: DWORD Config Read the register to get its current value and store the value into a variable.
Step2: Change the variable to expected value.
Step3: DWORD Config Write the changed variable value to the register.

Example:

Step1: DWORD var = DWORD Config Read PCR 0x234[31:0];
Step2: var = ((var & 0xFFFF_0000) | 0x0000_55AA); //change var[15:0] = 0x55AA
Step3: DWORD Config Write “var” value to PCR 0x234;

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