USN | | | | | | | | | | 10CS46

Fourth Semester B.E. Degree Examination, December 2012
Computer Organization

Time: 3 hrs. Max. Marks:100

Note: *Answer FIVE full questions, selecting atleast TWO questions from each part.*

PART – A

1 a. Explain the different functional units of a digital computer. (05 Marks)

b. Draw and explain the connection between memory and processor with the respective registers. (05 Marks)

c. Explain clearly SPEC rating and its significance. Assuming that the reference computer is ultra SPARC10 work station with 300 MHz ultra SPARC processor. A company has to purchase 1000 new computers hence ordered testing of new computer with SPEC 2000. Following observation were made.

Programs	Runtime on reference computer	Runtime in new computer
1	50 minutes	5 Minutes
2	75 Minutes	4 Minutes
3	60 Minutes	6 Minutes
4	30 Minutes	3 Minutes

The company system manger will place the order for purchasing new computers only if the overall SPEC rating is atleast 12. After the said test will the system manger place order for purchase of new computer. (10 Marks)

2 a. What is little endian and big endian memory? Represent the number 64243848H in 32 bits big endian and little endian memory. (06 Marks)

b. What is addressing mode? Explain immediate, direct and indirect addressing mode by an example. (06 Marks)

c. Explain logical shift and rotate instructions, with examples. (08 Marks)

3 a. Define memory mapped I/O and IO mapped I/O, with examples. (05 Marks)

b. Explain how interrupt requests from several IO devices can be communicated to a processor through a single INTR line. (10 Marks)

c. What are the different methods of DMA? Explain them in brief. (05 Marks)

4 a. With a block diagram, explain how the keyboard is connected to processor. (06 Marks)

b. Explain the serial port and serial interface. (06 Marks)

c. Explain architecture and protocols, with respect to USB. (08 Marks)

PART – B

5 a. Draw a diagram and explain the working of 16 Mega bits DRAM chip configured as 2M × 8. Also explain as at how it can be made to work in fast page mode. (10 Marks)

b. Briefly explain any four non-voltile memory concepts. (05 Marks)

c. With figure analyse the memory hierarchy interms of speed cost and size. (05 Marks)

6 a. Explain the design of a four bits carry – look ahead adder circuit. (10 Marks)
b. Gives Booth's algorithm to multiply two binary numbers. Explain the working of algorithm by taking an example. (10 Marks)

7 a. Write and explain the control sequence for execution of an unconditional branch instruction. (10 Marks)
b. Draw and explain multiple bus organization. Explain its advantages. (10 Marks)

8 a. Write short note on power wall (06 Marks)
b. What you mean by shared memory multiprocessors. (06 Marks)
c. Explain the different approaches used in multithreading. (08 Marks)

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