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☐ B Read counter 0

☐ C No operation

Question 6

Control words and status information from the microprocessor to PIC and from PIC to microprocessor respectively, are transferred through the

☐ A data bus buffer

☐ B CAS0-CAS2

☐ C IR0-IR7

Question 7

With respect to 8255 Programmable Peripheral Interface (PPI), which of the following statement(s) are correct?

1. Mode 0: Any port can be used as input/output ports.

2. Mode 1: Both Port A and B can be used as input/output ports.

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☐ B CAS0-CAS2

☐ C IR0-IR7

Question 7

With respect to 8255 Programmable Peripheral Interface (PPI), which of the following statement(s) are correct?

1. Mode 0: Any port can be used as input/output ports.
2. Mode 1: Both Port A and B can be used as input/output ports.
3. Mode 2: Port B can be used either in Mode 0 or Mode 1.

☐ A Only 1 is correct.

☐ B Only 2 is correct.

☐ C Only 3 is correct.


☒ D All 1, 2 and 3 are correct.

Question 8

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C

Only 3 is correct.

D

All 1, 2 and 3 are correct.

Question 8

In 8255, under the I/O mode of operation we have __ modes. Under which mode will have the following features
i) A 5 bit control port is available.
ii) Three I/O lines are available at Port C.

A

3, Mode2

B

2, Mode 2

C

4, Mode 3

D

3, Mode 2

Question 9

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(A) 3, Mode2

(B) 2, Mode 2

(C) 4, Mode 3

(D) 3, Mode 2

Question 9

If you want to read TM0

(A) $A1=0, A0=0, RD'=0$

(B) $A1=1, A0=0, RD'=1$

(C) $A1=0, A0=1, RD'=0$

(D) $A1=1, A0=1, RD'=0$

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A $A1=0, A0=0, RD'=0$

B $A1=1, A0=0, RD'=1$

C $A1=0, A0=1, RD'=0$

D $A1=1, A0=1, RD'=0$

Which of these register's contents are used for auto-initialization (internally)?

- ☒ C Base Address Register

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