

Do the Assignment in Proper way submit it by 19 Apr 2015 and for each day delay of assignment will deducted 2 marks from your account.

Show to me; These 3 4 & 5 assignment will decide your 20 marks of internal (ADE) & 20 marks of internal Electronics lab.

ASSIGNMENT 3

Question 1:— Draw the block diagram of the Bidirectional Shift register and explain its working.

Question 2:— Draw the block diagram of the Universal Shift register and explain its working.

Question 3:— Explain the working of Johnson Counter (Twisted ring counter)

Question: Draw the circuit diagram of Schmitt trigger of both inverting and Non inverting type and explain the working and also explain the impact of Hysteresis.

Assignment 4

Note Draw all the required waveform in each question.

Question 1:— Draw the internal architecture and Pin diagram of 555 timer IC. How it work in Astable and Monostable mode and derive an expression for the frequency.

Question 2:— List the Various application of 555 timer in details for both the mode and derive an expression for T_{on} in both Astable and Monostable mode.

Questions 3:— Classify the Voltage regulators. Draw the diagram of each of them and explain the working.

Questions 4:— Draw the OPM Based configuration of Voltage regulator.

Question 5:— Draw the circuit diagram of Astable (Square wave generator) multivibrator and Monostable (Pulse generator) multivibrator using Operational amplifier.

Assignment 5

Question 1:— Solve all the numerical problem asked in sessional papers.

Question 2: Classify the A to D converter. And explain the working of each of them.

Question 3:— Classify the D to A converters. And the working of each of them.

Question 4:— Classify the memory. Compare RAM and Rom. And explain concept behind memory organization for both RAM and ROM (4 X4 Diode array, transistor array).

Question 5 :— Compare SRAM and DRAM. Draw and explain the architecture of SRAM And DRAM cell.