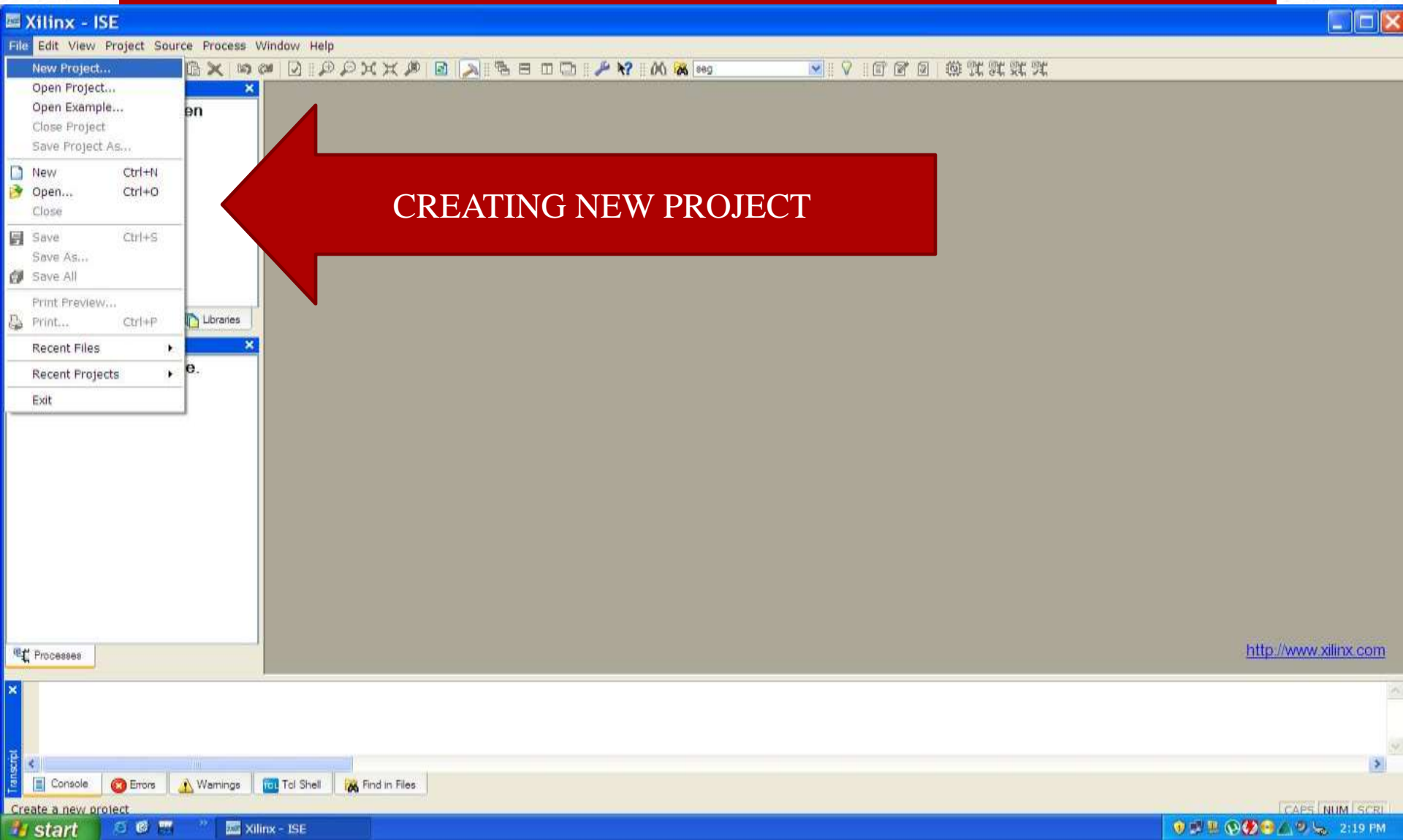


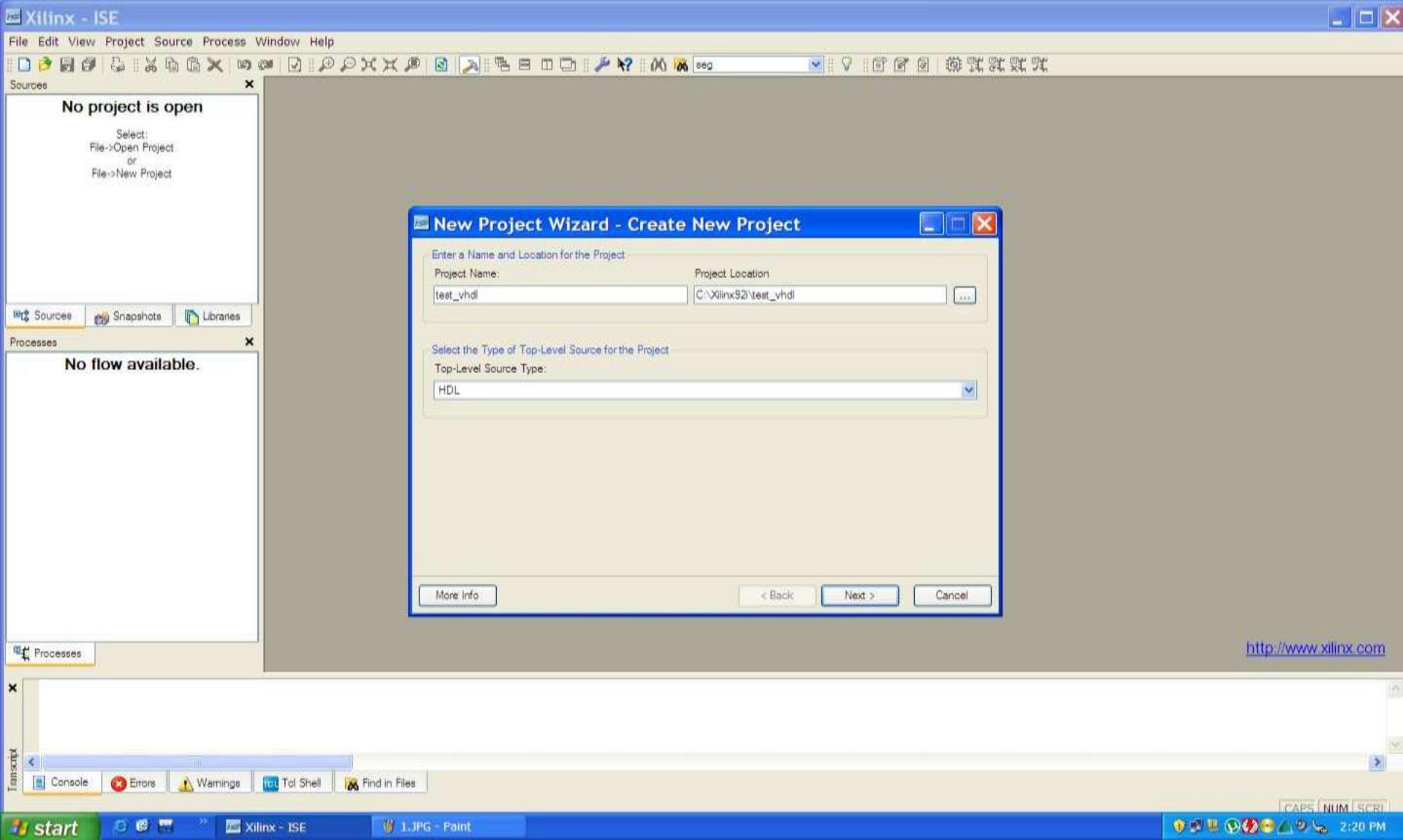
DHD LAB CSP 745

TA's: ROHIT NARULA (2011JVL2064)

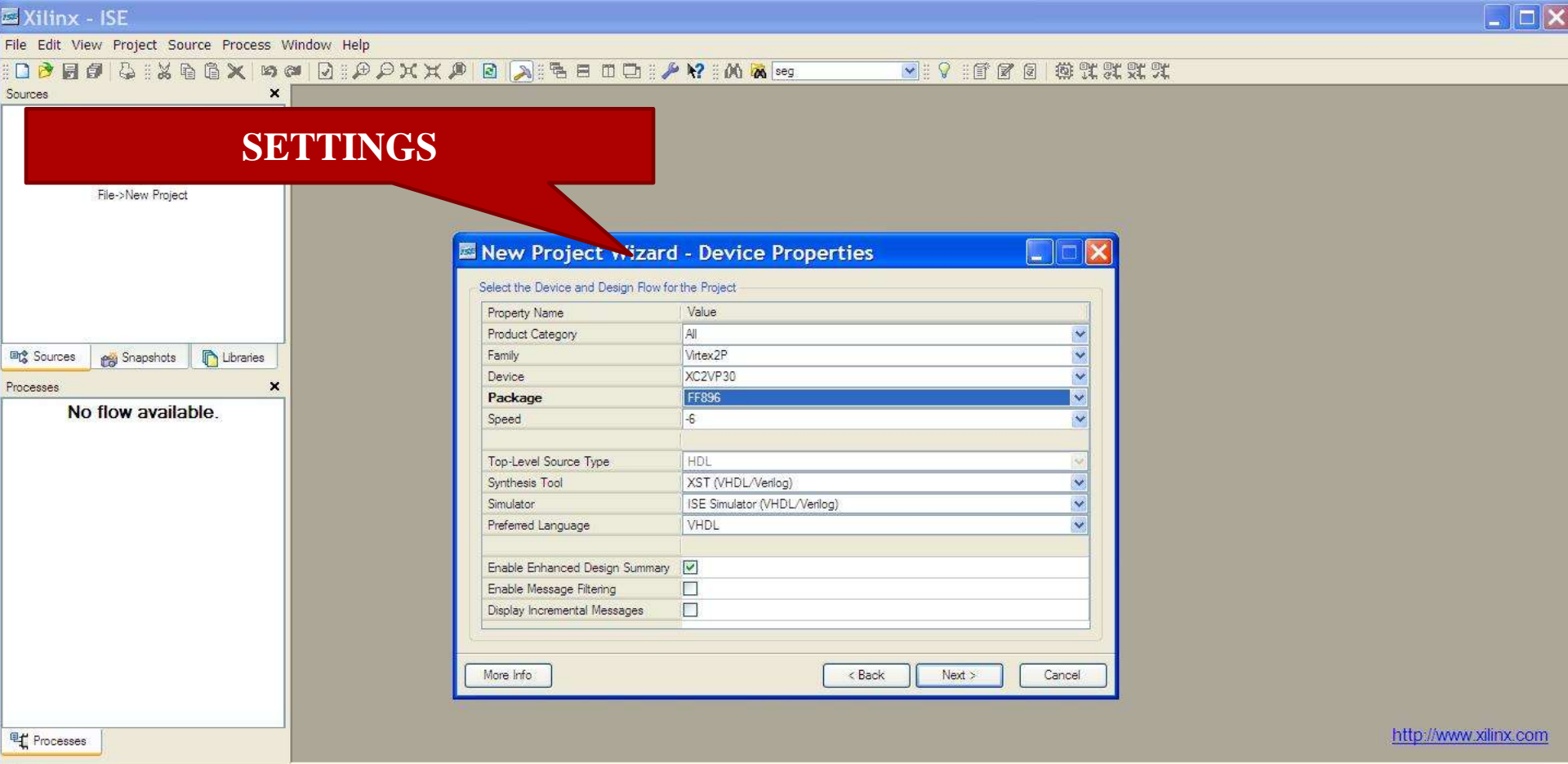
MOHAMMAD ZAID (2011JVL2066)

Basic Steps:

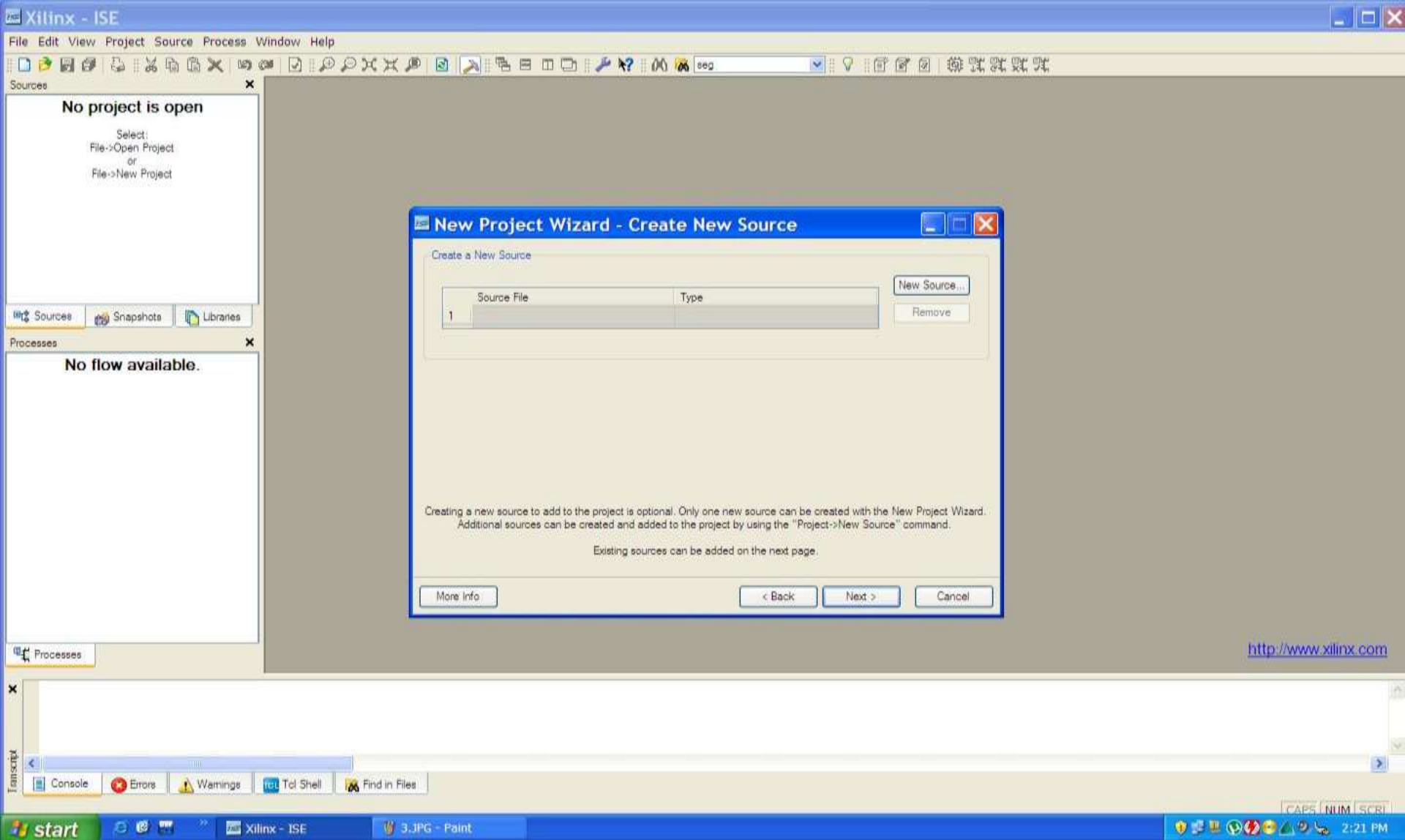




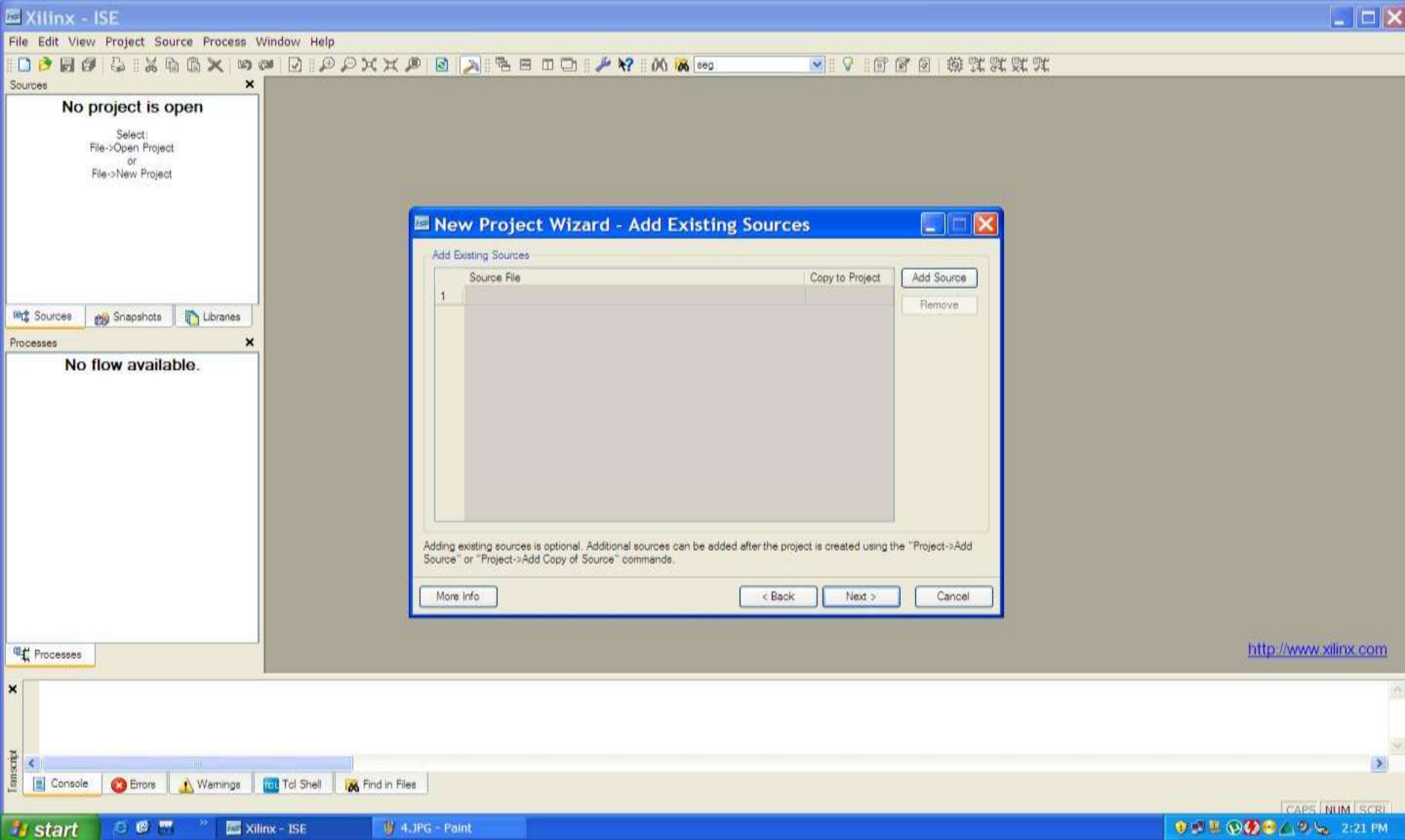
STEP:2



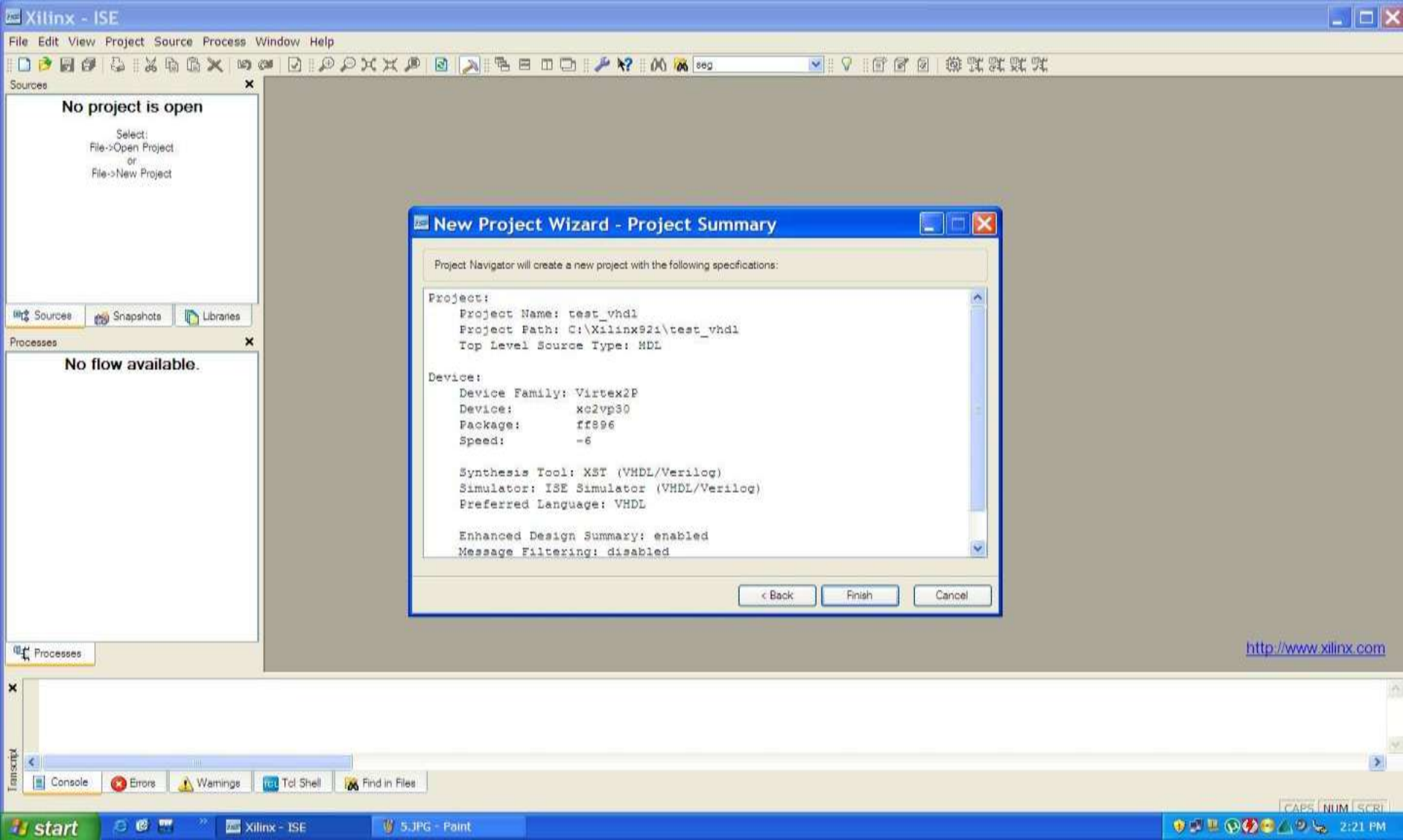
STEP:3



STEP:4

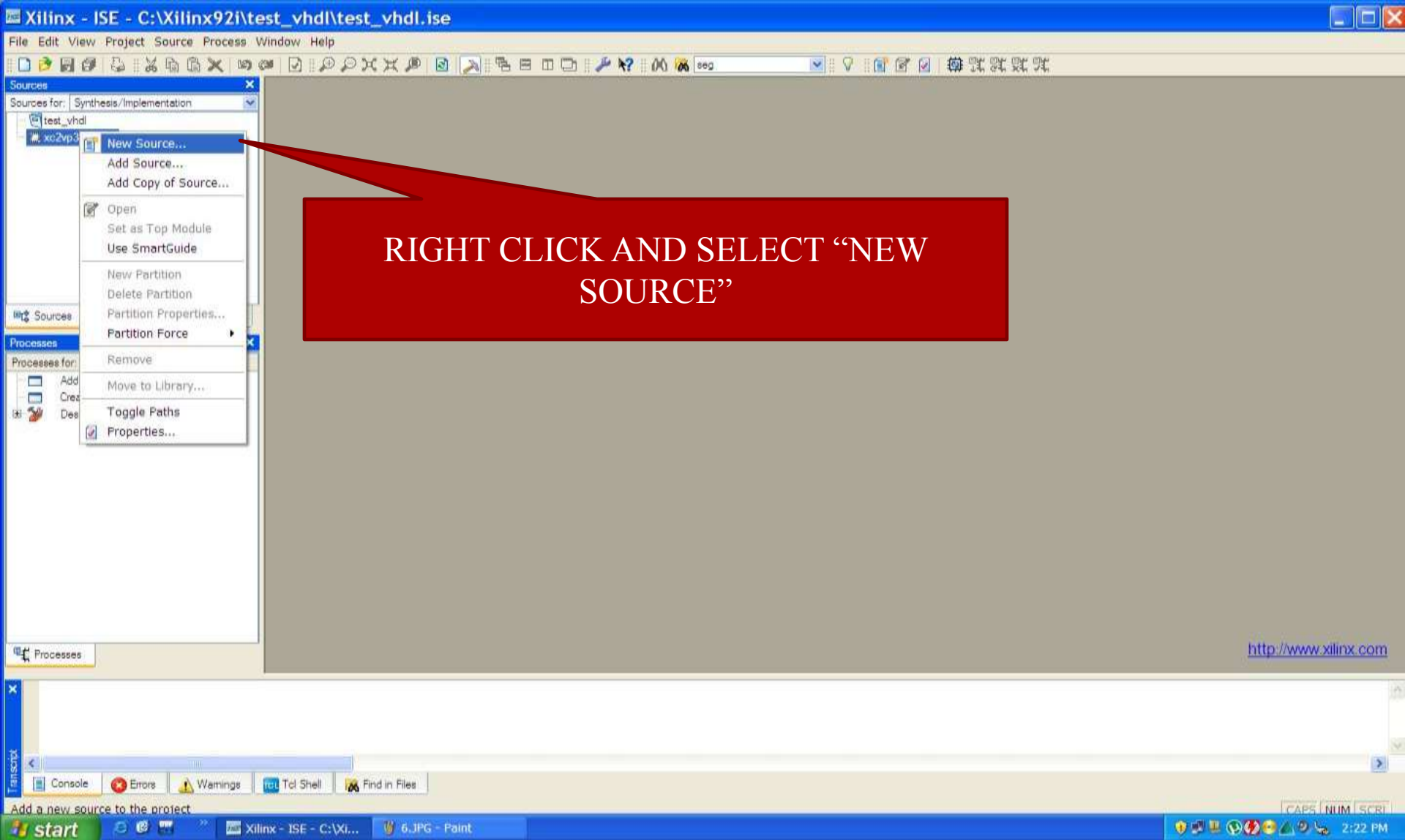


STEP:5

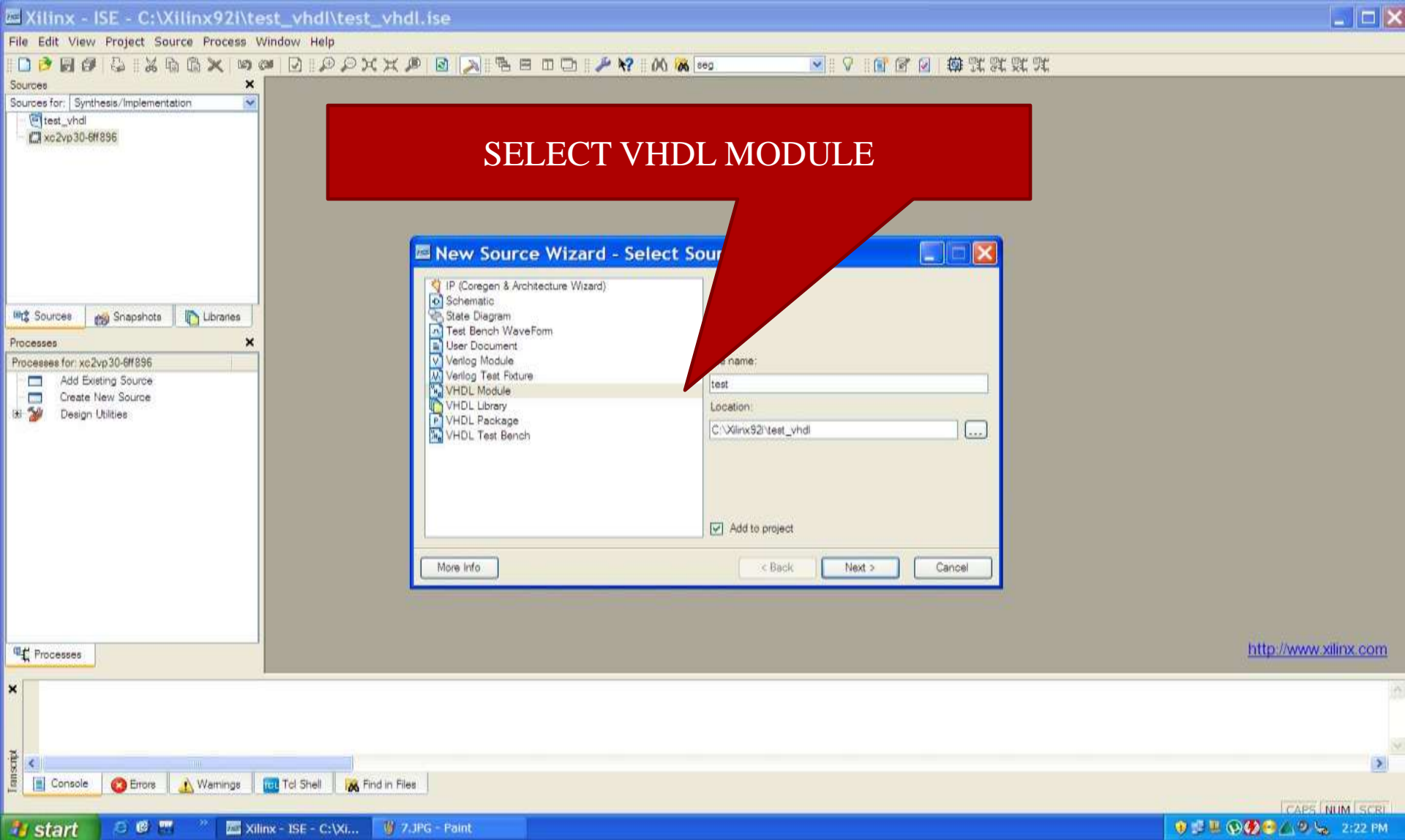


<http://www.xilinx.com>

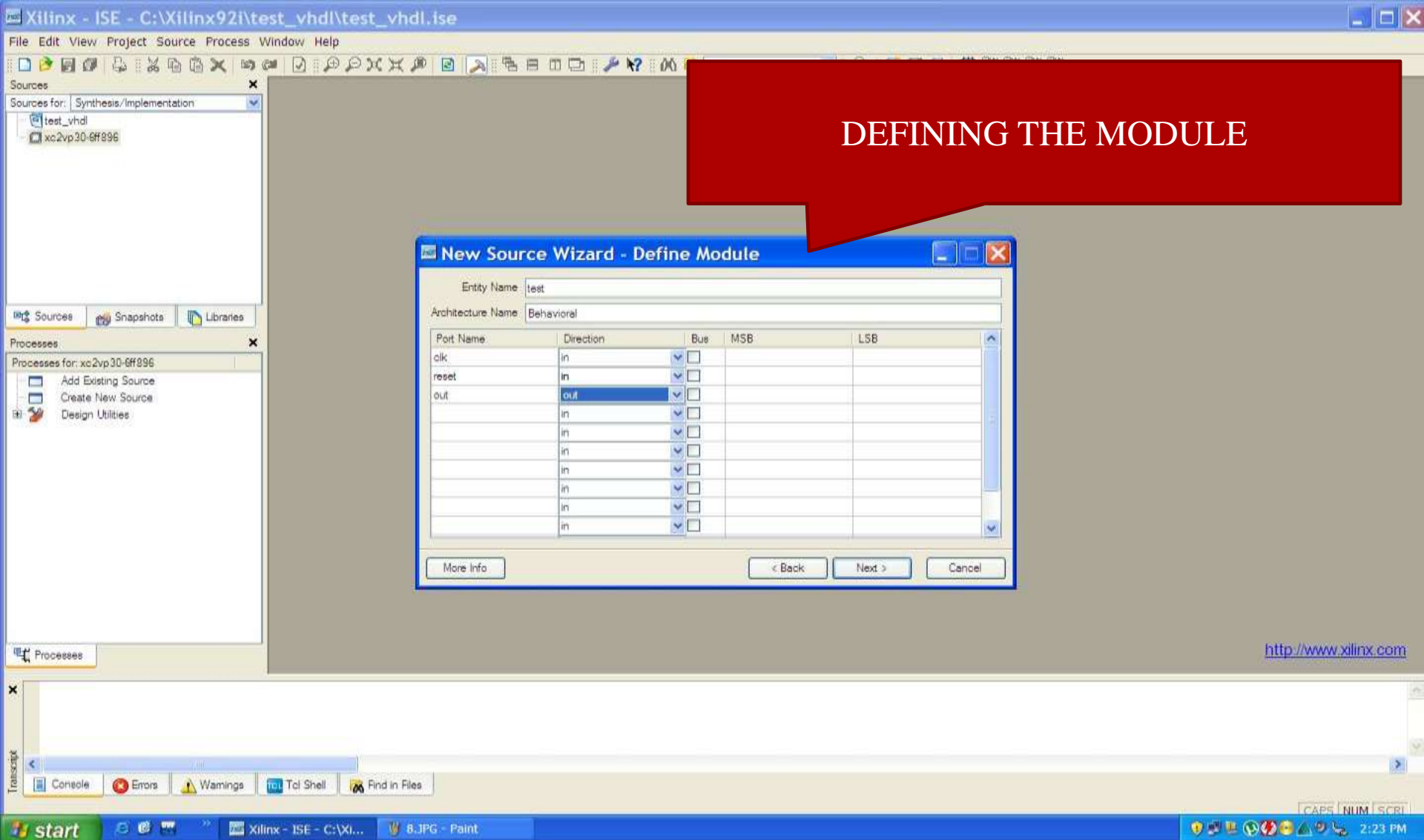
STEP:6



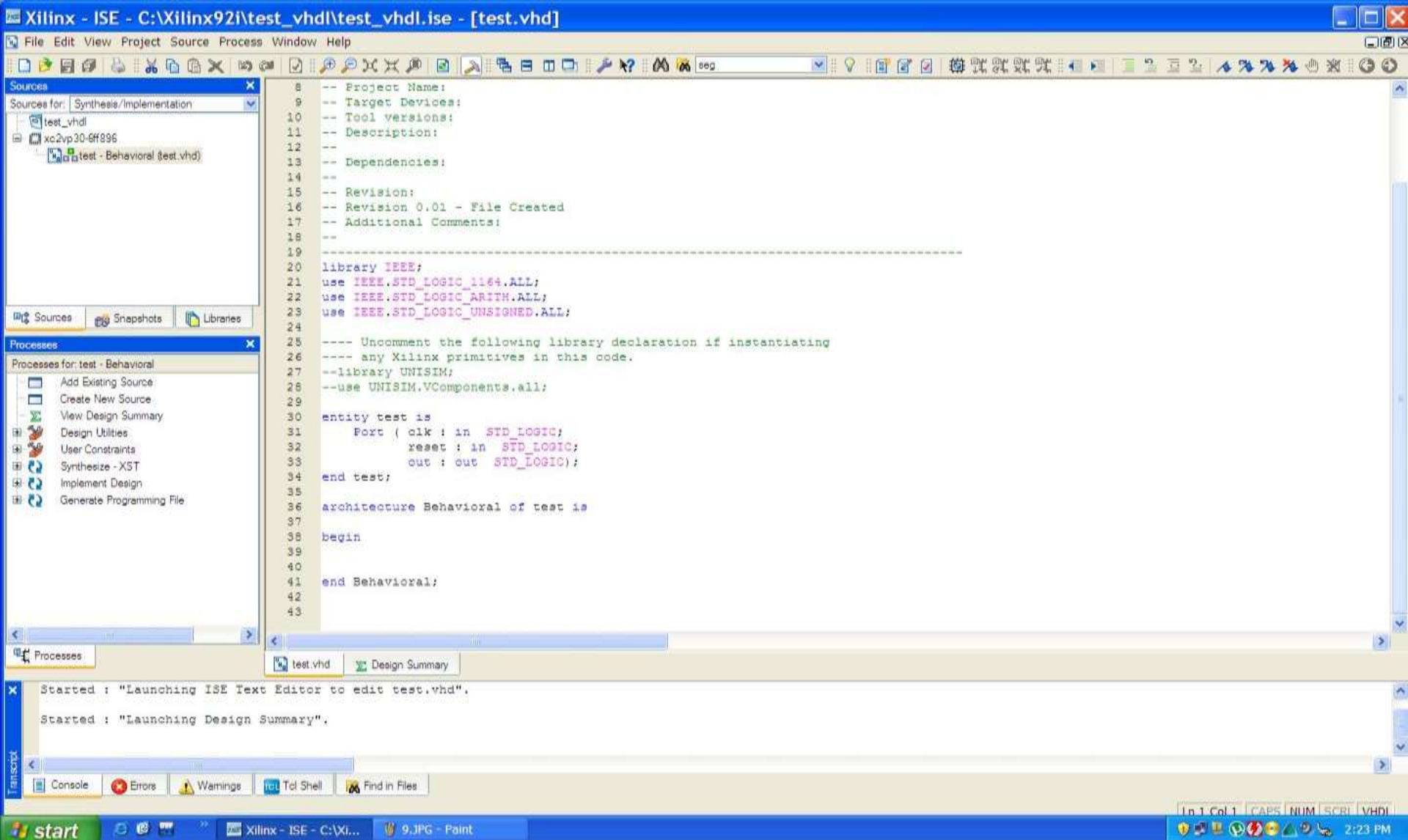
STEP:7



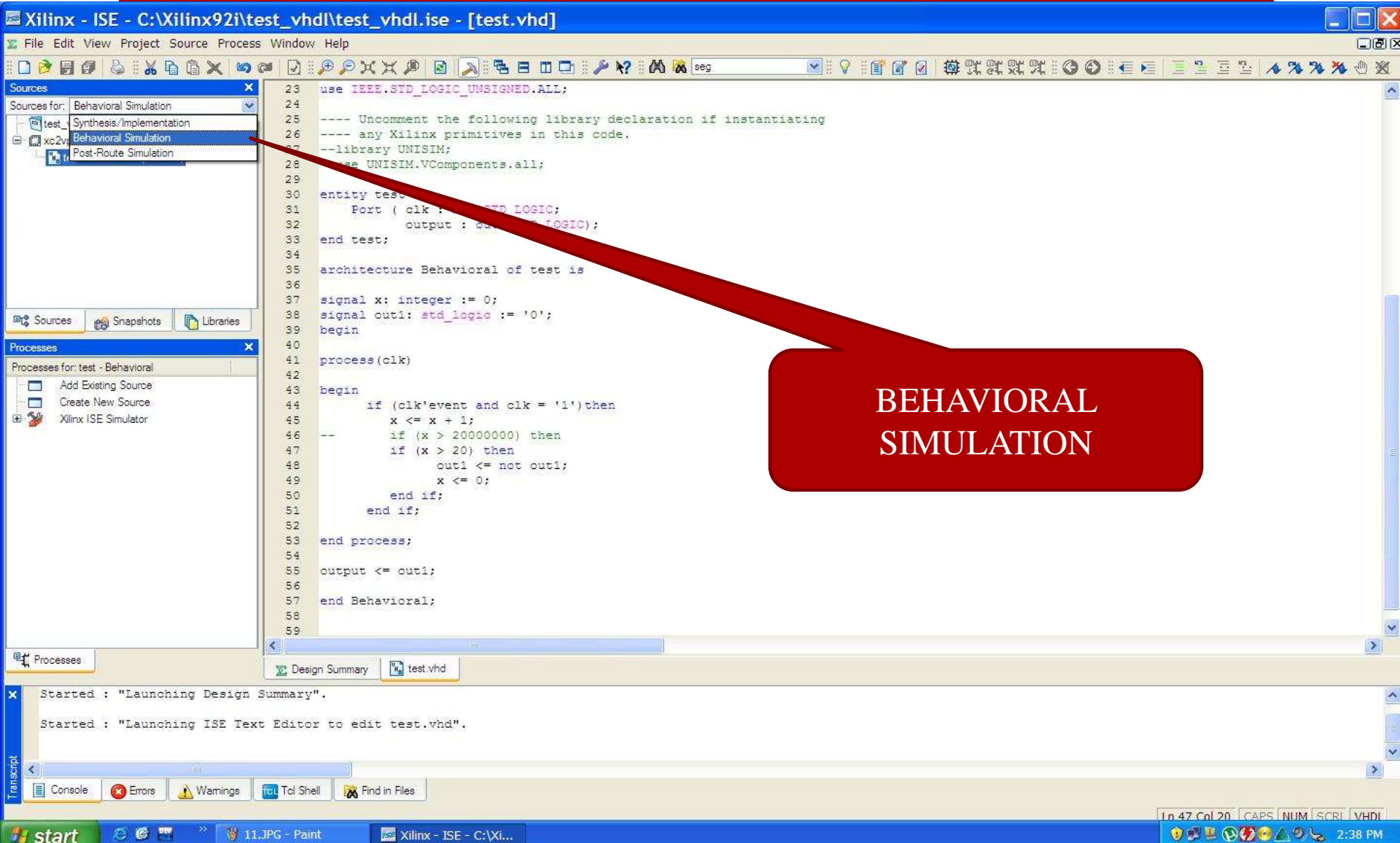
STEP:8



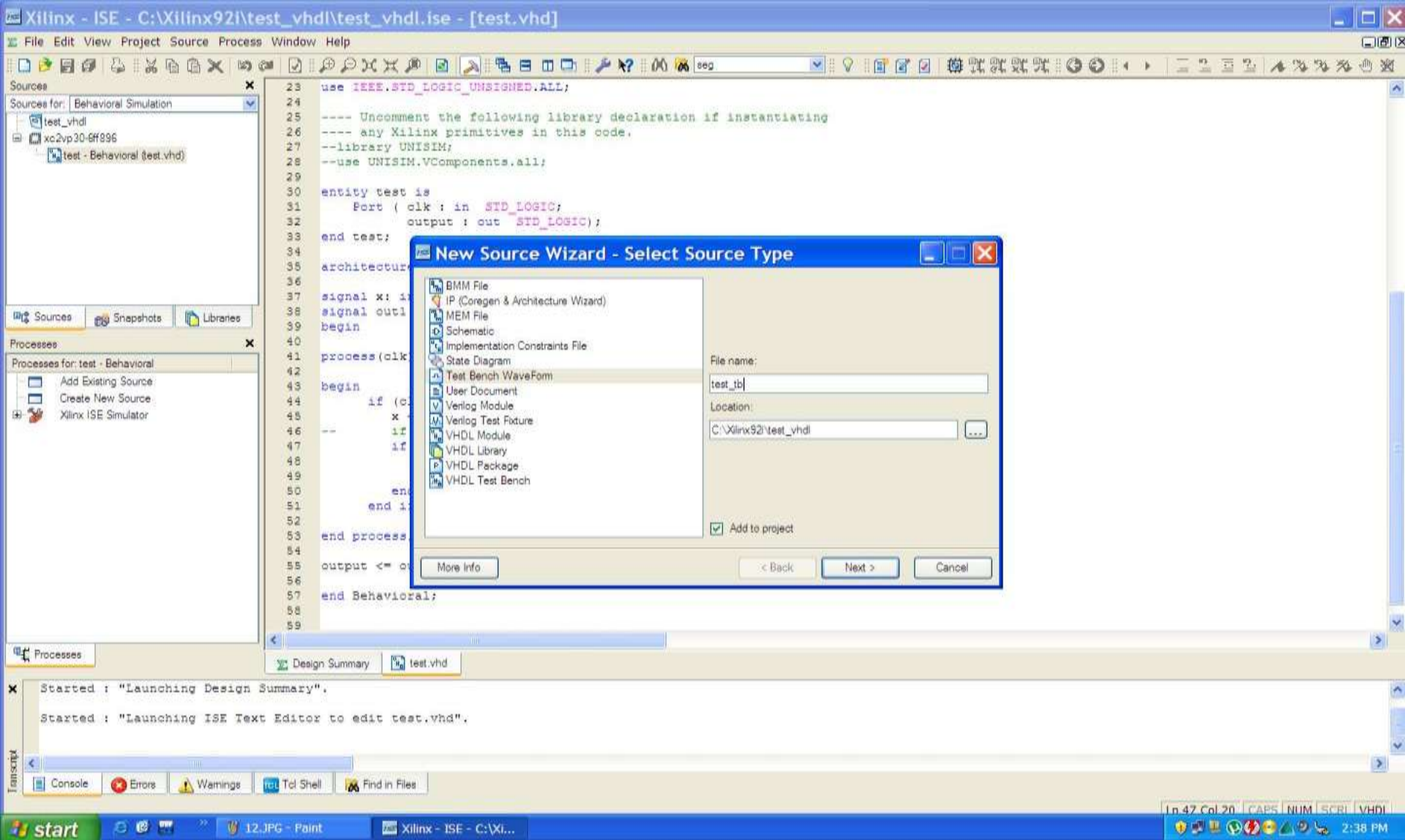
STEP:9



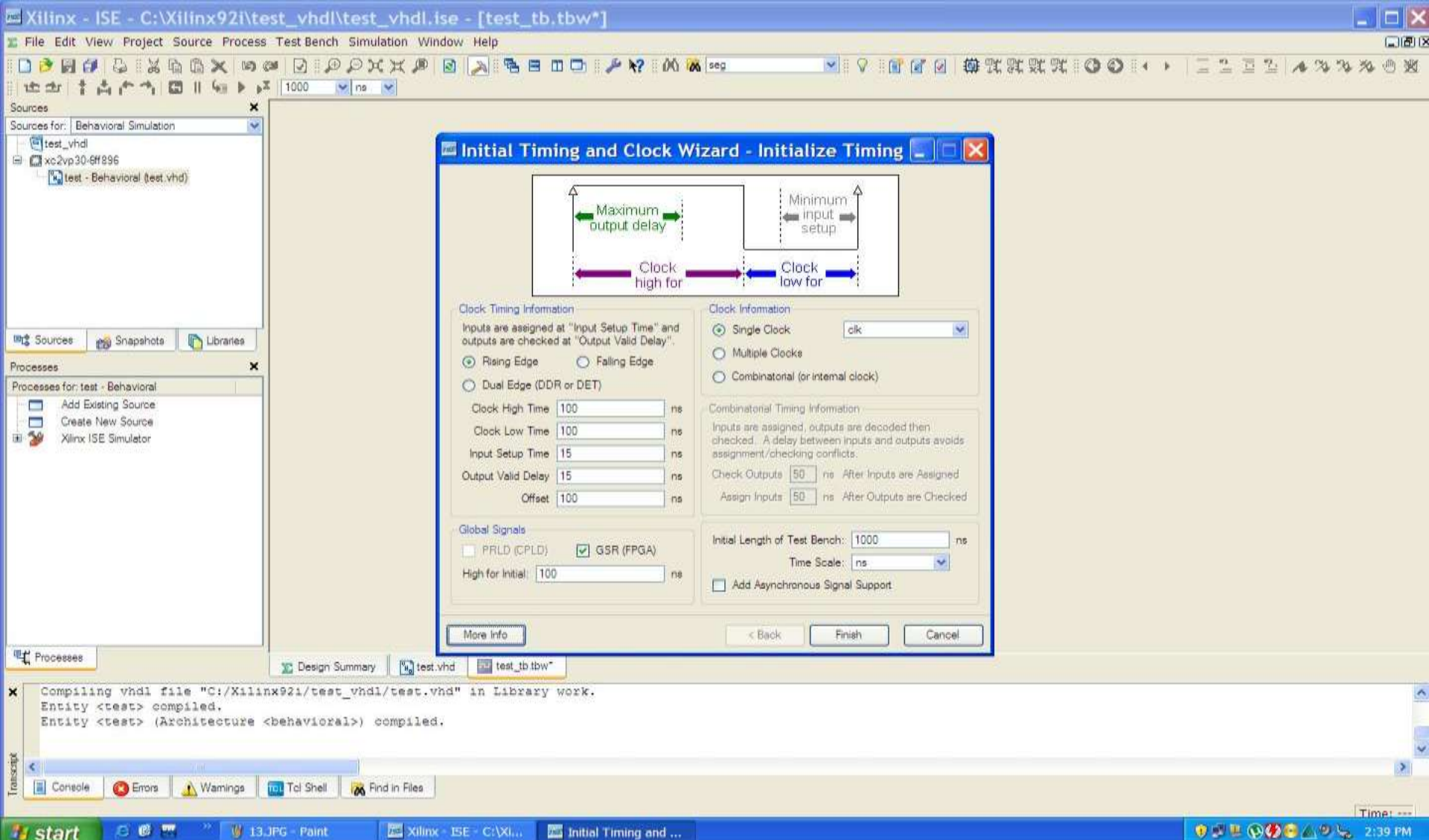
STEP:10



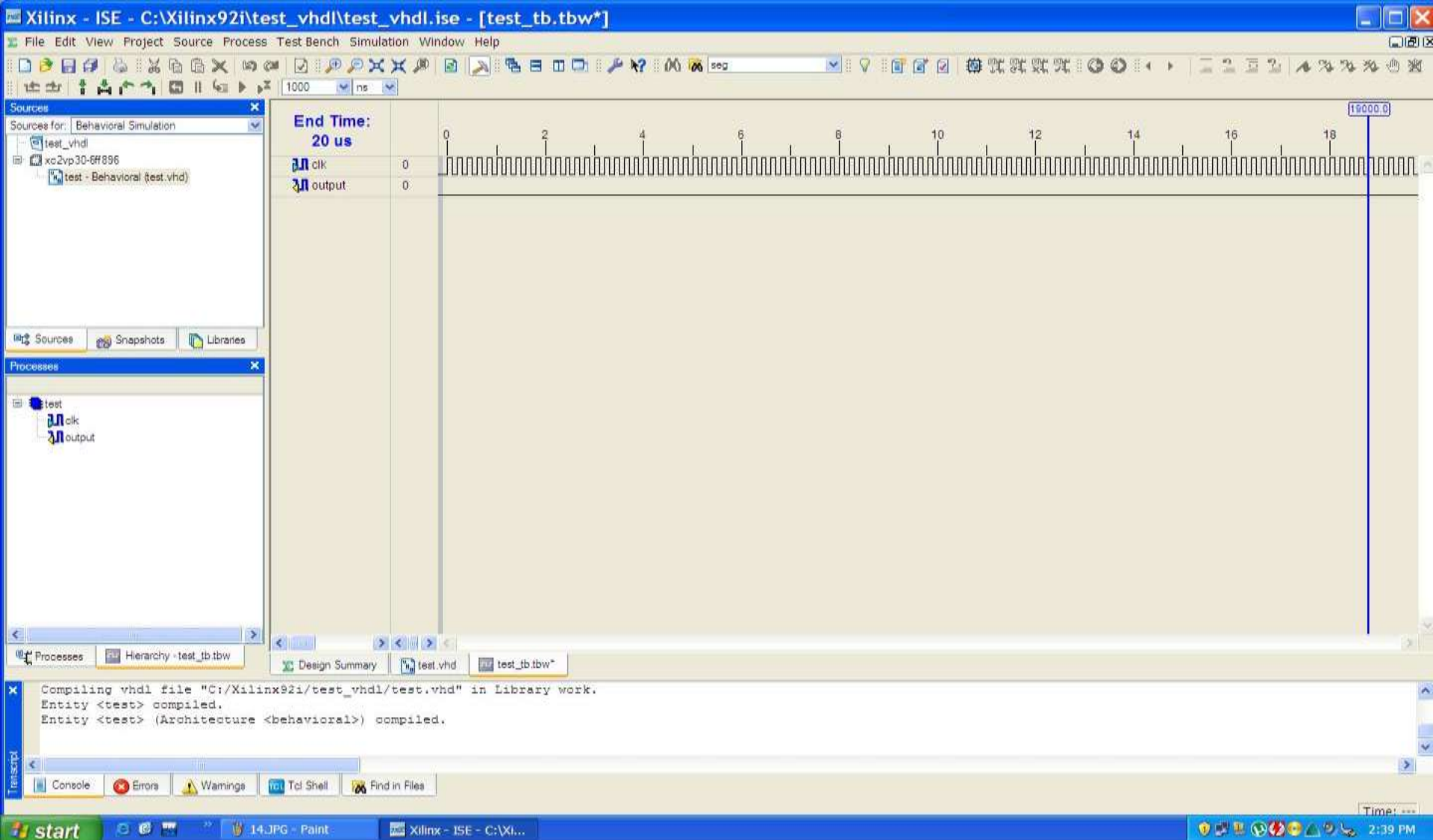
STEP:11



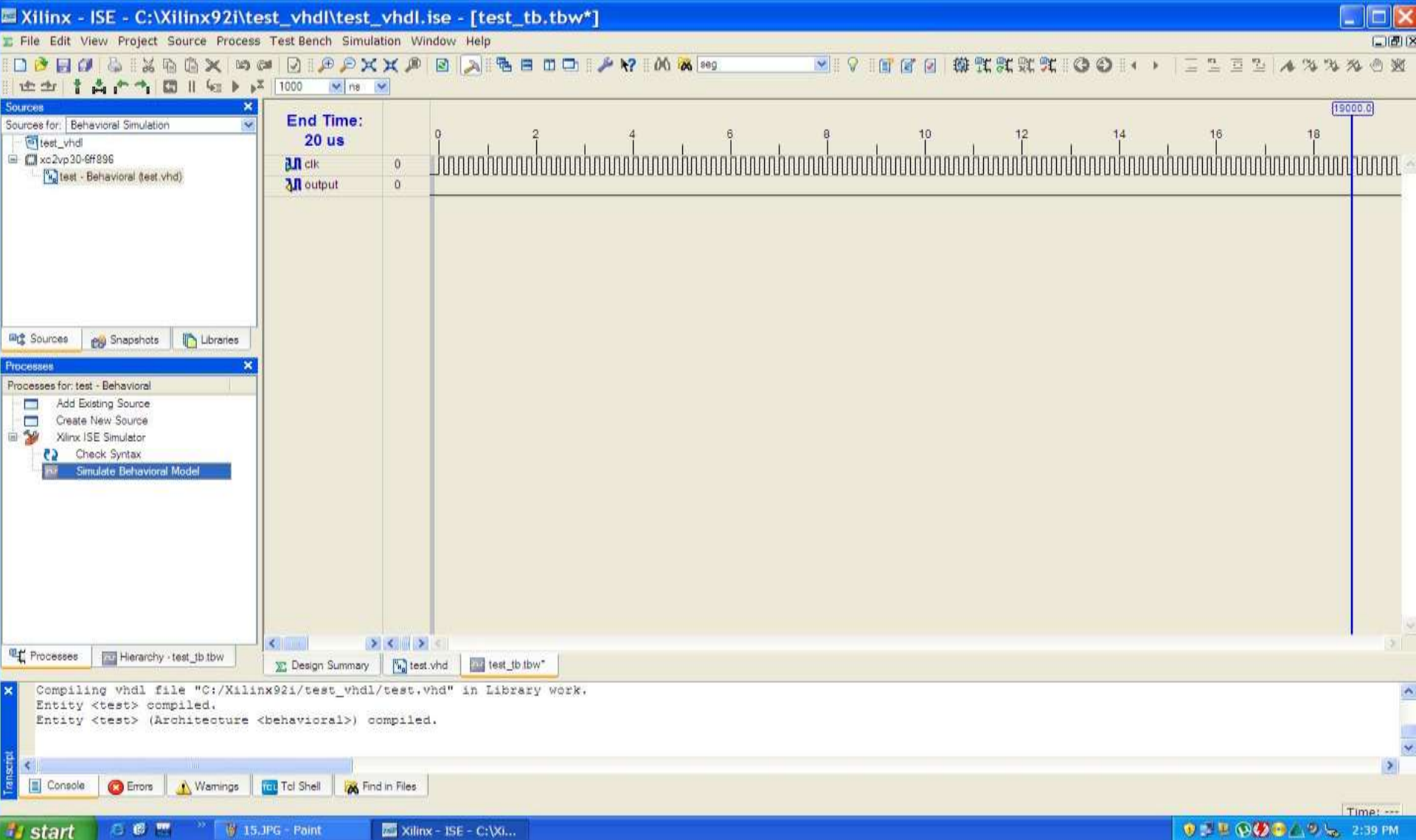
STEP:12



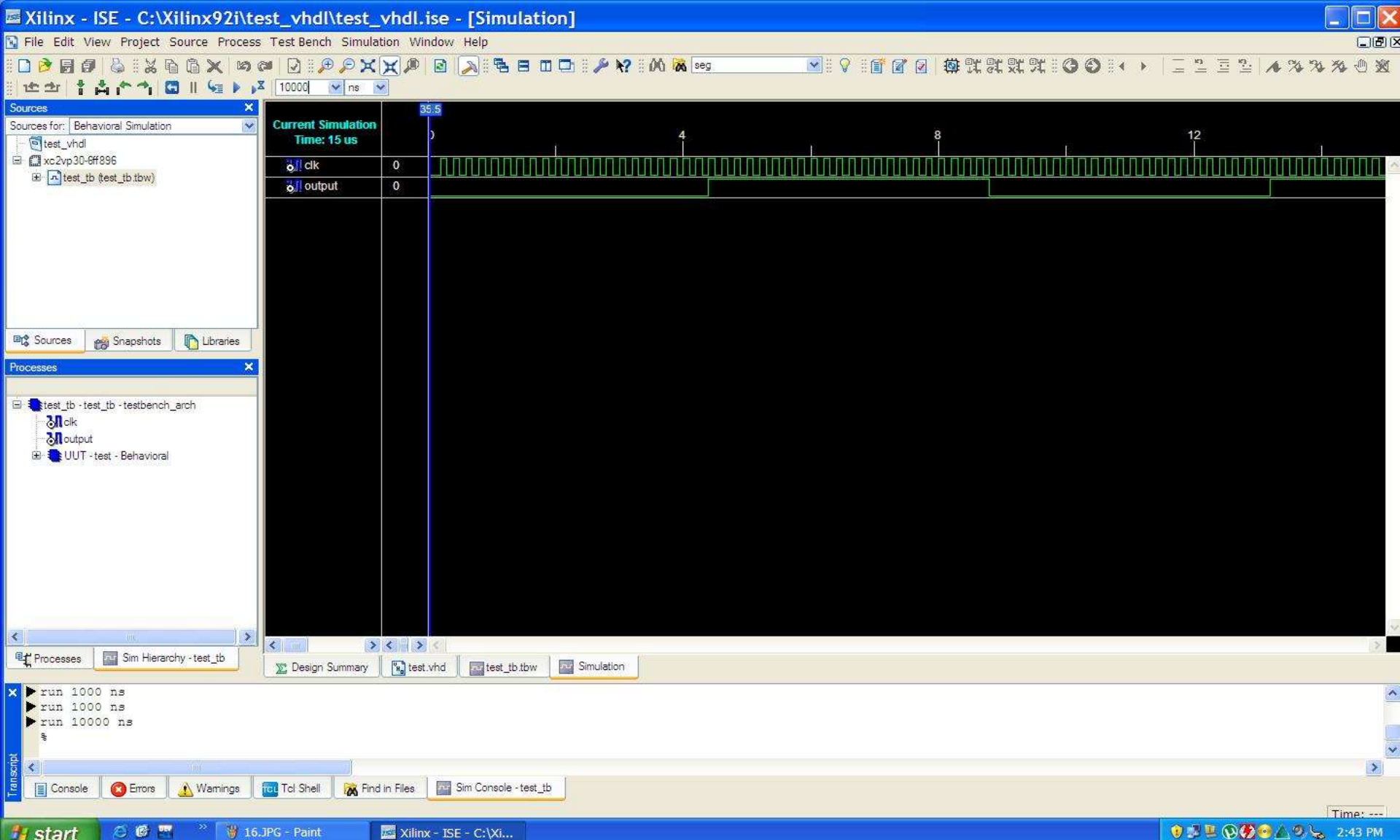
STEP:13



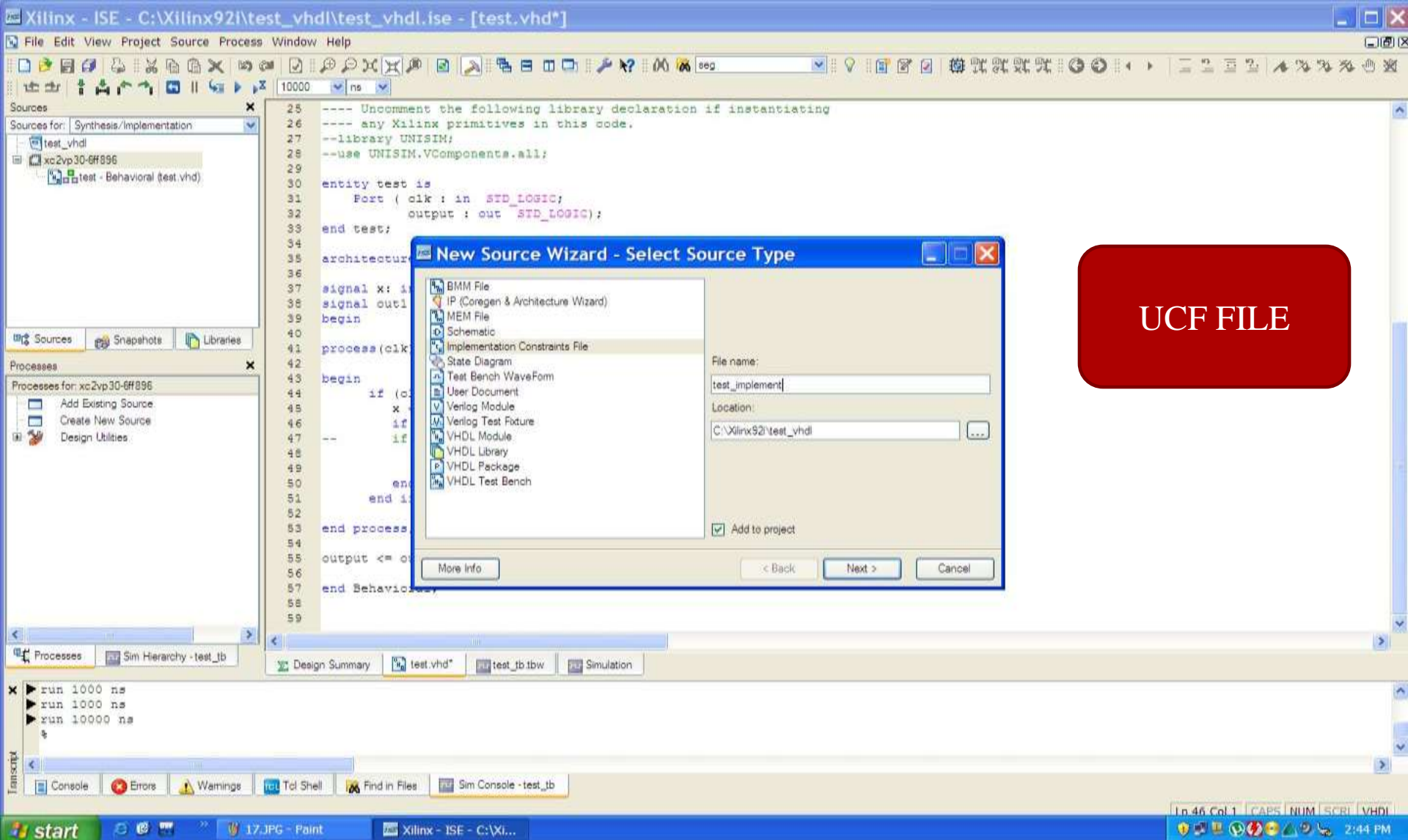
STEP:14



STEP:15

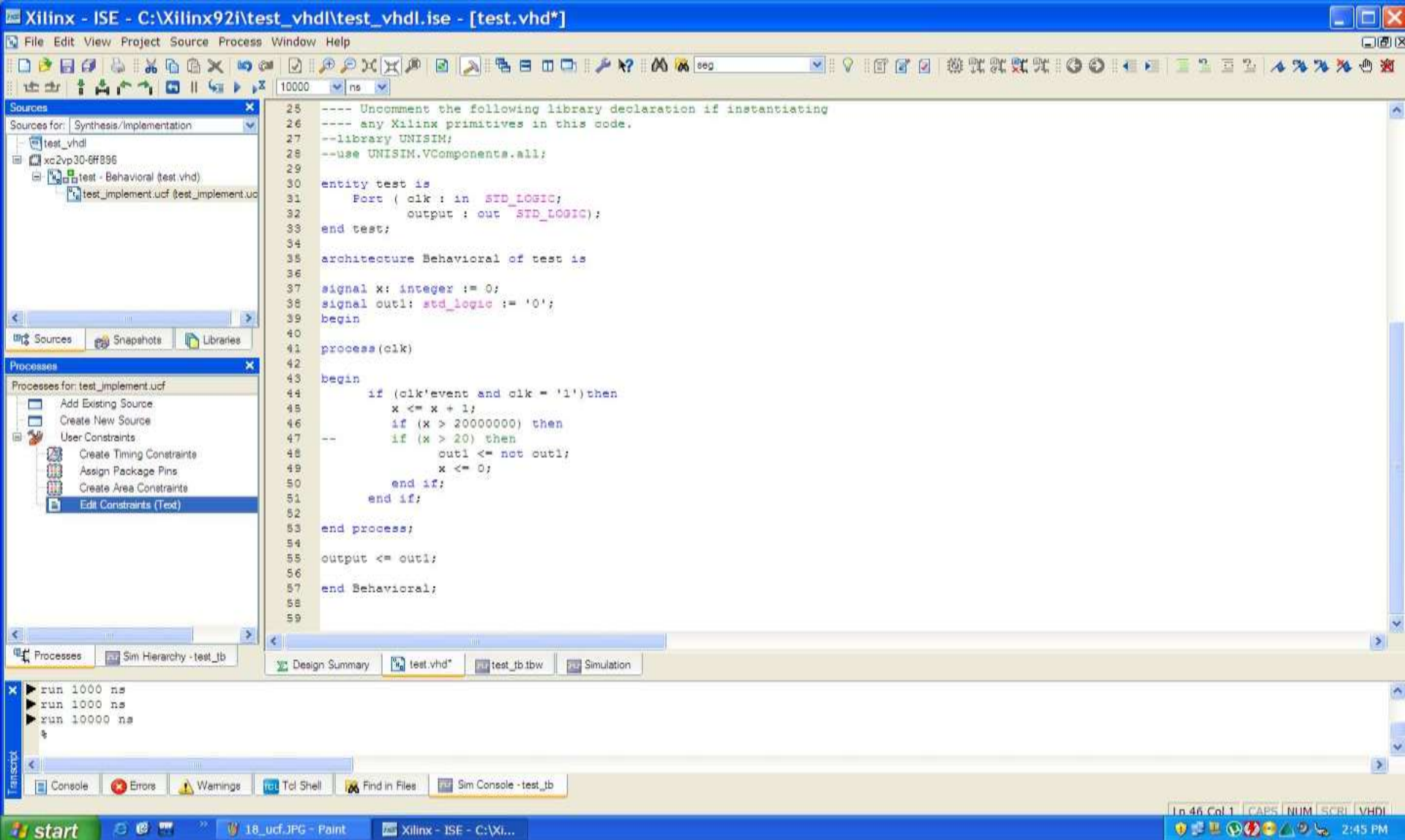


STEP:16

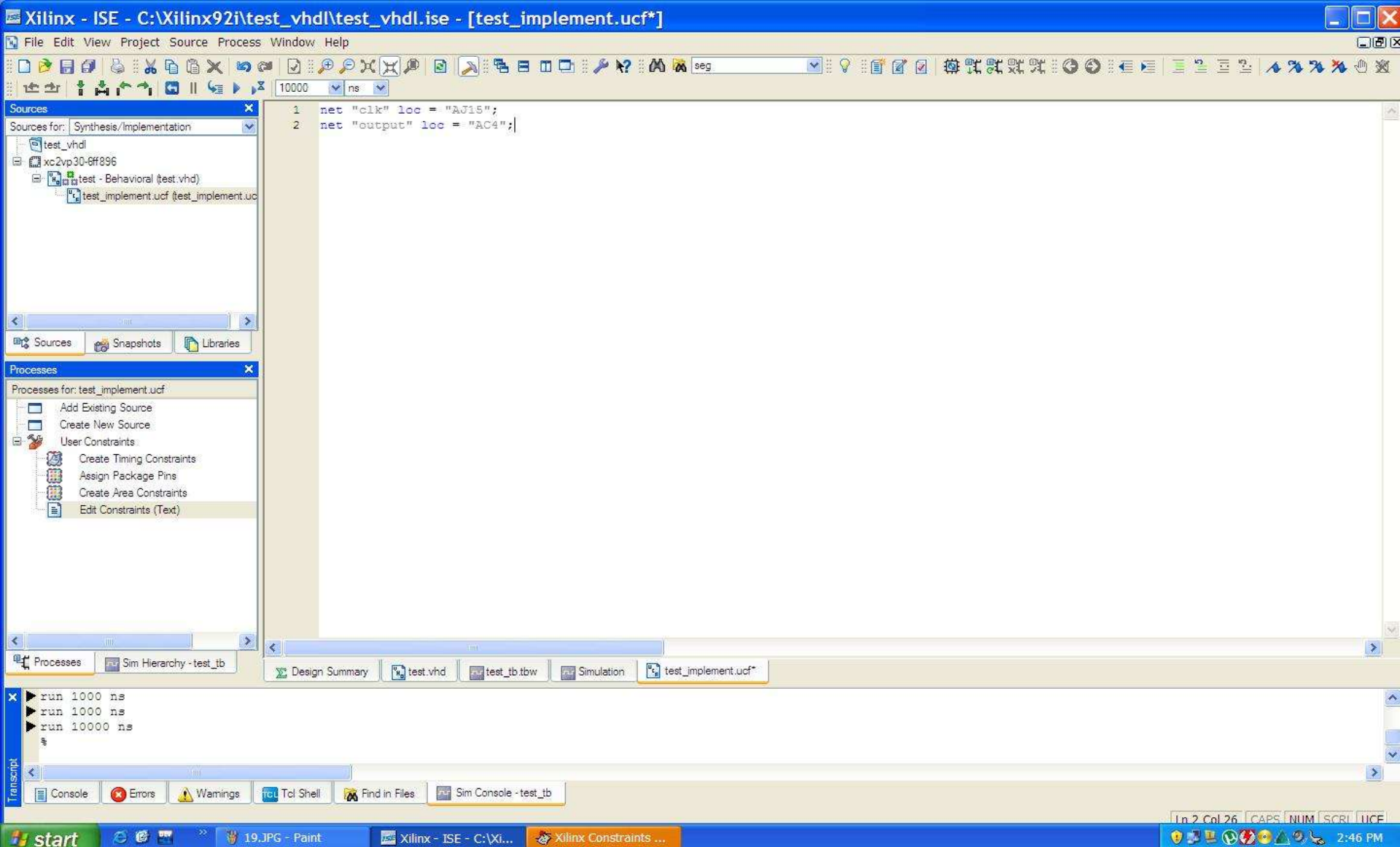


UCF FILE

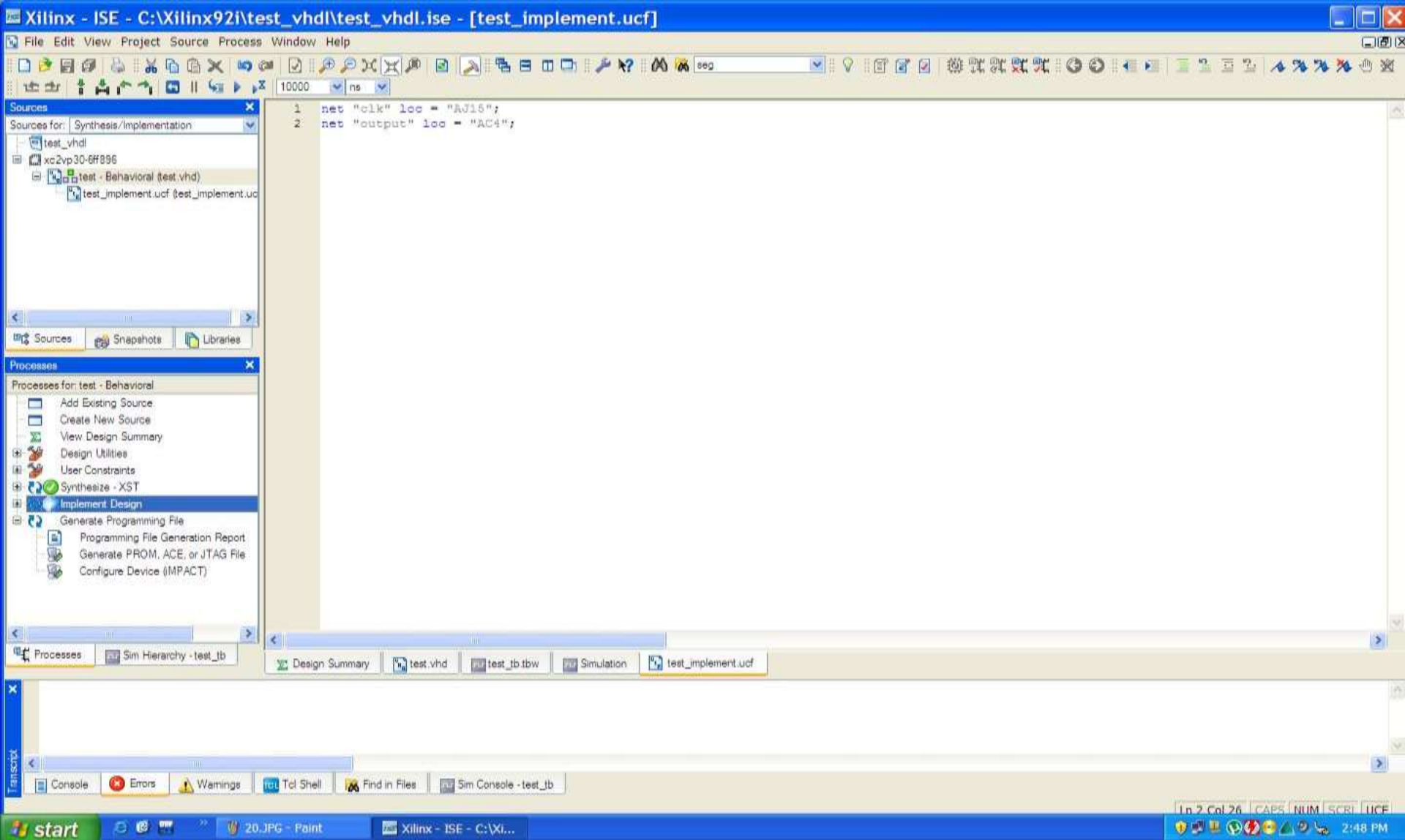
STEP:17



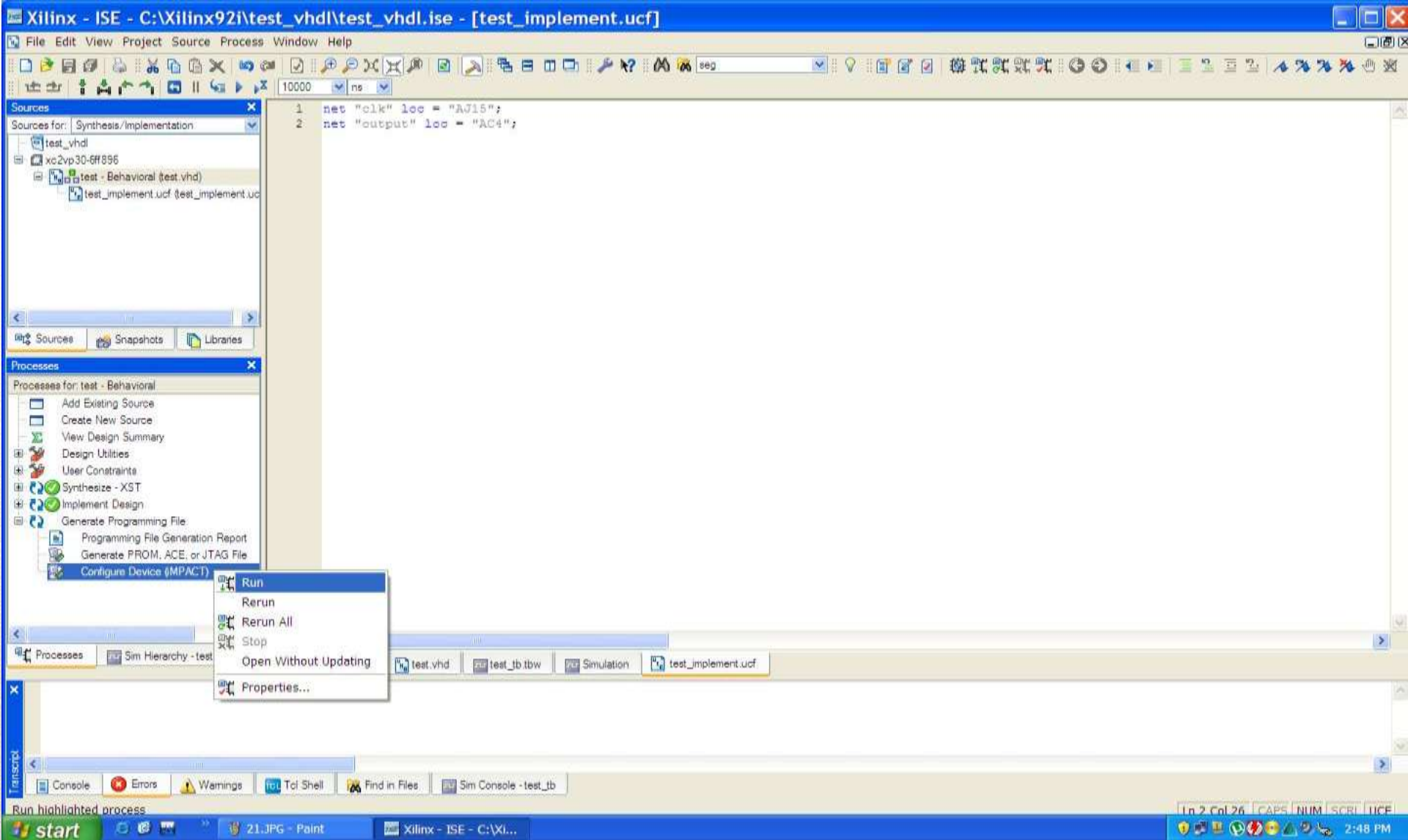
STEP:18



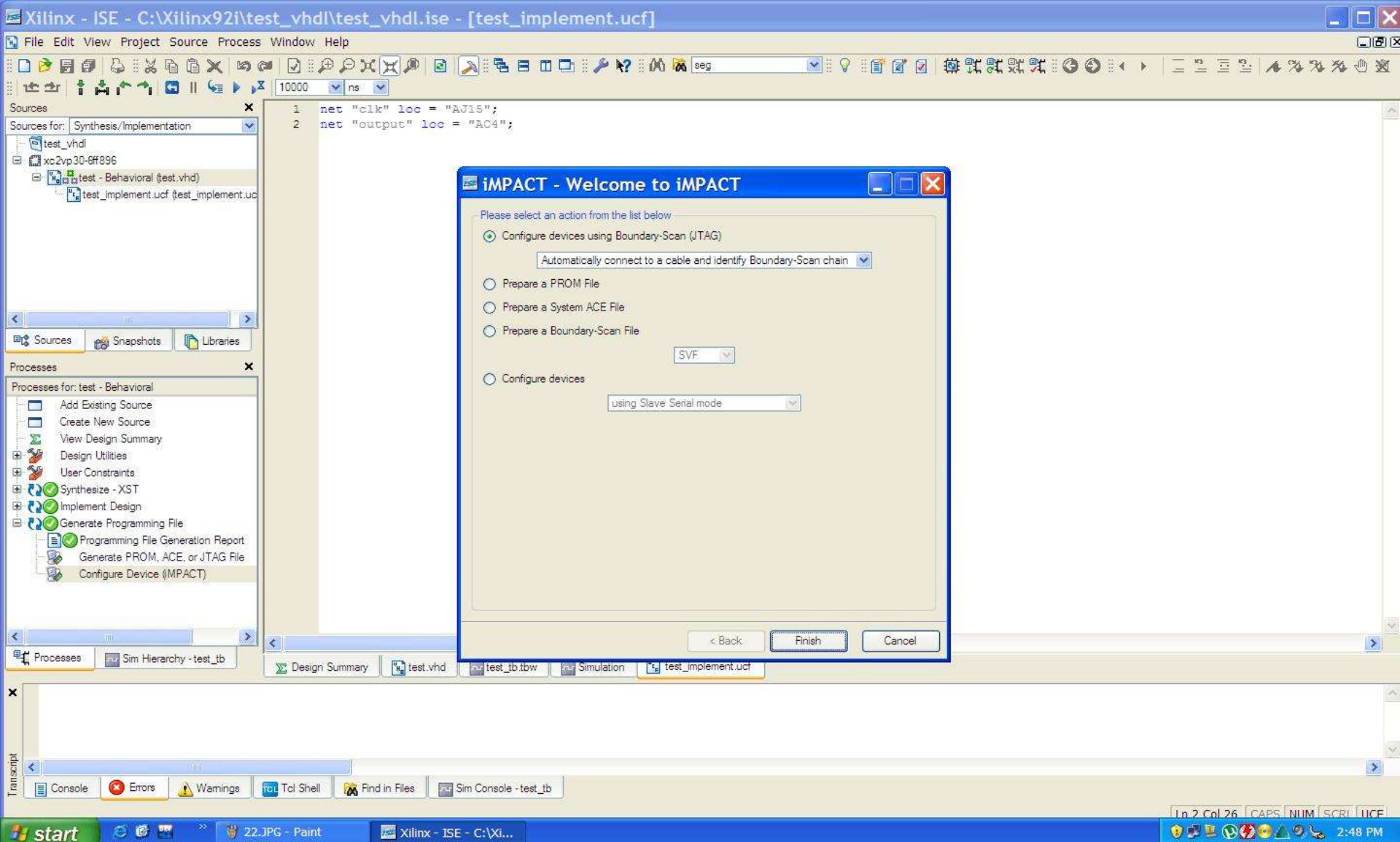
STEP:19



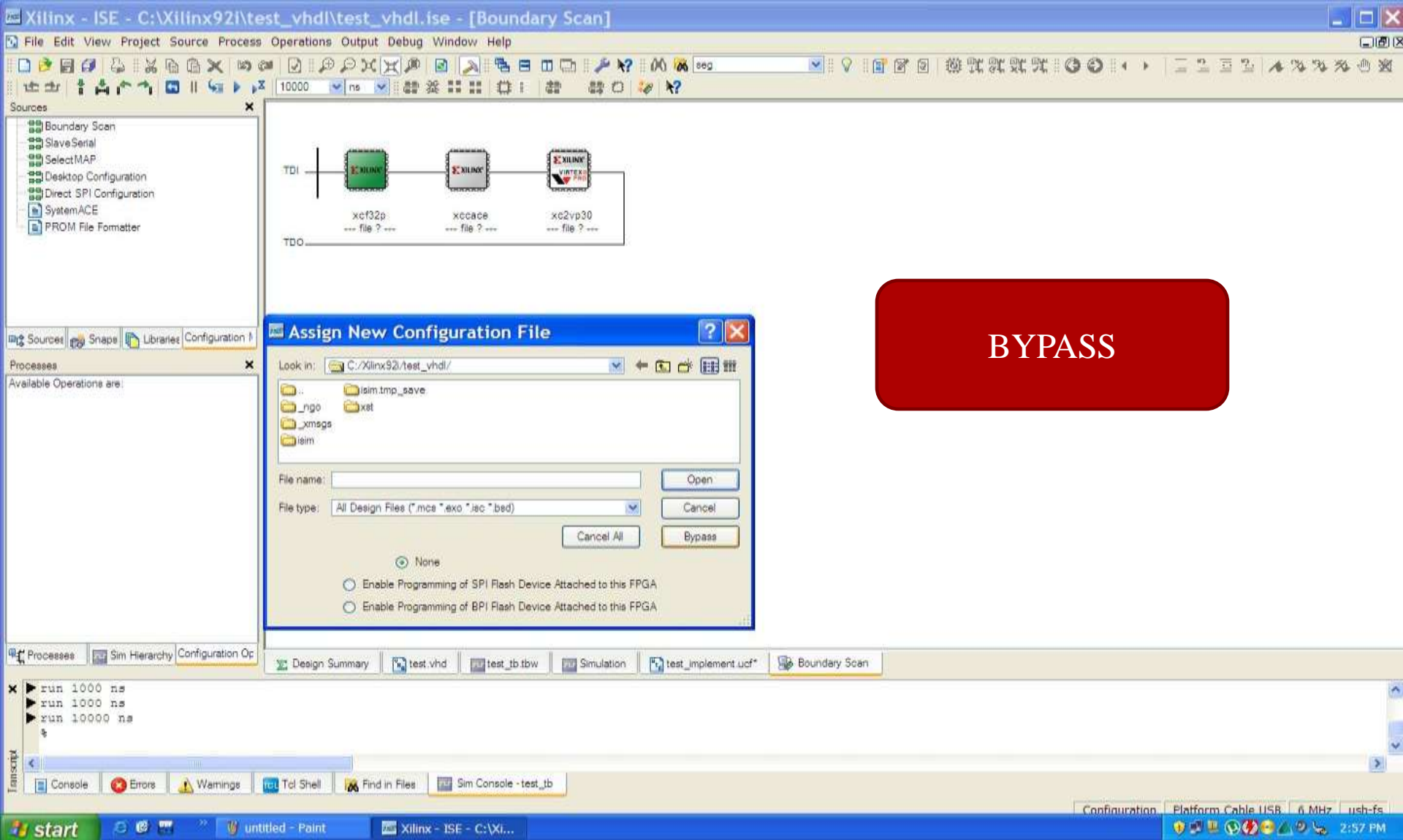
STEP:20



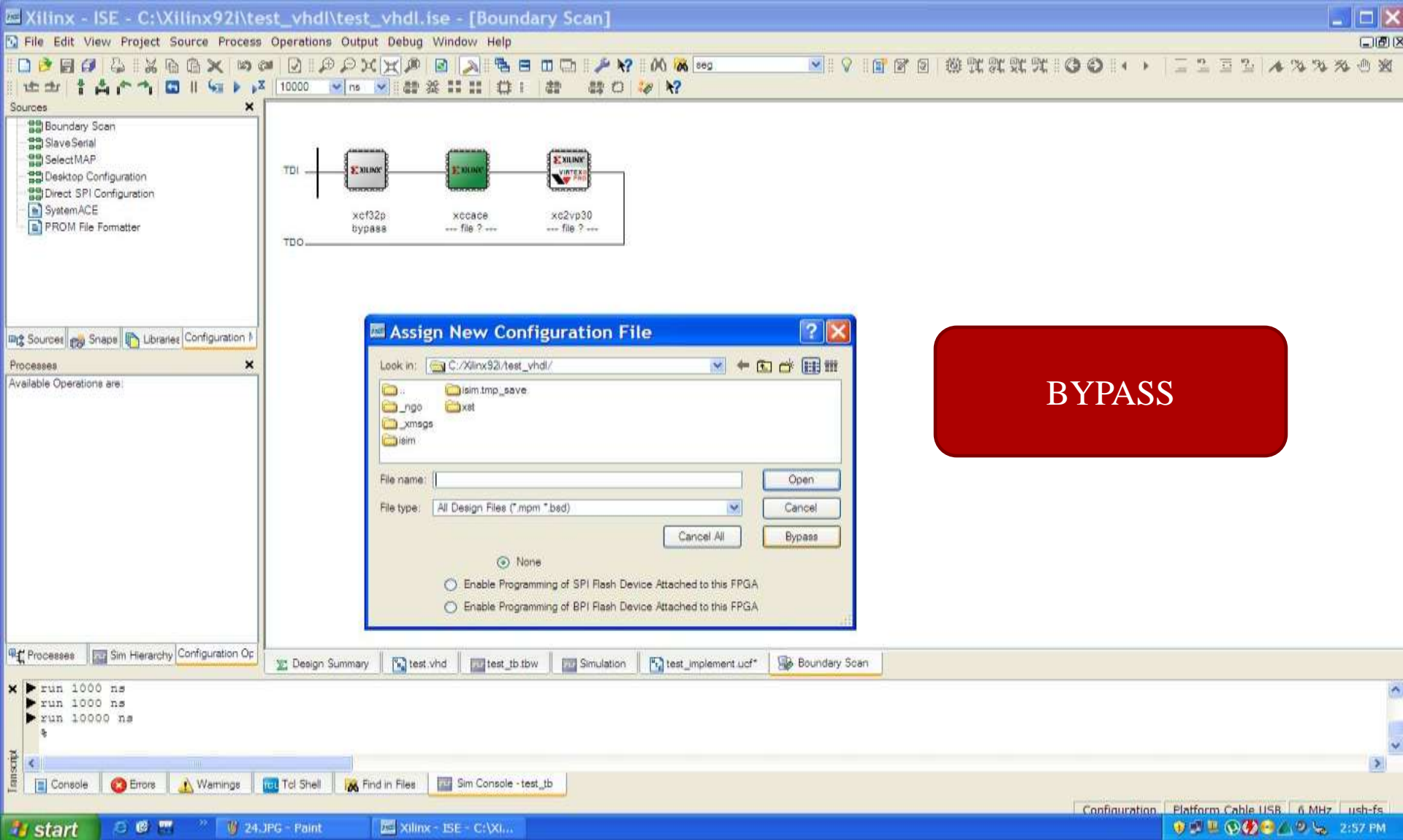
STEP:21



STEP:23

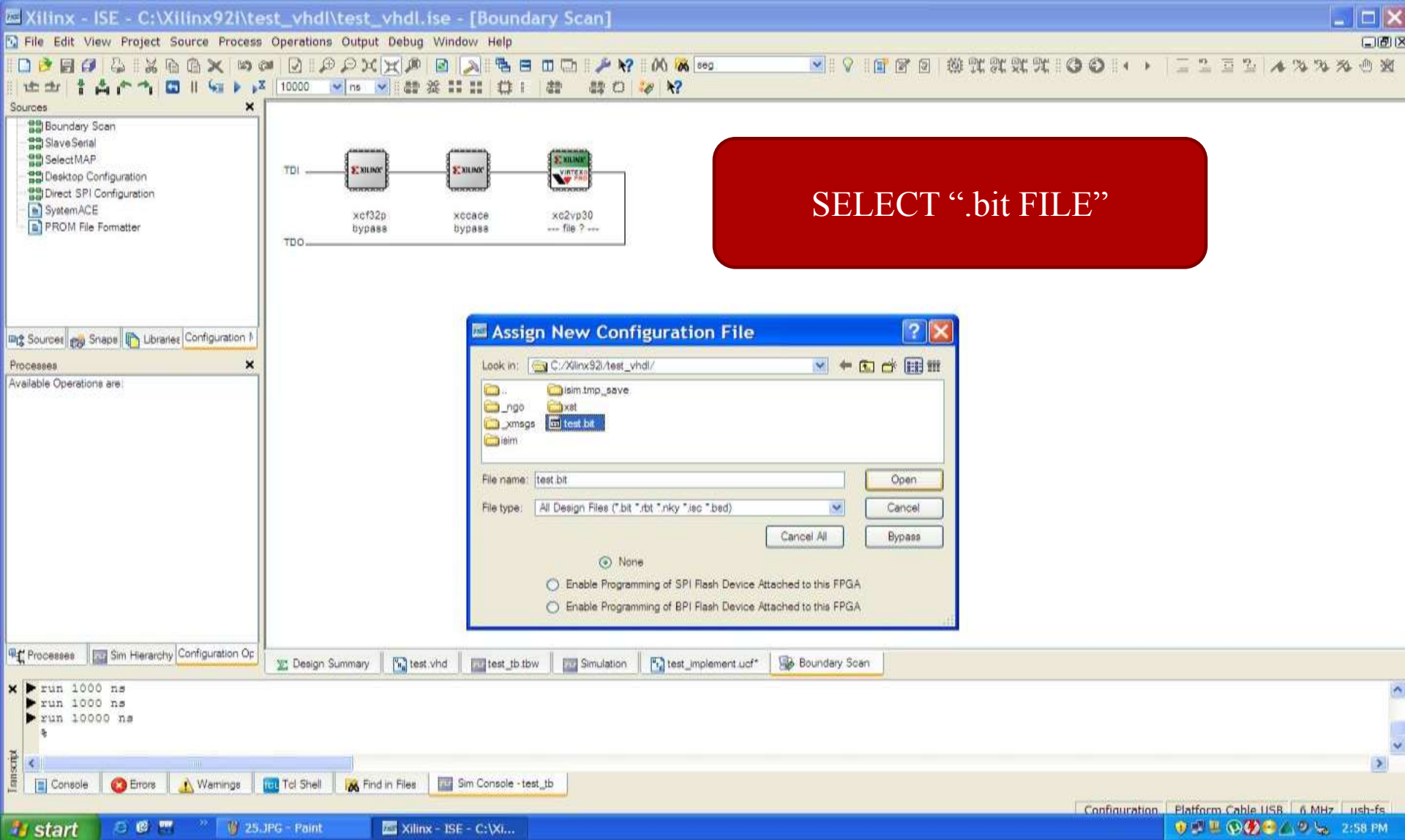


STEP:24

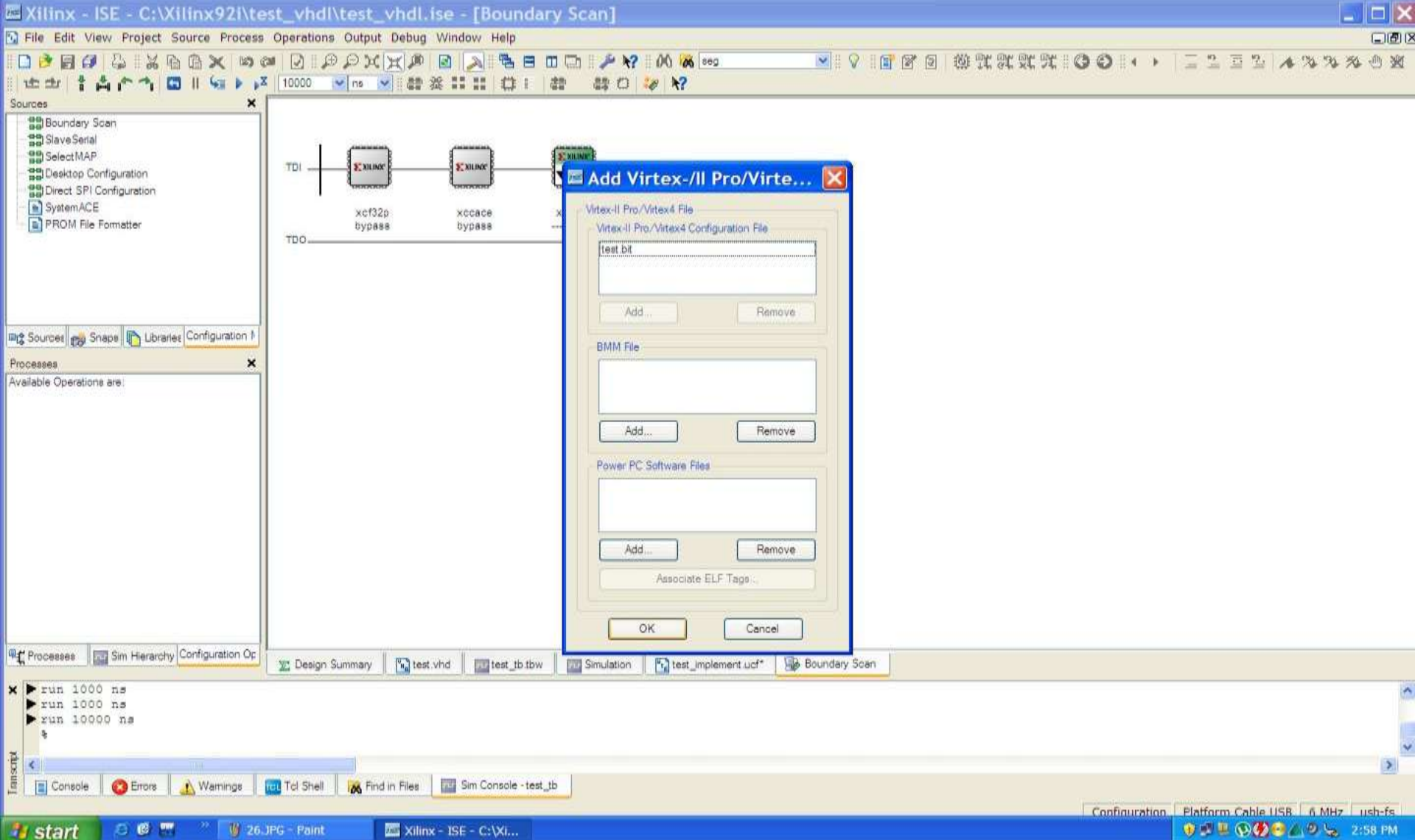


BYPASS

STEP:25



STEP:26



STEP:27

Xilinx - ISE - C:\Xilinx92i\test_vhdl\test_vhdl.ise - [Boundary Scan]

File Edit View Project Source Process Operations Output Debug Window Help

10000 ns

Sources

- Boundary Scan
- Slave Serial
- SelectMAP
- Desktop Configuration
- Direct SPI Configuration
- SystemACE
- PROM File Formatter

Processes

Available Operations are:

Please wait, loading device #3, C:\Xilinx92i\test_vhdl\test.bit

TDI

TDO

xc32p bypass

xc32p bypass

xc2vp30 --- file ? ---

Warning

WARNING:IMPACT:2257 - Startup Clock has been changed to 'JtagClk' in the bitstream stored in memory, but the original bitstream file remains unchanged.

OK

run 1000 ns

run 1000 ns

run 10000 ns

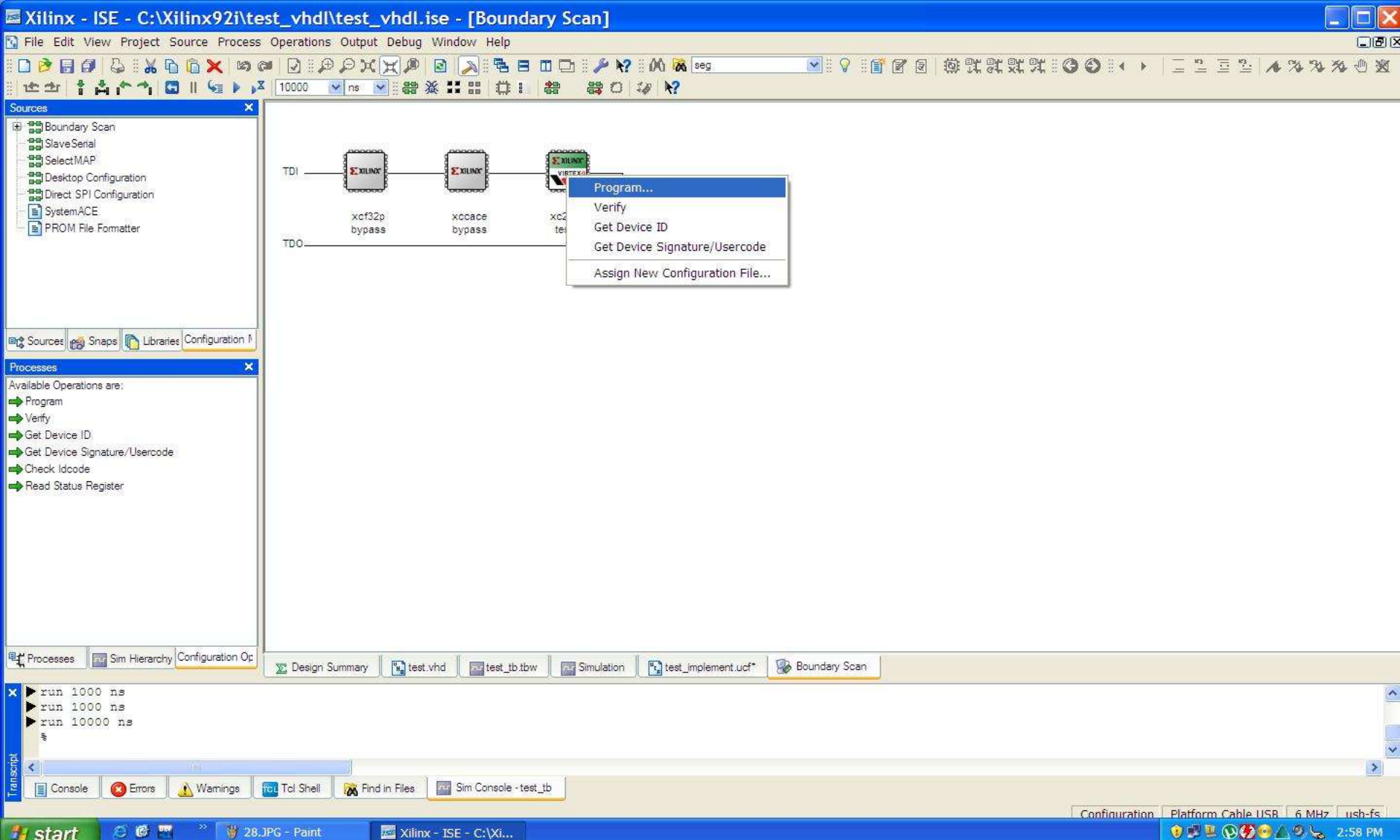
Transcript

Console Errors Warnings Tcl Shell Find in Files Sim Console - test_tb

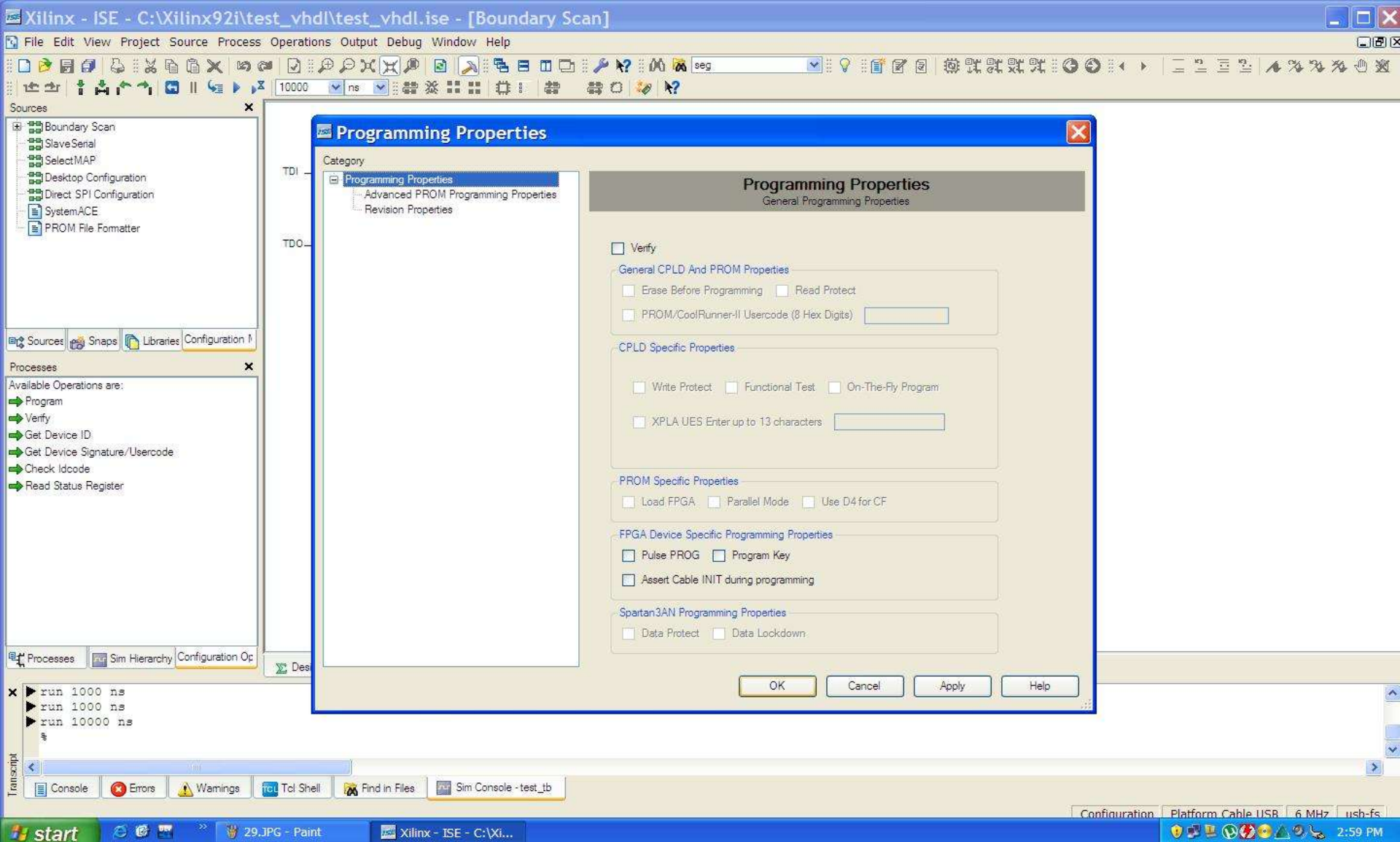
Configuration Platform Cable USB 6 MHz usb-fs

start 27.JPG - Paint Xilinx - ISE - C:\Xi... Warning 2:58 PM

STEP:28



STEP:29



STEP:30

Xilinx - ISE - C:\Xilinx92i\test_vhdl\test_vhdl.ise - [Boundary Scan]

File Edit View Project Source Process Operations Output Debug Window Help

10000 ns

Sources

- Boundary Scan
- Slave Serial
- SelectMAP
- Desktop Configuration
- Direct SPI Configuration
- SystemACE
- PROM File Formatter

Processes

Available Operations are:

- Program
- Verify
- Get Device ID
- Get Device Signature/Usercode
- Check Idcode
- Read Status Register

Diagram:

```
graph LR; TDI --- xc32p[xc32p bypass]; xc32p --- xcace[xcace bypass]; xcace --- xc2vp30[xc2vp30 test.bit]; xc2vp30 --- TDO
```

YIPPEeeee!!!!

Program Succeeded

run 1000 ns
run 1000 ns
run 10000 ns

Configuration Platform Cable USB 6 MHz ush-fs

start 30.JPG - Paint Xilinx - ISE - C:\Xi...

2:59 PM

STEP:31