

# UNIT 4

## Analog and Digital Electronics

### Sequential Circuit

### SHIFT REGISTER

Rajeev Pandey  
ECE Department

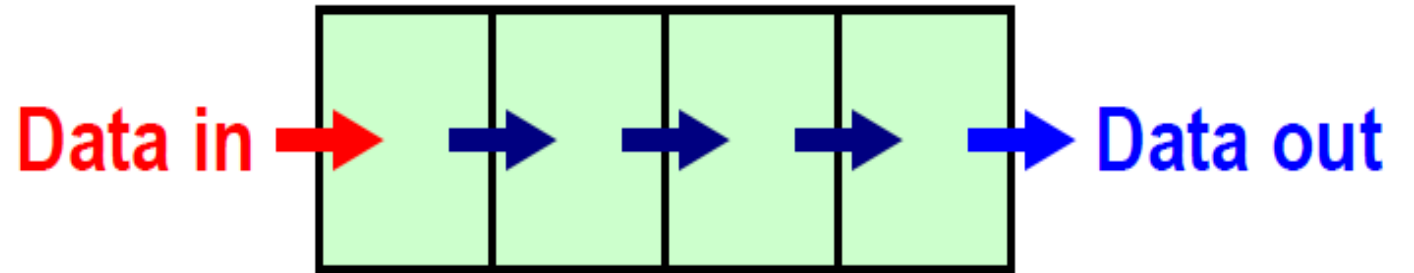
# Type of the Shift Registers

- Serial In Serial Output
  - a. Left to Right
  - b. Right to left
  - c. Rotate Left
  - d. Rotate Right
- Parallel in Parallel out
- Parallel in Serial Output
- Serial in Parallel out
- Universal Shift register

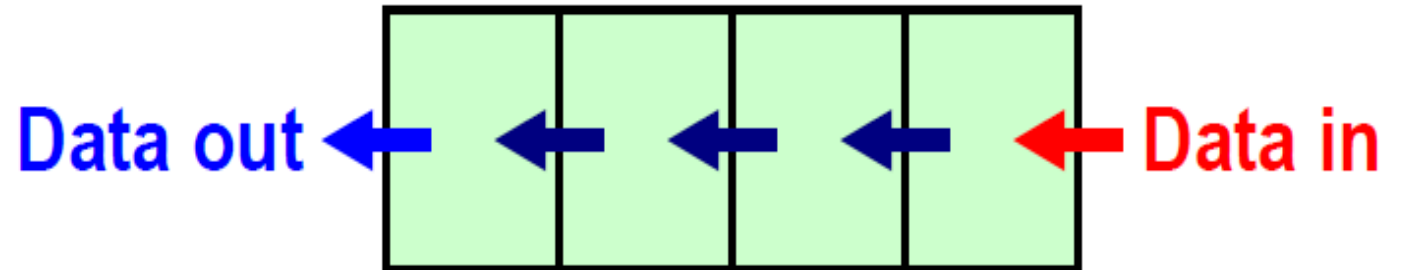
# Shift Register

## 1. Serial-in Serial-out

1(a) Left to Right

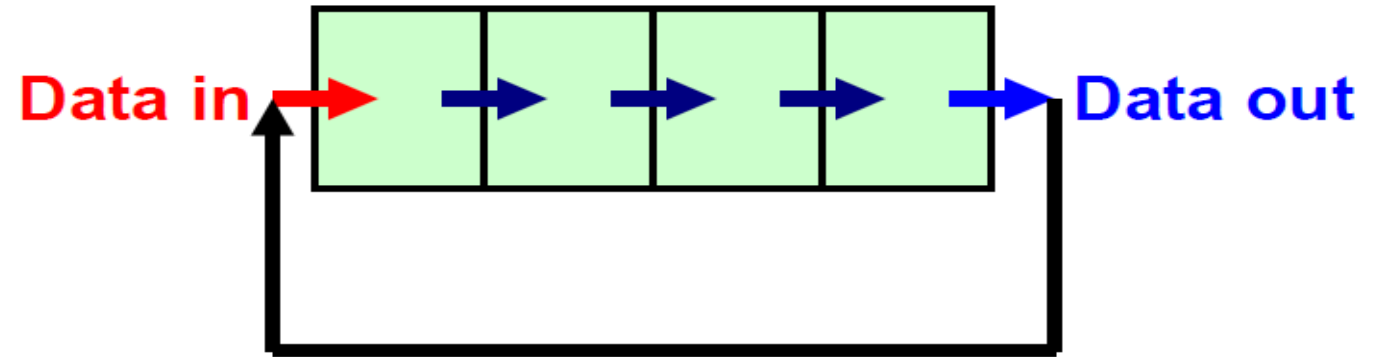


1(b) Right to Left

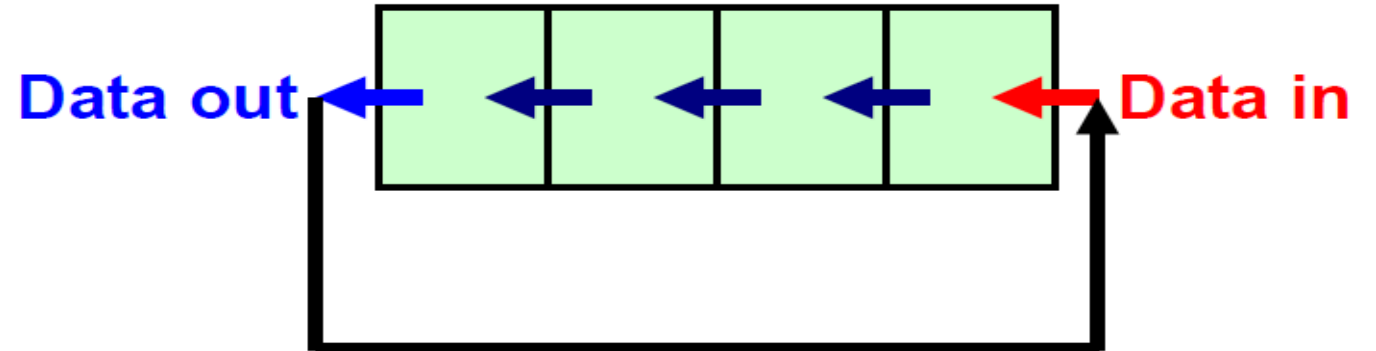


Contd.

1(c) Rotate Left

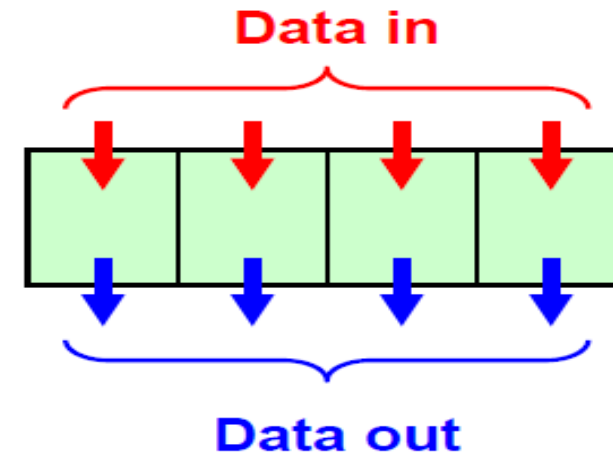


1(d) Rotate Right

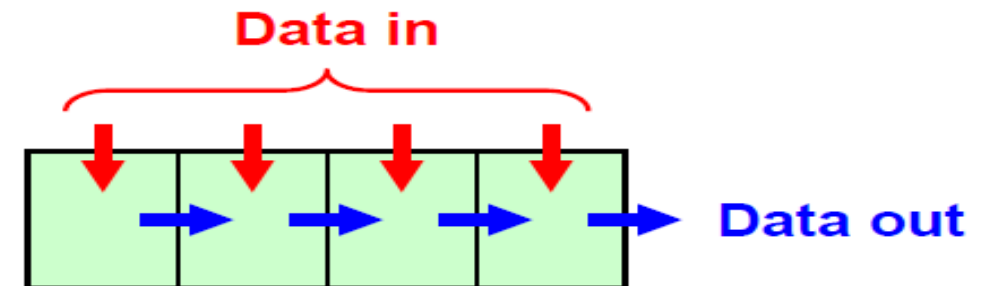


Contd.

## 2. Parallel-in Parallel-out

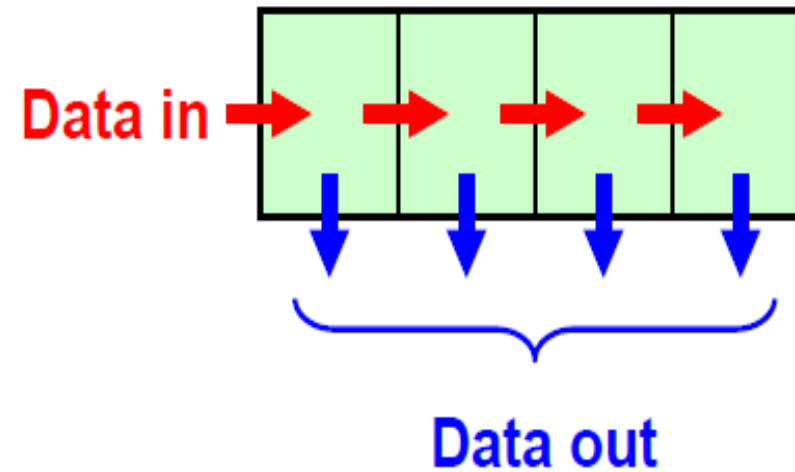


## 3. Parallel-in Serial-out

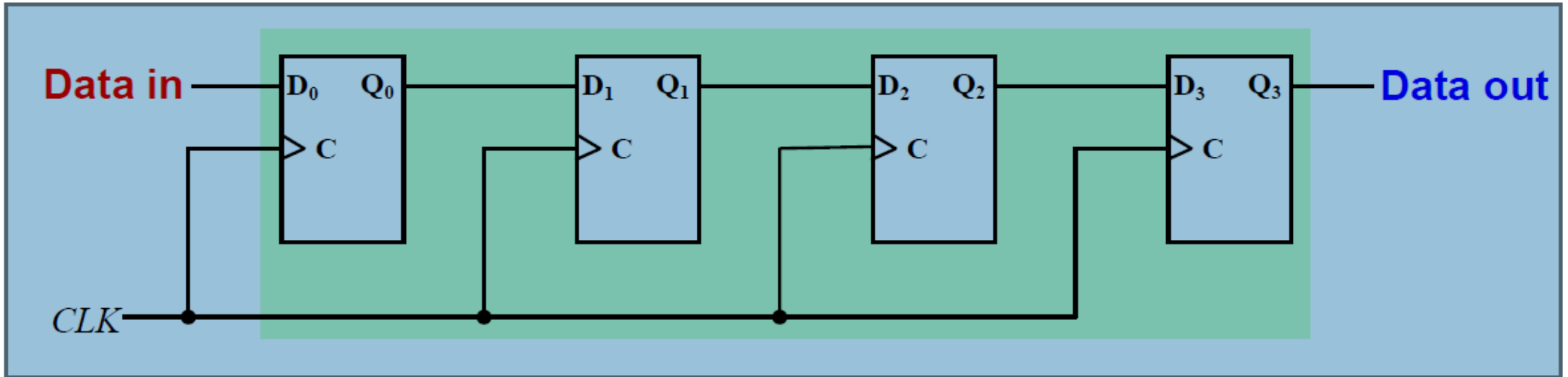


Contd.

#### 4. Serial-in Parallel-out



# Serial in Serial out Register



# Operation

Let data in is 00001010

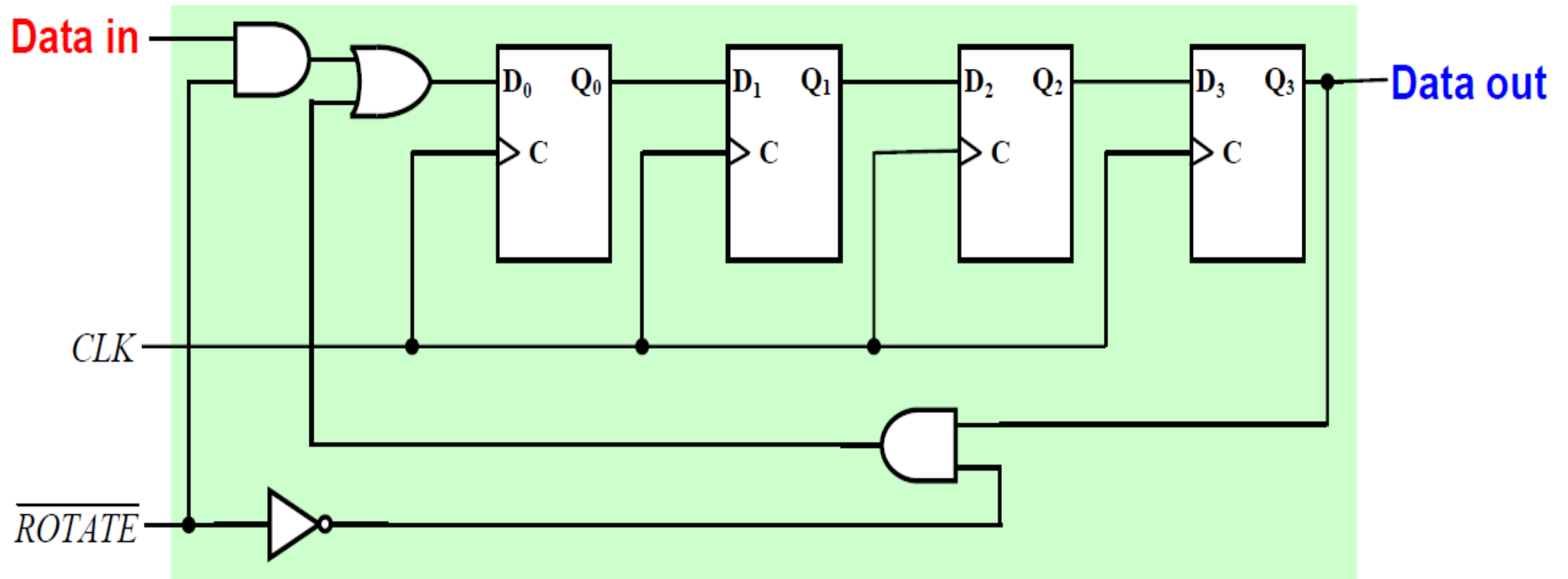
Clock	Q0	Q1	Q2	Q3	Data out
Initially	0	0	0	0	
1	0	0	0	0	0
2	1	0	0	0	0
3	0	1	0	0	0
4	1	0	1	0	0
5	0	1	0	1	0
6	0	0	1	0	1
7	0	0	0	1	0
8	0	0	0	0	1



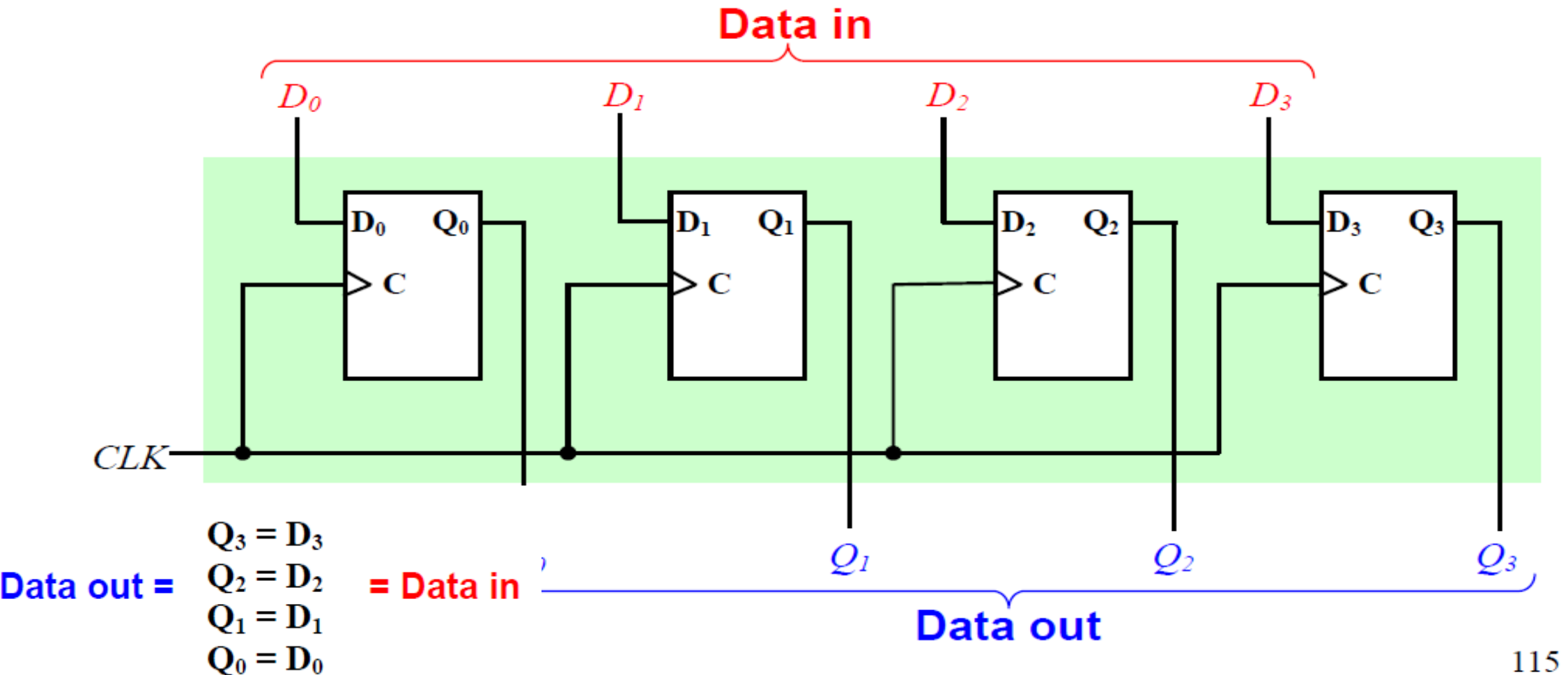
# Data rotation

Rotate\_b=1 data-in mode

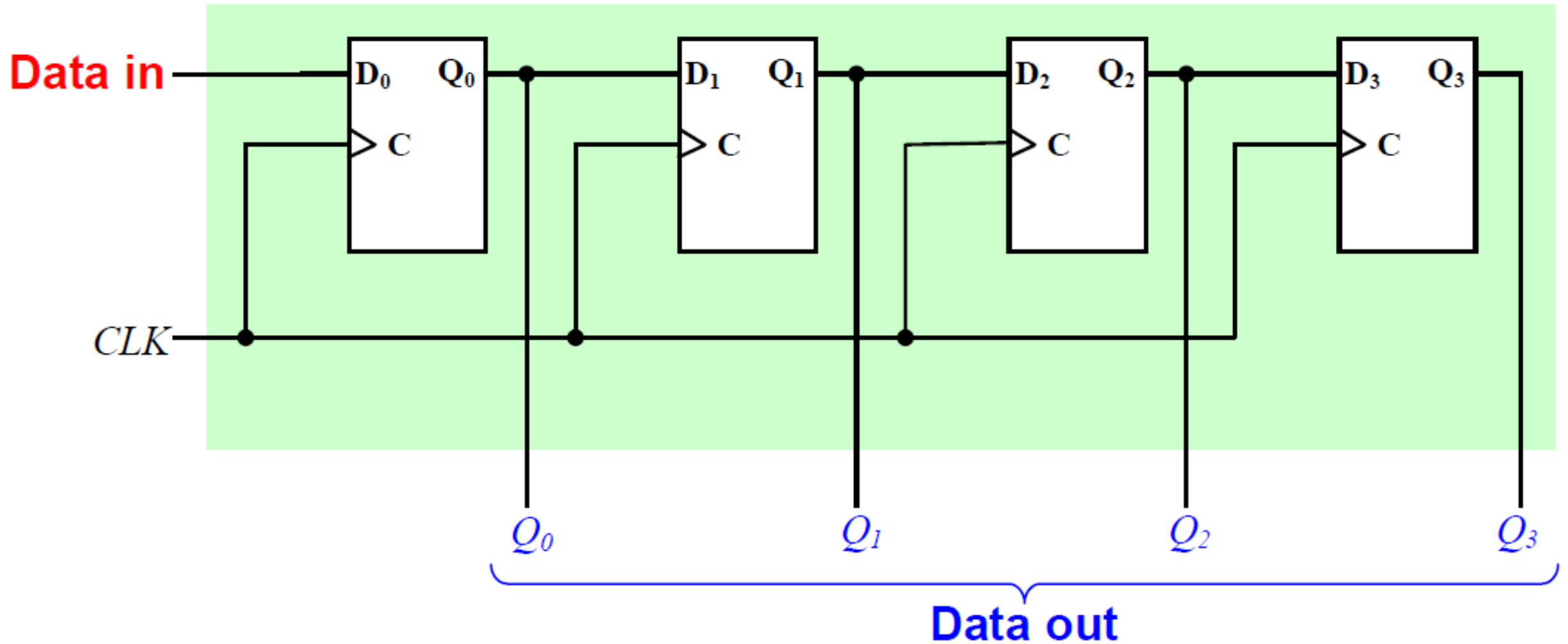
Rotate\_b=0 Store data rotation enable



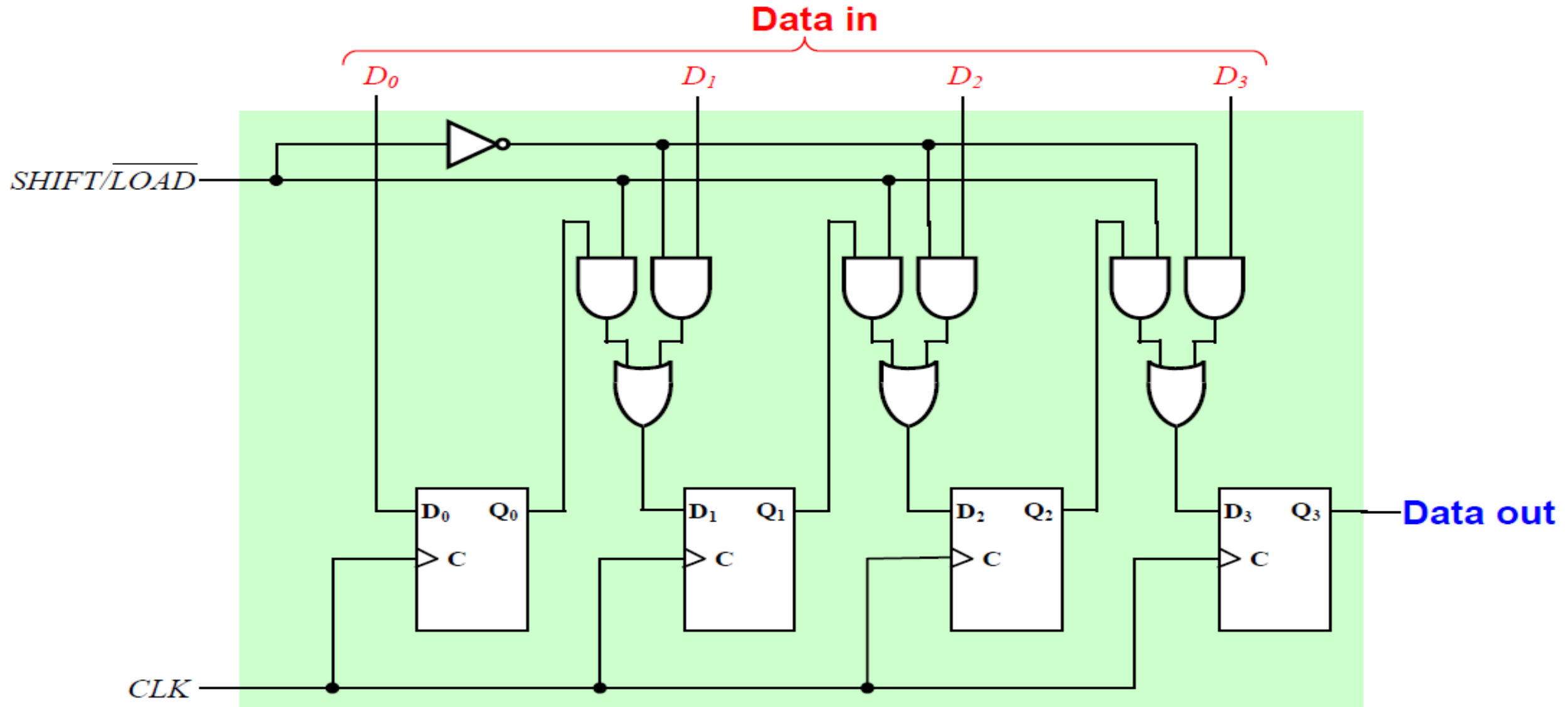
# Parallel IN Parallel out shift register



# Serial in Parallel Shift Register



# Parallel in Serial out Shift Register



# Home-Work

Universal Shift Register and its working

**END**

**NEXT Counter**