* On-chip networks are important to the understanding of future multi-core processors as different baseline.
* The tasks for NOC is to reduce the unfavorable features like price , energy usage and space occupied in return giving favorable features such as low latency and high bandwidth.
* They have depended on a router adopt from off-chip networks. The on-chip network are having complex designing which leads proper scalability .
* In this ,we discuss about NOC (Network of chips) architecture.
* It (NOC knowledge) helps bring alteration in chips to bring favorable features with trade offs
* Finding profitable routes in NOC by simulating varies paths of routing by gem5 simulator . The designs of paths are classified based on unique nature. The nature of path is defined by length of string connected and how there are connected. Even small impact sum up into large changes depending upon task processing with nature as great affect. The saving of time is benefit with cost effectively. Some time we can remove undesirable path is effective.
* The new chips should be better than pervious one. Intern the improvement one way or other but it should consistent . The noc routing is trustworthy with respect to combing different material in chip & end up unreliable. Hoping ,it can bring result which needed.