

Multicores		
Time	Speaker	Topic
8:45 - 9:00	Madhura Purnaprajna	Introductory remarks
9:00 -10:15	R. Govindarajan	Out of order processing
10:15-11:30	Biswa	Caches and virtual memory
Break		
12:00-13:15	Arkaprava Basu	Multicore architectures; Cache coherence
Lunch		
14:30-16:00	Biswa	Lab session (OOO + Cache performance)
16:00-16:30		Discussion & doubt clearing session

Memory Hierarchy and Security				
Time	Speaker	Topic		
9:00 -10:15	Vivek Sheshadri	DRAM architecture, related performance issues and optimizations		
10:15-11:30	Manu Awasthi	Non-volatile memories + IO		
Break				
12:00-13:15	Biswa	Microarchitecture security		
Lunch				
14:30-16:00	Manu Awasthi	Lab session on DRAM		
16:00-16:30		Discussion & doubt clearing session		

Hetrogeneous Architectures		
Time	Speaker	Topic
9:00 -10:15	Madhura Purnaprajna	Introduction to FPGA; Parallel computing using FPGA and optimizations
10:15-11:30	Vishwesh Jatala	GPUs, CUDA programming and architectural optimizations
Break		
12:00-13:15	Sharad Sinha	Hetrogeneous architectures (CPU + FPGA systems)
Lunch		
14:30-16:00	Vishwesh Jatala	Lab session (GPUs and CUDA)
16:00-16:30		Discussion & doubt clearing session

Performance Analysis		
Time	Speaker	Topic
9:00 -10:15	Subramaniam K V	Performance tools for CPUs
10:15-11:30	Sharad Sinha	Performance analysis (CPU+FPGA) and energy efficiency
Break		
12:00-13:15	Kanishka	Performance projections with simulators
Lunch		
14:30-16:00	Subramaniam K V Kanishka	Lab Session
Break		
16:15-17:15	All speakers	Panel Discussion (Future of Architecture/Career opportunity/Research trends)
17:15-17:30	Vishwesh Jatala	Vote of thanks