|  |  |  |  |  |  |  |  |  |  |
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|  |  | Mode2 | Mode3 | Mode4 | Mode5 | Mode6 | Mode7 | Mode8 | Mode9 |
| 1 | PA0 | UART0\_TX | I2C0\_SDA | TIMA0\_C0 | TIMA\_FAL1 | TIMG8\_C1 | FCC\_IN |  |  |
| 2 | PA1 | UART0\_RX | I2C0\_SCL | TIMA0\_C1 | TIMA\_FAL2 | TIMG8\_IDX | TIMG8\_C0 |  |  |
| 3 | PA28 | UART0\_TX | I2C0\_SDA | TIMA0\_C3 | TIMA\_FAL0 | TIMG7\_C0 | TIMA1\_C0 |  |  |
| 4 | PA29 | I2C1\_SCL | UART2\_RTS | TIMG8\_C0 | TIMG6\_C0 |  |  |  |  |
| 5 | PA30 | I2C1\_SDA | UART2\_CTS | TIMG8\_C1 | TIMG6\_C1 |  |  |  |  |
| 6 | PA31 | UART0\_RX | I2C0\_SCL | TIMA0\_C3N | TIMG12\_C1 | CLK\_OUT | TIMG7\_C1 | TIMA1\_C1 |  |
| 7 | PA2 | ROSCTIMG8\_C1 | SPI0\_CS0 | TIMG7\_C1 | SPI1\_CS0 |  |  |  |  |
| 8 | PA3 | LFXINTIMG8\_C0 | SPI0\_CS1 | UART2\_CTS | TIMA0\_C2 | COMP1\_OUT | TIMG7\_C0 | TIMA0\_C1 | I2C1\_SDA |
| 9 | PA4 | LFXOUTTIMG8\_C1 | SPI0\_POCI | UART2\_RTS | TIMA0\_C3 | LFCLK\_IN | TIMG7\_C1 | TIMA0\_C1N | I2C1\_SCL |
| 10 | PA5 | HFXINTIMG8\_C0 | SPI0\_PICO | TIMA\_FAL1 | TIMG0\_C0 | TIMG6\_C0 | FCC\_IN |  |  |
| 11 | PA6 | HFXOUTTIMG8\_C1 | SPI0\_SCK | TIMA\_FAL0 | TIMG0\_C1 | HFCLK\_IN | TIMG6\_C1 | TIMA0\_C2N |  |
| 12 | PB0 | UART0\_TX | SPI1\_CS2 | TIMA1\_C0 | TIMA0\_C2 |  |  |  |  |
| 13 | PB1 | UART0\_RX | SPI1\_CS3 | TIMA1\_C1 | TIMA0\_C2N |  |  |  |  |
| 14 | PA7 | COMP0\_OUT | CLK\_OUT | TIMG8\_C0 | TIMA0\_C2 | TIMG8\_IDX | TIMG7\_C1 | TIMA0\_C1 |  |
| 15 | PB2 | UART3\_TX | UART2\_CTS | I2C1\_SCL | TIMA0\_C3 | UART1\_CTS | TIMG6\_C0 | TIMA1\_C0 |  |
| 16 | PB3 | UART3\_RX | UART2\_RTS | I2C1\_SDA | TIMA0\_C3N | UART1\_RTS | TIMG6\_C1 | TIMA1\_C1 |  |
| 17 | PB4 | UART1\_TX | UART3\_CTS | TIMA1\_C0 | TIMA0\_C2 | TIMA1\_C0N |  |  |  |
| 18 | PB5 | UART1\_RX | UART3\_RTS | TIMA1\_C1 | TIMA0\_C2N | TIMA1\_C1N |  |  |  |
| 19 | PA8 | UART1\_TX | SPI0\_CS0 | UART0\_RTS | TIMA0\_C0 | TIMA1\_C0N |  |  |  |
| 20 | PA9 | UART1\_RX | SPI0\_PICO | UART0\_CTS | TIMA0\_C1 | RTC\_OUT | TIMA0\_C0N | TIMA1\_C1N | CLK\_OUT |
| 21 | PA10 | UART0\_TX | SPI0\_POCI | I2C0\_SDA | TIMA1\_C0 | TIMG12\_C0 | TIMA0\_C2 | I2C1\_SDA | CLK\_OUT |
| 22 | PA11 | UART0\_RX | SPI0\_SCK | I2C0\_SCL | TIMA1\_C1 | COMP0\_OUT | TIMA0\_C2N | I2C1\_SCL |  |
| 23 | PB6 | UART1\_TX | SPI1\_CS0 | SPI0\_CS1 | TIMG8\_C0 | UART2\_CTS | TIMG6\_C0 | TIMA1\_C0N |  |
| 24 | PB7 | UART1\_RX | SPI1\_POCI | SPI0\_CS2 | TIMG8\_C1 | UART2\_RTS | TIMG6\_C1 | TIMA1\_C1N |  |
| 25 | PB8 | UART1\_CTS | SPI1\_PICO | TIMA0\_C0 | COMP1\_OUT |  |  |  |  |
| 26 | PB9 | UART1\_RTS | SPI1\_SCK | TIMA0\_C1 | TIMA0\_C0N |  |  |  |  |
| 27 | PB10 | TIMG0\_C0 | TIMG8\_C0 | COMP1\_OUT | TIMG6\_C0 |  |  |  |  |
| 28 | PB11 | TIMG0\_C1 | TIMG8\_C1 | CLK\_OUT | TIMG6\_C1 |  |  |  |  |
| 29 | PB12 | UART3\_TX | TIMA0\_C2 | TIMA\_FAL1 | TIMA0\_C1 |  |  |  |  |
| 30 | PB13 | UART3\_RX | TIMA0\_C3 | TIMG12\_C0 | TIMA0\_C1N |  |  |  |  |
| 31 | PB14 | SPI1\_CS3 | SPI1\_POCI | SPI0\_CS3 | TIMG12\_C1 | TIMG8\_IDX | TIMA0\_C0 |  |  |
| 32 | PB15 | UART2\_TX | SPI1\_PICO | UART3\_CTS | TIMG8\_C0 | TIMG7\_C0 |  |  |  |
| 33 | PB16 | UART2\_RX | SPI1\_SCK | UART3\_RTS | TIMG8\_C1 | TIMG7\_C1 |  |  |  |
| 34 | PA12 | UART3\_CTS | SPI0\_SCK | TIMG0\_C0 | CAN\_TX | TIMA0\_C3 | FCC\_IN |  |  |
| 35 | PA13 | UART3\_RTS | SPI0\_POCI | UART3\_RX | TIMG0\_C1 | CAN\_RX | TIMA0\_C3N |  |  |
| 36 | PA14 | UART0\_CTS | SPI0\_PICO | UART3\_TX | TIMG12\_C0 | CLK\_OUT |  |  |  |
| 37 | PA15 | UART0\_RTS | SPI1\_CS2 | I2C1\_SCL | TIMA1\_C0 | TIMG8\_IDX | TIMA1\_C0N | TIMA0\_C2 |  |
| 38 | PA16 | COMP2\_OUT | SPI1\_POCI | I2C1\_SDA | TIMA1\_C1 | TIMA1\_C1N | TIMA0\_C2N | FCC\_IN |  |
| 39 | PA17 | UART1\_TX | SPI1\_SCK | I2C1\_SCL | TIMA0\_C3 | TIMG7\_C0 | TIMA1\_C0 |  |  |
| 40 | PA18 | UART1\_RX | SPI1\_PICO | I2C1\_SDA | TIMA0\_C3N | TIMG7\_C1 | TIMA1\_C1 |  |  |
| 41 | PA19 | SWDIO |  |  |  |  |  |  |  |
| 42 | PA20 | SWCLK |  |  |  |  |  |  |  |
| 43 | PB17 | UART2\_TX | SPI0\_PICO | SPI1\_CS1 | TIMA1\_C0 | TIMA0\_C2 |  |  |  |
| 44 | PB18 | UART2\_RX | SPI0\_SCK | SPI1\_CS2 | TIMA1\_C1 | TIMA0\_C2N |  |  |  |
| 45 | PB19 | COMP2\_OUT | SPI0\_POCI | TIMG8\_C1 | UART0\_CTS | TIMG7\_C1 |  |  |  |
| 46 | PA21 | UART2\_TX | TIMG8\_C0 | UART1\_CTS | TIMA0\_C0 | TIMG6\_C0 |  |  |  |
| 47 | PA22 | UART2\_RX | TIMG8\_C1 | UART1\_RTS | TIMA0\_C1 | CLK\_OUT | TIMA0\_C0N | TIMG6\_C1 |  |
| 48 | PB20 | SPI0\_CS2 | SPI1\_CS0 | TIMA0\_C2 | TIMG12\_C0 | TIMA\_FAL1 | TIMA0\_C1 | TIMA1\_C1N |  |
| 49 | PB21 | SPI1\_POCI | TIMG8\_C0 |  |  |  |  |  |  |
| 50 | PB22 | SPI1\_PICO | TIMG8\_C1 |  |  |  |  |  |  |
| 51 | PB23 | SPI1\_SCK | COMP0\_OUT | TIMA\_FAL0 |  |  |  |  |  |
| 52 | PB24 | SPI0\_CS3 | SPI0\_CS1 | TIMA0\_C3 | TIMG12\_C1 | TIMA0\_C1N | TIMA1\_C0N |  |  |
| 53 | PA23 | UART2\_TX | SPI0\_CS3 | TIMA0\_C3 | TIMG0\_C0 | UART3\_CTS | TIMG7\_C0 | TIMG8\_C0 |  |
| 54 | PA24 | UART2\_RX | SPI0\_CS2 | TIMA0\_C3N | TIMG0\_C1 | UART3\_RTS | TIMG7\_C1 | TIMA1\_C1 |  |
| 55 | PA25 | UART3\_RX | SPI1\_CS3 | TIMG12\_C1 | TIMA0\_C3 | TIMA0\_C1N |  |  |  |
| 56 | PB25 | UART0\_CTS | SPI0\_CS0 | TIMA\_FAL2 |  |  |  |  |  |
| 57 | PB26 | UART0\_RTS | SPI0\_CS1 | TIMA0\_C3 | TIMG6\_C0 | TIMA1\_C0 |  |  |  |
| 58 | PB27 | COMP2\_OUT | SPI1\_CS1 | TIMA0\_C3N | TIMG6\_C1 | TIMA1\_C1 |  |  |  |
| 59 | PA26 | UART3\_TX | SPI1\_CS0 | TIMG8\_C0 | TIMA\_FAL0 | CAN\_TX | TIMG7\_C0 |  |  |
| 60 | PA27 | RTC\_OUT | SPI1\_CS1 | TIMG8\_C1 | TIMA\_FAL2 | CAN\_RX | TIMG7\_C1 |  |  |

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|  |  | Mode2 | Mode3 | Mode4 | Mode5 | Mode6 | Mode7 | Mode8 | Mode9 |
| 1 | PA0 | UART0\_TX | I2C0\_SDA | TIMA0\_C0 | TIMA\_FAL1 | TIMG8\_C1 | FCC\_IN |  |  |
| 2 | PA1 | UART0\_RX | I2C0\_SCL | TIMA0\_C1 | TIMA\_FAL2 | TIMG8\_IDX | TIMG8\_C0 |  |  |
| 7 | PA2 | TIMG8\_C1 | SPI0\_CS0 | TIMG7\_C1 | SPI1\_CS0 |  |  |  |  |
| 8 | PA3 | TIMG8\_C0 | SPI0\_CS1 | UART2\_CTS | TIMA0\_C2 | COMP1\_OUT | TIMG7\_C0 | TIMA0\_C1 | I2C1\_SDA |
| 9 | PA4 | TIMG8\_C1 | SPI0\_POCI | UART2\_RTS | TIMA0\_C3 | LFCLK\_IN | TIMG7\_C1 | TIMA0\_C1N | I2C1\_SCL |
| 10 | PA5 | TIMG8\_C0 | SPI0\_PICO | TIMA\_FAL1 | TIMG0\_C0 | TIMG6\_C0 | FCC\_IN |  |  |
| 11 | PA6 | TIMG8\_C1 | SPI0\_SCK | TIMA\_FAL0 | TIMG0\_C1 | HFCLK\_IN | TIMG6\_C1 | TIMA0\_C2N |  |
| 14 | PA7 | COMP0\_OUT | CLK\_OUT | TIMG8\_C0 | TIMA0\_C2 | TIMG8\_IDX | TIMG7\_C1 | TIMA0\_C1 |  |
| 19 | PA8 | UART1\_TX | SPI0\_CS0 | UART0\_RTS | TIMA0\_C0 | TIMA1\_C0N |  |  |  |
| 20 | PA9 | UART1\_RX | SPI0\_PICO | UART0\_CTS | TIMA0\_C1 | RTC\_OUT | TIMA0\_C0N | TIMA1\_C1N | CLK\_OUT |
| 21 | PA10 | UART0\_TX | SPI0\_POCI | I2C0\_SDA | TIMA1\_C0 | TIMG12\_C0 | TIMA0\_C2 | I2C1\_SDA | CLK\_OUT |
| 22 | PA11 | UART0\_RX | SPI0\_SCK | I2C0\_SCL | TIMA1\_C1 | COMP0\_OUT | TIMA0\_C2N | I2C1\_SCL |  |
| 34 | PA12 | UART3\_CTS | SPI0\_SCK | TIMG0\_C0 | CAN\_TX | TIMA0\_C3 | FCC\_IN |  |  |
| 35 | PA13 | UART3\_RTS | SPI0\_POCI | UART3\_RX | TIMG0\_C1 | CAN\_RX | TIMA0\_C3N |  |  |
| 36 | PA14 | UART0\_CTS | SPI0\_PICO | UART3\_TX | TIMG12\_C0 | CLK\_OUT |  |  |  |
| 37 | PA15 | UART0\_RTS | SPI1\_CS2 | I2C1\_SCL | TIMA1\_C0 | TIMG8\_IDX | TIMA1\_C0N | TIMA0\_C2 |  |
| 38 | PA16 | COMP2\_OUT | SPI1\_POCI | I2C1\_SDA | TIMA1\_C1 | TIMA1\_C1N | TIMA0\_C2N | FCC\_IN |  |
| 39 | PA17 | UART1\_TX | SPI1\_SCK | I2C1\_SCL | TIMA0\_C3 | TIMG7\_C0 | TIMA1\_C0 |  |  |
| 40 | PA18 | UART1\_RX | SPI1\_PICO | I2C1\_SDA | TIMA0\_C3N | TIMG7\_C1 | TIMA1\_C1 |  |  |
| 41 | PA19 | SWDIO |  |  |  |  |  |  |  |
| 42 | PA20 | SWCLK |  |  |  |  |  |  |  |
| 46 | PA21 | UART2\_TX | TIMG8\_C0 | UART1\_CTS | TIMA0\_C0 | TIMG6\_C0 |  |  |  |
| 47 | PA22 | UART2\_RX | TIMG8\_C1 | UART1\_RTS | TIMA0\_C1 | CLK\_OUT | TIMA0\_C0N | TIMG6\_C1 |  |
| 53 | PA23 | UART2\_TX | SPI0\_CS3 | TIMA0\_C3 | TIMG0\_C0 | UART3\_CTS | TIMG7\_C0 | TIMG8\_C0 |  |
| 54 | PA24 | UART2\_RX | SPI0\_CS2 | TIMA0\_C3N | TIMG0\_C1 | UART3\_RTS | TIMG7\_C1 | TIMA1\_C1 |  |
| 55 | PA25 | UART3\_RX | SPI1\_CS3 | TIMG12\_C1 | TIMA0\_C3 | TIMA0\_C1N |  |  |  |
| 59 | PA26 | UART3\_TX | SPI1\_CS0 | TIMG8\_C0 | TIMA\_FAL0 | CAN\_TX | TIMG7\_C0 |  |  |
| 60 | PA27 | RTC\_OUT | SPI1\_CS1 | TIMG8\_C1 | TIMA\_FAL2 | CAN\_RX | TIMG7\_C1 |  |  |
| 3 | PA28 | UART0\_TX | I2C0\_SDA | TIMA0\_C3 | TIMA\_FAL0 | TIMG7\_C0 | TIMA1\_C0 |  |  |
| 4 | PA29 | I2C1\_SCL | UART2\_RTS | TIMG8\_C0 | TIMG6\_C0 |  |  |  |  |
| 5 | PA30 | I2C1\_SDA | UART2\_CTS | TIMG8\_C1 | TIMG6\_C1 |  |  |  |  |
| 6 | PA31 | UART0\_RX | I2C0\_SCL | TIMA0\_C3N | TIMG12\_C1 | CLK\_OUT | TIMG7\_C1 | TIMA1\_C1 |  |
| 12 | PB0 | UART0\_TX | SPI1\_CS2 | TIMA1\_C0 | TIMA0\_C2 |  |  |  |  |
| 13 | PB1 | UART0\_RX | SPI1\_CS3 | TIMA1\_C1 | TIMA0\_C2N |  |  |  |  |
| 15 | PB2 | UART3\_TX | UART2\_CTS | I2C1\_SCL | TIMA0\_C3 | UART1\_CTS | TIMG6\_C0 | TIMA1\_C0 |  |
| 16 | PB3 | UART3\_RX | UART2\_RTS | I2C1\_SDA | TIMA0\_C3N | UART1\_RTS | TIMG6\_C1 | TIMA1\_C1 |  |
| 17 | PB4 | UART1\_TX | UART3\_CTS | TIMA1\_C0 | TIMA0\_C2 | TIMA1\_C0N |  |  |  |
| 18 | PB5 | UART1\_RX | UART3\_RTS | TIMA1\_C1 | TIMA0\_C2N | TIMA1\_C1N |  |  |  |
| 23 | PB6 | UART1\_TX | SPI1\_CS0 | SPI0\_CS1 | TIMG8\_C0 | UART2\_CTS | TIMG6\_C0 | TIMA1\_C0N |  |
| 24 | PB7 | UART1\_RX | SPI1\_POCI | SPI0\_CS2 | TIMG8\_C1 | UART2\_RTS | TIMG6\_C1 | TIMA1\_C1N |  |
| 25 | PB8 | UART1\_CTS | SPI1\_PICO | TIMA0\_C0 | COMP1\_OUT |  |  |  |  |
| 26 | PB9 | UART1\_RTS | SPI1\_SCK | TIMA0\_C1 | TIMA0\_C0N |  |  |  |  |
| 27 | PB10 | TIMG0\_C0 | TIMG8\_C0 | COMP1\_OUT | TIMG6\_C0 |  |  |  |  |
| 28 | PB11 | TIMG0\_C1 | TIMG8\_C1 | CLK\_OUT | TIMG6\_C1 |  |  |  |  |
| 29 | PB12 | UART3\_TX | TIMA0\_C2 | TIMA\_FAL1 | TIMA0\_C1 |  |  |  |  |
| 30 | PB13 | UART3\_RX | TIMA0\_C3 | TIMG12\_C0 | TIMA0\_C1N |  |  |  |  |
| 31 | PB14 | SPI1\_CS3 | SPI1\_POCI | SPI0\_CS3 | TIMG12\_C1 | TIMG8\_IDX | TIMA0\_C0 |  |  |
| 32 | PB15 | UART2\_TX | SPI1\_PICO | UART3\_CTS | TIMG8\_C0 | TIMG7\_C0 |  |  |  |
| 33 | PB16 | UART2\_RX | SPI1\_SCK | UART3\_RTS | TIMG8\_C1 | TIMG7\_C1 |  |  |  |
| 43 | PB17 | UART2\_TX | SPI0\_PICO | SPI1\_CS1 | TIMA1\_C0 | TIMA0\_C2 |  |  |  |
| 44 | PB18 | UART2\_RX | SPI0\_SCK | SPI1\_CS2 | TIMA1\_C1 | TIMA0\_C2N |  |  |  |
| 45 | PB19 | COMP2\_OUT | SPI0\_POCI | TIMG8\_C1 | UART0\_CTS | TIMG7\_C1 |  |  |  |
| 48 | PB20 | SPI0\_CS2 | SPI1\_CS0 | TIMA0\_C2 | TIMG12\_C0 | TIMA\_FAL1 | TIMA0\_C1 | TIMA1\_C1N |  |
| 49 | PB21 | SPI1\_POCI | TIMG8\_C0 |  |  |  |  |  |  |
| 50 | PB22 | SPI1\_PICO | TIMG8\_C1 |  |  |  |  |  |  |
| 51 | PB23 | SPI1\_SCK | COMP0\_OUT | TIMA\_FAL0 |  |  |  |  |  |
| 52 | PB24 | SPI0\_CS3 | SPI0\_CS1 | TIMA0\_C3 | TIMG12\_C1 | TIMA0\_C1N | TIMA1\_C0N |  |  |
| 56 | PB25 | UART0\_CTS | SPI0\_CS0 | TIMA\_FAL2 |  |  |  |  |  |
| 57 | PB26 | UART0\_RTS | SPI0\_CS1 | TIMA0\_C3 | TIMG6\_C0 | TIMA1\_C0 |  |  |  |
| 58 | PB27 | COMP2\_OUT | SPI1\_CS1 | TIMA0\_C3N | TIMG6\_C1 | TIMA1\_C1 |  |  |  |

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|  |  | Mode2 | Mode3 | Mode4 | Mode5 | Mode6 | Mode7 | Mode8 |
| 1 | PA0 | UART0\_TX | I2C0\_SDA | TIMA0\_C0 | TIMA\_FAL1 | TIMG8\_C1 | FCC\_IN |  |
| 2 | PA1 | UART0\_RX | I2C0\_SCL | TIMA0\_C1 | TIMA\_FAL2 | TIMG8\_IDX | TIMG8\_C0 |  |
| 7 | PA2 | TIMG8\_C1 | SPI0\_CS0 | TIMG7\_C1 | SPI1\_CS0 |  |  |  |
| 14 | PA7 | COMP0\_OUT | CLK\_OUT | TIMG8\_C0 | TIMA0\_C2 | TIMG8\_IDX | TIMG7\_C1 | TIMA0\_C1 |
| 19 | PA8 | UART1\_TX | SPI0\_CS0 | UART0\_RTS | TIMA0\_C0 | TIMA1\_C0N |  |  |
| 20 | PA9 | UART1\_RX | SPI0\_PICO | UART0\_CTS | TIMA0\_C1 | RTC\_OUT | TIMA0\_C0N | TIMA1\_C1N |
| 21 | PA10 | UART0\_TX | SPI0\_POCI | I2C0\_SDA | TIMA1\_C0 | TIMG12\_C0 | TIMA0\_C2 | I2C1\_SDA |
| 22 | PA11 | UART0\_RX | SPI0\_SCK | I2C0\_SCL | TIMA1\_C1 | COMP0\_OUT | TIMA0\_C2N | I2C1\_SCL |
| 34 | PA12 | UART3\_CTS | SPI0\_SCK | TIMG0\_C0 | CAN\_TX | TIMA0\_C3 | FCC\_IN |  |
| 35 | PA13 | UART3\_RTS | SPI0\_POCI | UART3\_RX | TIMG0\_C1 | CAN\_RX | TIMA0\_C3N |  |
| 36 | PA14 | UART0\_CTS | SPI0\_PICO | UART3\_TX | TIMG12\_C0 | CLK\_OUT |  |  |
| 37 | PA15 | UART0\_RTS | SPI1\_CS2 | I2C1\_SCL | TIMA1\_C0 | TIMG8\_IDX | TIMA1\_C0N | TIMA0\_C2 |
| 38 | PA16 | COMP2\_OUT | SPI1\_POCI | I2C1\_SDA | TIMA1\_C1 | TIMA1\_C1N | TIMA0\_C2N | FCC\_IN |
| 39 | PA17 | UART1\_TX | SPI1\_SCK | I2C1\_SCL | TIMA0\_C3 | TIMG7\_C0 | TIMA1\_C0 |  |
| 40 | PA18 | UART1\_RX | SPI1\_PICO | I2C1\_SDA | TIMA0\_C3N | TIMG7\_C1 | TIMA1\_C1 |  |
| 46 | PA21 | UART2\_TX | TIMG8\_C0 | UART1\_CTS | TIMA0\_C0 | TIMG6\_C0 |  |  |
| 47 | PA22 | UART2\_RX | TIMG8\_C1 | UART1\_RTS | TIMA0\_C1 | CLK\_OUT | TIMA0\_C0N | TIMG6\_C1 |
| 53 | PA23 | UART2\_TX | SPI0\_CS3 | TIMA0\_C3 | TIMG0\_C0 | UART3\_CTS | TIMG7\_C0 | TIMG8\_C0 |
| 54 | PA24 | UART2\_RX | SPI0\_CS2 | TIMA0\_C3N | TIMG0\_C1 | UART3\_RTS | TIMG7\_C1 | TIMA1\_C1 |
| 55 | PA25 | UART3\_RX | SPI1\_CS3 | TIMG12\_C1 | TIMA0\_C3 | TIMA0\_C1N |  |  |
| 59 | PA26 | UART3\_TX | SPI1\_CS0 | TIMG8\_C0 | TIMA\_FAL0 | CAN\_TX | TIMG7\_C0 |  |
| 60 | PA27 | RTC\_OUT | SPI1\_CS1 | TIMG8\_C1 | TIMA\_FAL2 | CAN\_RX | TIMG7\_C1 |  |
| 3 | PA28 | UART0\_TX | I2C0\_SDA | TIMA0\_C3 | TIMA\_FAL0 | TIMG7\_C0 | TIMA1\_C0 |  |
| 4 | PA29 | I2C1\_SCL | UART2\_RTS | TIMG8\_C0 | TIMG6\_C0 |  |  |  |
| 5 | PA30 | I2C1\_SDA | UART2\_CTS | TIMG8\_C1 | TIMG6\_C1 |  |  |  |
| 6 | PA31 | UART0\_RX | I2C0\_SCL | TIMA0\_C3N | TIMG12\_C1 | CLK\_OUT | TIMG7\_C1 | TIMA1\_C1 |
| 12 | PB0 | UART0\_TX | SPI1\_CS2 | TIMA1\_C0 | TIMA0\_C2 |  |  |  |
| 13 | PB1 | UART0\_RX | SPI1\_CS3 | TIMA1\_C1 | TIMA0\_C2N |  |  |  |
| 15 | PB2 | UART3\_TX | UART2\_CTS | I2C1\_SCL | TIMA0\_C3 | UART1\_CTS | TIMG6\_C0 | TIMA1\_C0 |
| 16 | PB3 | UART3\_RX | UART2\_RTS | I2C1\_SDA | TIMA0\_C3N | UART1\_RTS | TIMG6\_C1 | TIMA1\_C1 |
| 17 | PB4 | UART1\_TX | UART3\_CTS | TIMA1\_C0 | TIMA0\_C2 | TIMA1\_C0N |  |  |
| 18 | PB5 | UART1\_RX | UART3\_RTS | TIMA1\_C1 | TIMA0\_C2N | TIMA1\_C1N |  |  |
| 23 | PB6 | UART1\_TX | SPI1\_CS0 | SPI0\_CS1 | TIMG8\_C0 | UART2\_CTS | TIMG6\_C0 | TIMA1\_C0N |
| 24 | PB7 | UART1\_RX | SPI1\_POCI | SPI0\_CS2 | TIMG8\_C1 | UART2\_RTS | TIMG6\_C1 | TIMA1\_C1N |
| 25 | PB8 | UART1\_CTS | SPI1\_PICO | TIMA0\_C0 | COMP1\_OUT |  |  |  |
| 26 | PB9 | UART1\_RTS | SPI1\_SCK | TIMA0\_C1 | TIMA0\_C0N |  |  |  |
| 27 | PB10 | TIMG0\_C0 | TIMG8\_C0 | COMP1\_OUT | TIMG6\_C0 |  |  |  |
| 28 | PB11 | TIMG0\_C1 | TIMG8\_C1 | CLK\_OUT | TIMG6\_C1 |  |  |  |
| 29 | PB12 | UART3\_TX | TIMA0\_C2 | TIMA\_FAL1 | TIMA0\_C1 |  |  |  |
| 30 | PB13 | UART3\_RX | TIMA0\_C3 | TIMG12\_C0 | TIMA0\_C1N |  |  |  |
| 31 | PB14 | SPI1\_CS3 | SPI1\_POCI | SPI0\_CS3 | TIMG12\_C1 | TIMG8\_IDX | TIMA0\_C0 |  |
| 32 | PB15 | UART2\_TX | SPI1\_PICO | UART3\_CTS | TIMG8\_C0 | TIMG7\_C0 |  |  |
| 33 | PB16 | UART2\_RX | SPI1\_SCK | UART3\_RTS | TIMG8\_C1 | TIMG7\_C1 |  |  |
| 43 | PB17 | UART2\_TX | SPI0\_PICO | SPI1\_CS1 | TIMA1\_C0 | TIMA0\_C2 |  |  |
| 44 | PB18 | UART2\_RX | SPI0\_SCK | SPI1\_CS2 | TIMA1\_C1 | TIMA0\_C2N |  |  |
| 45 | PB19 | COMP2\_OUT | SPI0\_POCI | TIMG8\_C1 | UART0\_CTS | TIMG7\_C1 |  |  |
| 48 | PB20 | SPI0\_CS2 | SPI1\_CS0 | TIMA0\_C2 | TIMG12\_C0 | TIMA\_FAL1 | TIMA0\_C1 | TIMA1\_C1N |
| 49 | PB21 | SPI1\_POCI | TIMG8\_C0 |  |  |  |  |  |
| 50 | PB22 | SPI1\_PICO | TIMG8\_C1 |  |  |  |  |  |
| 51 | PB23 | SPI1\_SCK | COMP0\_OUT | TIMA\_FAL0 |  |  |  |  |
| 52 | PB24 | SPI0\_CS3 | SPI0\_CS1 | TIMA0\_C3 | TIMG12\_C1 | TIMA0\_C1N | TIMA1\_C0N |  |
| 56 | PB25 | UART0\_CTS | SPI0\_CS0 | TIMA\_FAL2 |  |  |  |  |
| 57 | PB26 | UART0\_RTS | SPI0\_CS1 | TIMA0\_C3 | TIMG6\_C0 | TIMA1\_C0 |  |  |
| 58 | PB27 | COMP2\_OUT | SPI1\_CS1 | TIMA0\_C3N | TIMG6\_C1 | TIMA1\_C1 |  |  |

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  | Mode2 | Mode3 | Mode4 | Mode5 | Mode6 | Mode7 | Mode8 |
| 1 | PA0 | U0\_TX | I0\_SDA | TA0\_C0 | TA\_FAL1 | TG8\_C1 | FCC\_IN |  |
| 2 | PA1 | U0\_RX | I0\_SCL | TA0\_C1 | TA\_FAL2 | TG8\_IDX | TG8\_C0 |  |
| 7 | PA2 | TG8\_C1 | SP0\_CSP0 | TG7\_C1 | SP1\_CSP0 |  |  |  |
| 14 | PA7 | C0\_OUT | CK\_OUT | TG8\_C0 | TA0\_C2 | TG8\_IDX | TG7\_C1 | TA0\_C1 |
| 19 | PA8 | U1\_TX | SP0\_CSP0 | U0\_RTS | TA0\_C0 | TA1\_C0N |  |  |
| 20 | PA9 | U1\_RX | SP0\_PICO | U0\_CTS | TA0\_C1 | R\_OUT | TA0\_C0N | TA1\_C1N |
| 21 | PA10 | U0\_TX | SP0\_POCI | I0\_SDA | TA1\_C0 | TG12\_C0 | TA0\_C2 | I1\_SDA |
| 22 | PA11 | U0\_RX | SP0\_SCK | I0\_SCL | TA1\_C1 | C0\_OUT | TA0\_C2N | I1\_SCL |
| 34 | PA12 | U3\_CTS | SP0\_SCK | TG0\_C0 | CAN\_TX | TA0\_C3 | FCC\_IN |  |
| 35 | PA13 | U3\_RTS | SP0\_POCI | U3\_RX | TG0\_C1 | CAN\_RX | TA0\_C3N |  |
| 36 | PA14 | U0\_CTS | SP0\_PICO | U3\_TX | TG12\_C0 | CK\_OUT |  |  |
| 37 | PA15 | U0\_RTS | SP1\_CS2 | I1\_SCL | TA1\_C0 | TG8\_IDX | TA1\_C0N | TA0\_C2 |
| 38 | PA16 | C2\_OUT | SP1\_POCI | I1\_SDA | TA1\_C1 | TA1\_C1N | TA0\_C2N | FCC\_IN |
| 39 | PA17 | U1\_TX | SP1\_SCK | I1\_SCL | TA0\_C3 | TG7\_C0 | TA1\_C0 |  |
| 40 | PA18 | U1\_RX | SP1\_PICO | I1\_SDA | TA0\_C3N | TG7\_C1 | TA1\_C1 |  |
| 46 | PA21 | U2\_TX | TG8\_C0 | U1\_CTS | TA0\_C0 | TG6\_C0 |  |  |
| 47 | PA22 | U2\_RX | TG8\_C1 | U1\_RTS | TA0\_C1 | CK\_OUT | TA0\_C0N | TG6\_C1 |
| 53 | PA23 | U2\_TX | SP0\_CS3 | TA0\_C3 | TG0\_C0 | U3\_CTS | TG7\_C0 | TG8\_C0 |
| 54 | PA24 | U2\_RX | SP0\_CS2 | TA0\_C3N | TG0\_C1 | U3\_RTS | TG7\_C1 | TA1\_C1 |
| 55 | PA25 | U3\_RX | SP1\_CS3 | TG12\_C1 | TA0\_C3 | TA0\_C1N |  |  |
| 59 | PA26 | U3\_TX | SP1\_CSP0 | TG8\_C0 | TA\_FAL0 | CAN\_TX | TG7\_C0 |  |
| 60 | PA27 | RTC\_OUT | SP1\_CSP1 | TG8\_C1 | TA\_FAL2 | CAN\_RX | TG7\_C1 |  |
| 3 | PA28 | U0\_TX | I0\_SDA | TA0\_C3 | TA\_FAL0 | TG7\_C0 | TA1\_C0 |  |
| 4 | PA29 | I1\_SCL | U2\_RTS | TG8\_C0 | TG6\_C0 |  |  |  |
| 5 | PA30 | I1\_SDA | U2\_CTS | TG8\_C1 | TG6\_C1 |  |  |  |
| 6 | PA31 | U0\_RX | I0\_SCL | TA0\_C3N | TG12\_C1 | CK\_OUT | TG7\_C1 | TA1\_C1 |
| 12 | PB0 | U0\_TX | SP1\_CS2 | TA1\_C0 | TA0\_C2 |  |  |  |
| 13 | PB1 | U0\_RX | SP1\_CS3 | TA1\_C1 | TA0\_C2N |  |  |  |
| 15 | PB2 | U3\_TX | U2\_CTS | I1\_SCL | TA0\_C3 | U1\_CTS | TG6\_C0 | TA1\_C0 |
| 16 | PB3 | U3\_RX | U2\_RTS | I1\_SDA | TA0\_C3N | U1\_RTS | TG6\_C1 | TA1\_C1 |
| 17 | PB4 | U1\_TX | U3\_CTS | TA1\_C0 | TA0\_C2 | TA1\_C0N |  |  |
| 18 | PB5 | U1\_RX | U3\_RTS | TA1\_C1 | TA0\_C2N | TA1\_C1N |  |  |
| 23 | PB6 | U1\_TX | SP1\_CSP0 | SP0\_CSP1 | TG8\_C0 | U2\_CTS | TG6\_C0 | TA1\_C0N |
| 24 | PB7 | U1\_RX | SP1\_POCI | SP0\_CS2 | TG8\_C1 | U2\_RTS | TG6\_C1 | TA1\_C1N |
| 25 | PB8 | U1\_CTS | SP1\_PICO | TA0\_C0 | C1\_OUT |  |  |  |
| 26 | PB9 | U1\_RTS | SP1\_SCK | TA0\_C1 | TA0\_C0N |  |  |  |
| 27 | PB10 | TG0\_C0 | TG8\_C0 | C1\_OUT | TG6\_C0 |  |  |  |
| 28 | PB11 | TG0\_C1 | TG8\_C1 | CK\_OUT | TG6\_C1 |  |  |  |
| 29 | PB12 | U3\_TX | TA0\_C2 | TA\_FAL1 | TA0\_C1 |  |  |  |
| 30 | PB13 | U3\_RX | TA0\_C3 | TG12\_C0 | TA0\_C1N |  |  |  |
| 31 | PB14 | SP1\_CS3 | SP1\_POCI | SP0\_CS3 | TG12\_C1 | TG8\_IDX | TA0\_C0 |  |
| 32 | PB15 | U2\_TX | SP1\_PICO | U3\_CTS | TG8\_C0 | TG7\_C0 |  |  |
| 33 | PB16 | U2\_RX | SP1\_SCK | U3\_RTS | TG8\_C1 | TG7\_C1 |  |  |
| 43 | PB17 | U2\_TX | SP0\_PICO | SP1\_CSP1 | TA1\_C0 | TA0\_C2 |  |  |
| 44 | PB18 | U2\_RX | SP0\_SCK | SP1\_CS2 | TA1\_C1 | TA0\_C2N |  |  |
| 45 | PB19 | C2\_OUT | SP0\_POCI | TG8\_C1 | U0\_CTS | TG7\_C1 |  |  |
| 48 | PB20 | SP0\_CS2 | SP1\_CSP0 | TA0\_C2 | TG12\_C0 | TA\_FAL1 | TA0\_C1 | TA1\_C1N |
| 49 | PB21 | SP1\_POCI | TG8\_C0 |  |  |  |  |  |
| 50 | PB22 | SP1\_PICO | TG8\_C1 |  |  |  |  |  |
| 51 | PB23 | SP1\_SCK | C0\_OUT | TA\_FAL0 |  |  |  |  |
| 52 | PB24 | SP0\_CS3 | SP0\_CSP1 | TA0\_C3 | TG12\_C1 | TA0\_C1N | TA1\_C0N |  |
| 56 | PB25 | U0\_CTS | SP0\_CSP0 | TA\_FAL2 |  |  |  |  |
| 57 | PB26 | U0\_RTS | SP0\_CSP1 | TA0\_C3 | TG6\_C0 | TA1\_C0 |  |  |
| 58 | PB27 | C2\_OUT | SP1\_CSP1 | TA0\_C3N | TG6\_C1 | TA1\_C1 |  |  |