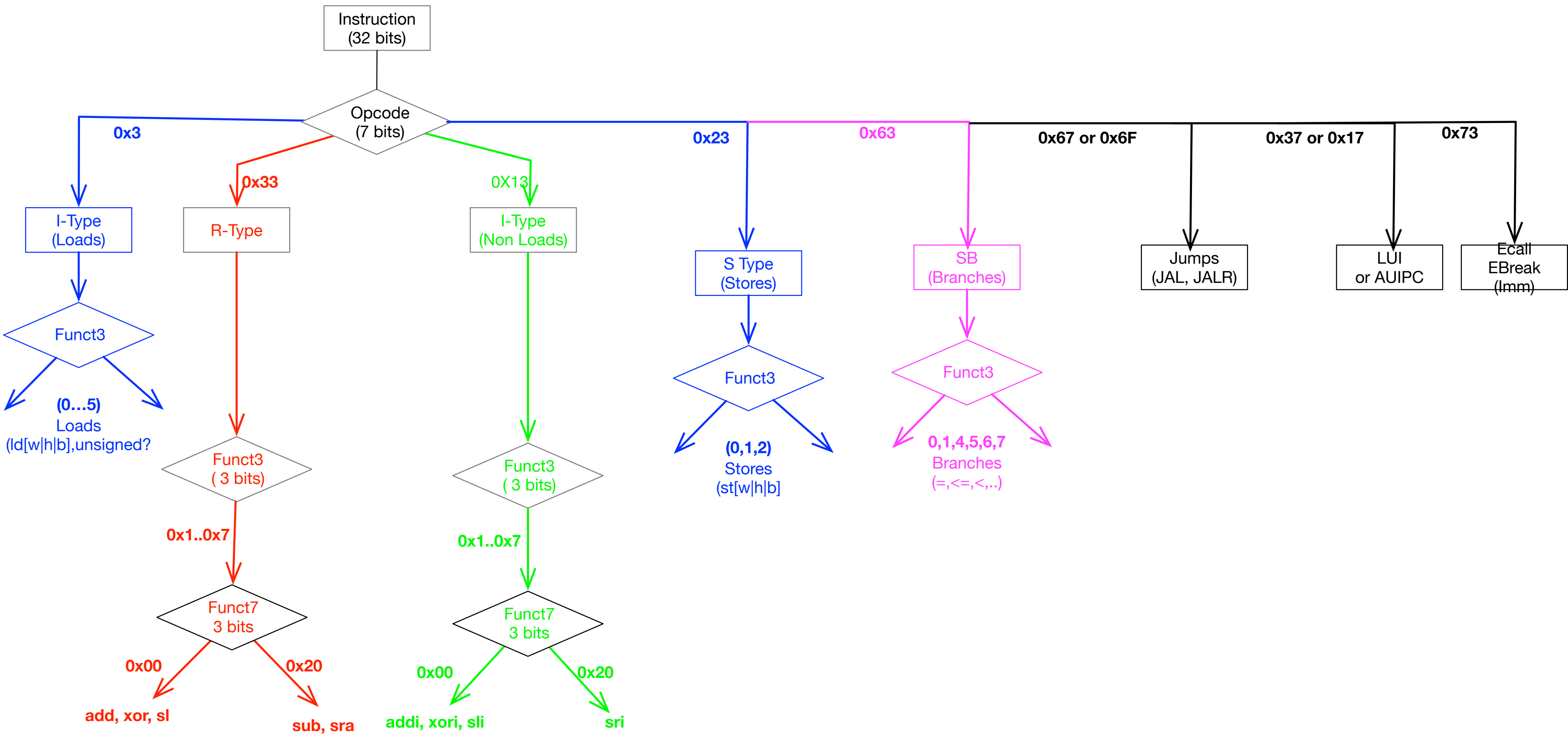


RISC V Disassembly Flowchart



* No subi. Can be implemented using addi with -ve

* Don't forget about immediate value embedded in instruction