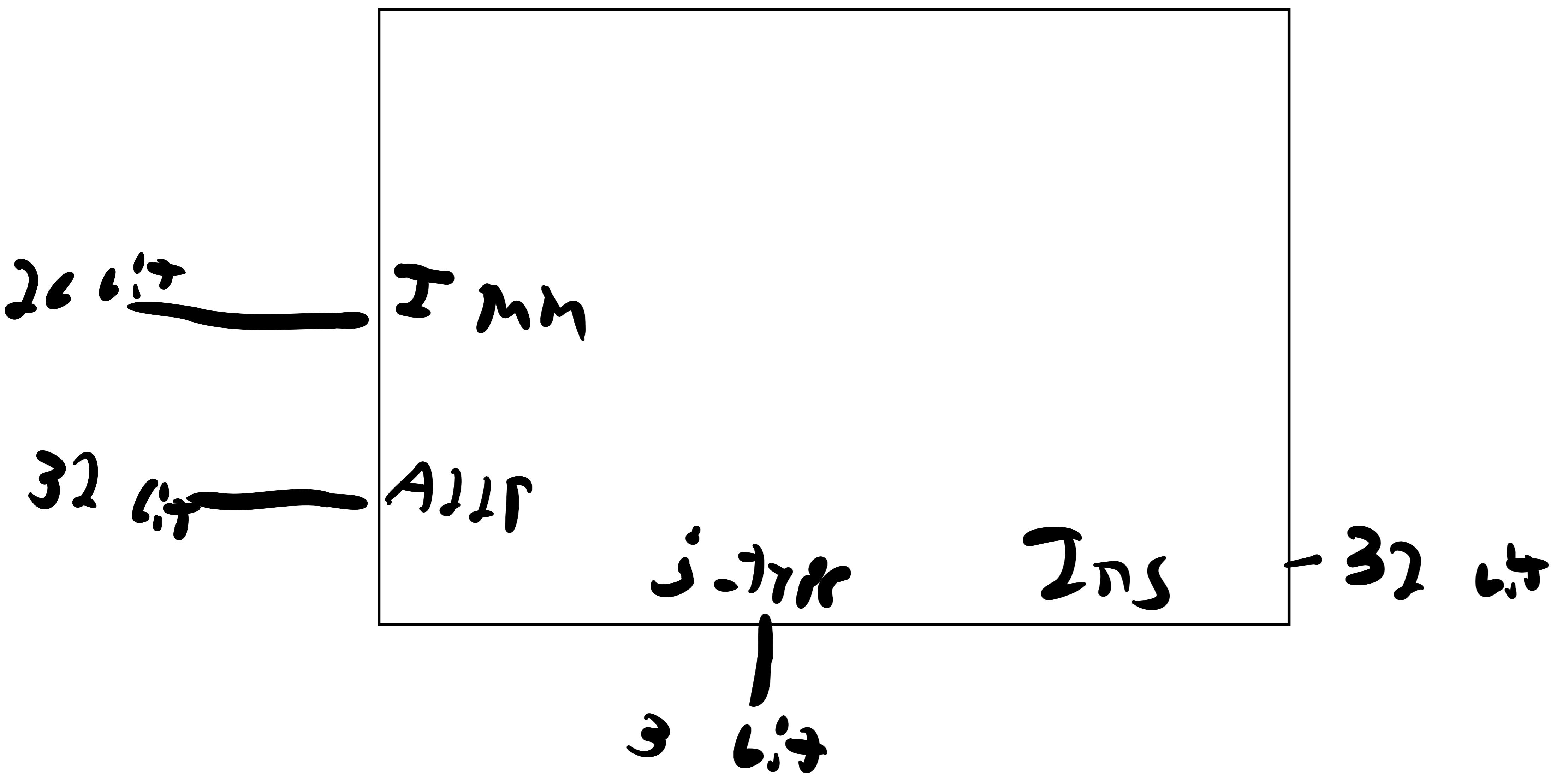


Fetch design

Functionality: Calculate and move to next instruction


based on current instruction

Interface symbol



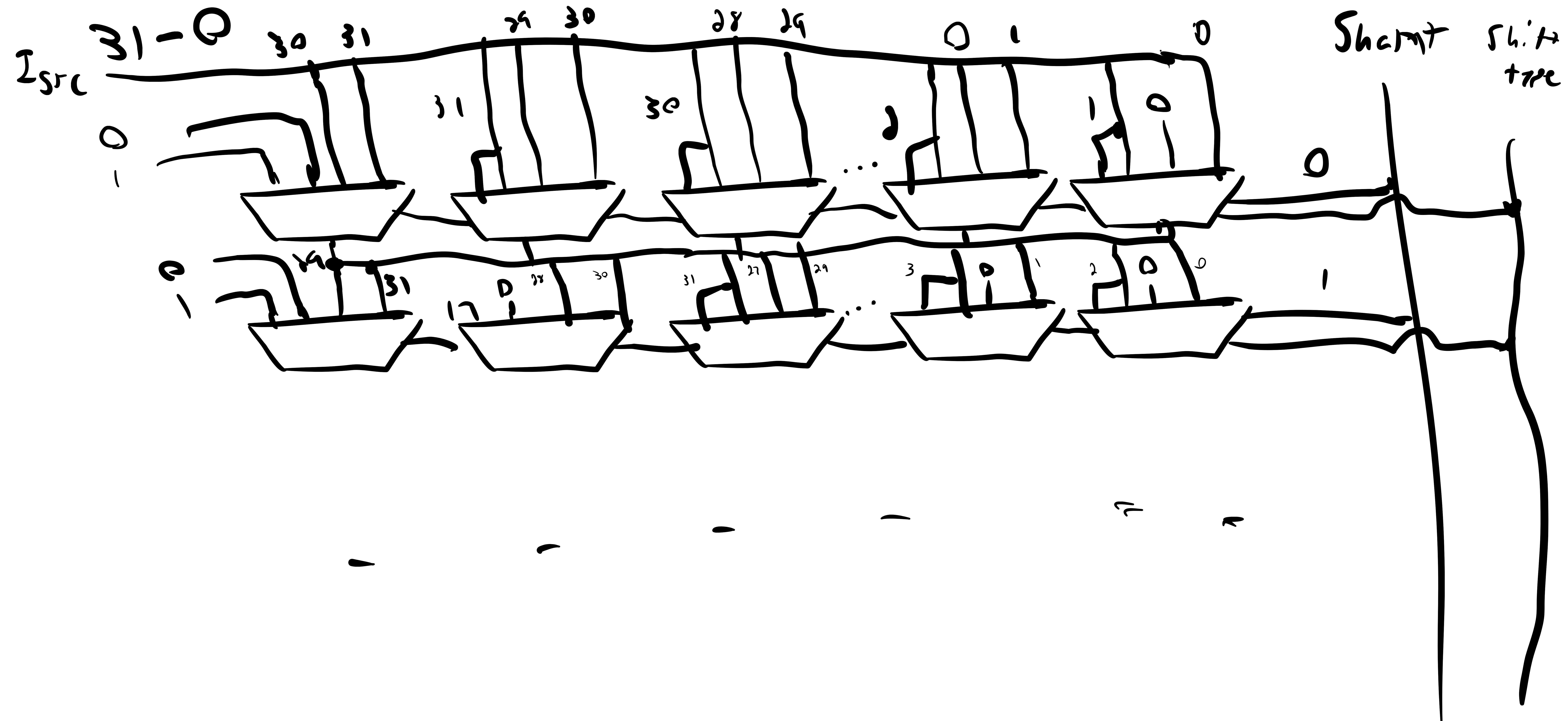
j-type	lookup
Signal	Operation
XXX0	$PC = PC + 4$
X01	$PC = \text{input Address}$
011	$PC = \text{branch Address}$
111	$PC = R[\text{RS}]$

Fetch Schematic



De coffee design

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Sub Components

32 bit Register

