

AACHI SRIRAJ AYENGAR

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Education

SR University, Telangana, India

Bachelor of Technology in Electronics and Communication Engineering

Aug 2021 – Aug 2025

CGPA: 8.36

SR College For Boys, Hanamkonda

MPC Intermediate

Jul 2019 – Jul 2021

Percentage: 92.2%

SR National High School, Hanamkonda

Secondary School

Jun 2018 – Jun 2019

GPA: 9.5

Projects

Diary Web Application using React, HTML, and CSS

- Developed a responsive diary application enabling users to create, edit, and delete entries, applying React Hooks for state management and React Router for seamless navigation.

Internships

PSPTECHHUB

Aug 2024 – Nov 2024

Software Developer Intern

- Simulated industrial monitoring with real-time Grafana dashboards using Python for data generation.
- Automated data flow from KepServerEX to InfluxDB using Telegraf.
- Designed interactive dashboards with Ignition Edge and Edge Historian, incorporating Python for data simulation.
- Connected KepServerEX to Ignition Edge using the OPC UA protocol.
- Gained some basic knowledge on handling JSON data.

Skills

Technical Skills:

- **Programming:** Proficient in Python with experience using the Flask framework for building web applications, and JavaScript for client-side scripting.
- **Web Development:** Skilled in HTML, CSS, and React, with experience developing responsive applications like a Diary App using React Hooks and Router for interactive state handling and navigation.
- **Computer Science Fundamentals:** Strong understanding of core computer science principles, including data structures and algorithms.
- **Database Management Software:** Worked with InfluxDB for managing and querying time-series data in real-time industrial monitoring systems, and have Knowledge of SQL and its application in data querying, manipulation, and integration within software and network-based systems.
- **IOT Tools:** Hands-on experience with Grafana, Telegraf, KepServerEX, and Ignition Edge for data collection, visualization, and OPC UA communication.
- **Source Code Management System:** Experienced with Git for source control and collaborative development.

Hackathons

Circuit Avishkar 2024

- Built a CapSense-based solution in a 24-hour hackathon hosted by Infineon Technologies & IEEE CAS.
- Debugged Python code and collaborated on rapid prototyping with teammates.