

DISTRIBUTED REAL-TIME SYSTEMS

1. What is a Real-Time System?
2. Distributed Real Time Systems
3. Predictability of Real-Time Systems
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5. Static and Dynamic Scheduling
6. Clock Synchronization
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What is a Real-Time System?

☞ A real-time system is a computer system in which the correctness of the system behavior depends not only on the logical results of the computations but also on the time when the results are produced.

☞ Real-time systems usually are in strong interaction with their physical environment. They receive data, process it, and return results *in right time*.

Examples:

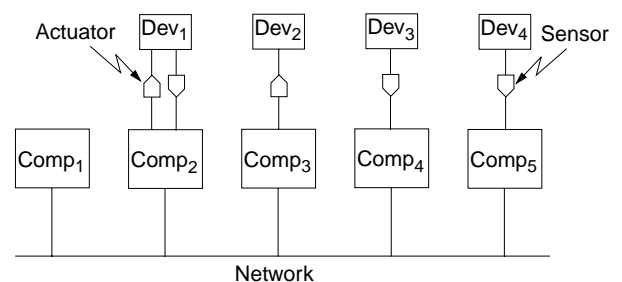
- Process control systems
- Computer-integrated manufacturing systems
- Aerospace and avionics systems
- Automotive electronics
- Medical equipment
- Nuclear power plant control
- Defence systems
- Consumer electronics
- Multimedia
- Telecommunications

Distributed Real-Time Systems

Real-time systems very often are implemented as distributed systems. Some reasons:

- Fault tolerance
- Certain processing of data has to be performed at the location of the sensors and actuators.
- Performance issues.

Distributed Real-Time Systems (cont'd)



Real -Time Systems

Some Typical Features

- They are time - critical.
The failure to meet time constraints can lead to degradation of the service or to catastrophe.
- They are made up of concurrent processes (tasks).
- The processes share resources (e.g. processor) and communicate to each other.
This makes scheduling of processes a central problem.
- Reliability and fault tolerance are essential.
Many applications are safety critical.
- Very often, such systems are dedicated to a specific functionality.
They perform a certain specific job which is fixed.
This is opposed to standard, general purpose computing, where several different programs are run on a computer to provide different services.
- Such systems are very often embedded in a larger system, like a car, CD-player, phone, camera, etc.

Soft and Hard Deadlines

☞ Time constraints are often expressed as deadlines at which processes have to complete their execution.

A deadline imposed on a process can be:

- Hard deadline: has to be met strictly, if not ⇒ "catastrophe".
- should be guaranteed a-priori, off-line.
- Soft deadlines: processes can be finished after its deadline, although the value provided by completion may degrade with time.
- Firm deadlines: similar to hard deadlines, but if the deadline is missed there is no catastrophe, only the result produced is of no use any more.

Predictability

- ☞ Predictability is one of the most important properties of any real-time system.
- ☞ Predictability means that it is possible to guarantee that deadlines are met as imposed by requirements:
- hard deadlines are always fulfilled.
 - soft deadlines are fulfilled to a degree which is sufficient for the imposed quality of service.

Some problems concerning predictability:

- Determine worst case execution times for each process.
- Determine worst case communication delays on the interconnection network.
- Determine bound on clock *drift* and *skew* (see later).
- Determine time overheads due to operating system (interrupt handling, process management, context switch, etc.).
- After all the problems above have been solved, comes the "big question":
Can the given processes and their related communications be scheduled on the available resources (processors, buses), so that deadlines are fulfilled?

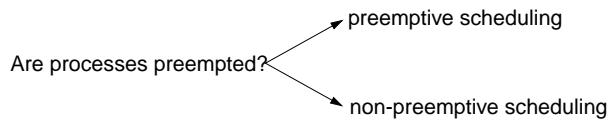
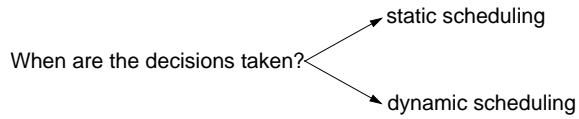
Scheduling

The scheduling problem:

Which process and communication has to be executed at a certain moment on a given processor or bus respectively, so that time constraints are fulfilled?

- ☞ A set of processes is *schedulable* if, given a certain scheduling policy, all constraints will be completed (if a solution to the scheduling problem can be found).

Scheduling Policies

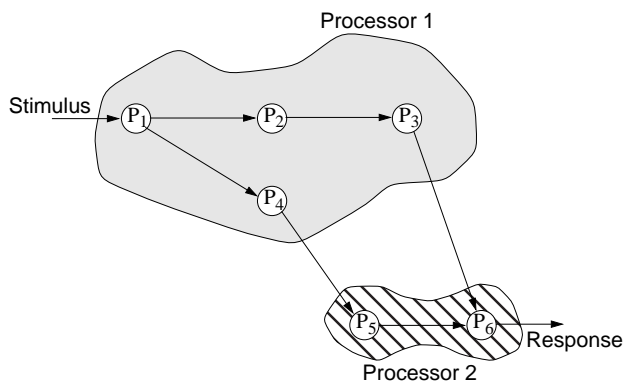


Scheduling Policies (cont'd)

Static scheduling: decisions are taken off-line.

- A table containing activation times of processes and communications is generated off line; this table is used at run-time by a very simple kernel.

Static Scheduling



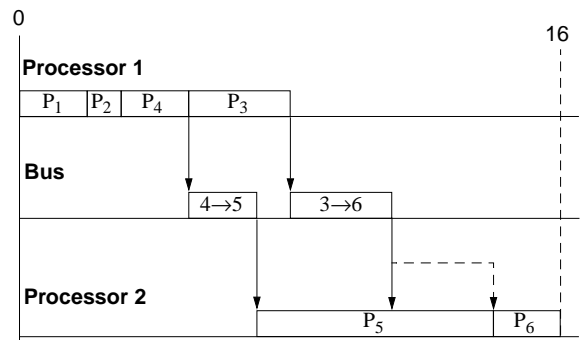
Deadline on response: 15

Worst case execution times:

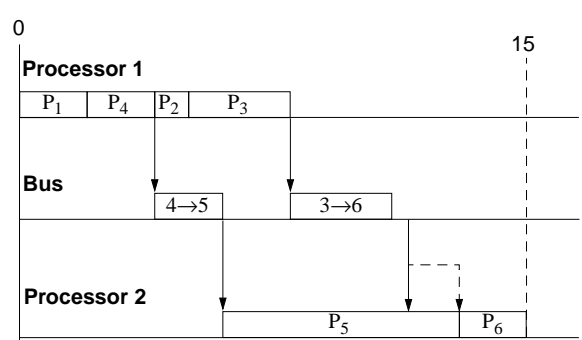
P ₁	2
P ₂	1
P ₃	3
P ₄	2
P ₅	7
P ₆	2
4→5	2
3→6	3

Static Scheduling (cont'd)

A first alternative



A second alternative



Static Scheduling (cont'd)

With the second alternative, the deadline is fulfilled.

Schedule table corresponding to second alternative:

Processor 1

0	P ₁
2	P ₄
4	P ₂
4	write on bus message 4→5
5	P ₃
8	write on bus message 3→6

Processor 2

6	read from bus message 4→5
6	P ₅
11	read from bus message 3→6
13	P ₆

We assume that the processors are able to process I/O and programs in parallel (there is an I/O coprocessor).

Static Scheduling (cont'd)

What is good?

- High predictability. Deadlines can be guaranteed - if the scheduling algorithm succeeded in building the schedule table.
- Easier to debug.
- Low execution time overhead.

What is bad?

- Assumes prior knowledge of processes (communications) and their characteristics.
- Not flexible (it works well only as long as processes/communications strictly behave as predicted).

Dynamic Scheduling

☞ No schedule (predetermined activation times) is generated off-line.

☞ Will the processes meet their deadlines?

This question can be answered only in very particular situations of dynamic scheduling!
Schedulability analysis tries to answer it.

Dynamic Scheduling (cont'd)

- Processes are activated as response to events (e.g. arrival of a signal, message, etc.).
- Processes have associated priorities. If several processes are ready to be activated on a processor, the highest priority process will be executed.

Priority based *preemptive* scheduling:

- At any given time the highest priority ready process is running.
 If a process becomes ready to be executed (the respective event has occurred), and it has a higher priority than the running process, the running process will be preempted and the new one will execute.

Dynamic Scheduling (cont'd)

☞ With certain restrictions in the process model, schedulability analyses can be performed:

For example:

- One single processor.
- All the n processes are periodic and have a fixed (worst case) computation time c_i and period T_i .
- All processes have a deadline equal to their period.
- All processes are independent (no resources shared and no precedence).
- Priorities are assigned to processes according to their period \Rightarrow the process with shorter period gets the higher priority.

Under the circumstances above, known as *rate monotonic scheduling*, all processes will meet their deadline if the following is true:

$$\sum_{i=1}^n \frac{c_i}{T_i} \leq n \left(2^{\frac{1}{n}} - 1 \right)$$

Dynamic Scheduling (cont'd)

As result of research in the area of real-time systems, schedulability tests have been developed for situations in which some of the restrictions above are relaxed:

- Several processors.
- Precedence constraints and communications between processes.
- Processes are sharing resources.
- Deadlines can be different from period.

☞ All schedulability tests require worst case execution times for processes and communications to be known!

Specific Issues Concerning Distributed Real-Time Systems

1. Clock synchronization
2. Real-Time Communication

Clock Synchronization

The need for synchronized distributed clocks:

- *Time driven systems*: in statically scheduled systems activities are started at "precise" times in different points of the distributed system.
- *Time stamps*: certain events or messages are associated with a time stamp showing the actual time when they have been produced; certain decisions in the system are based on the "exact" time of the event.
- *Calculating the duration of activities*: if such an activity starts on one processor and finishes on another (e.g. transmitting a message), calculating the duration needs clocks to be synchronized.

Computer Clocks

- A *quartz crystal* oscillates at well defined frequency and oscillations are counted (by hardware) in a register.
- After a certain number of oscillations, an interrupt is generated; this is the *clock tick*.
- At each clock tick, the *computer clock* is incremented by software.

Computer Clocks (cont'd)

The problems:

1. Crystals cannot be tuned perfectly. Temperature and other external factors can also influence their frequency.



Clock drift: the computer clock differs from the real time.

2. Two crystals are never identical.



Clock skew: the computer clocks on different processors of the distributed system show different time.

"Universal" Time

- The standard for measurement of time intervals: *International Atomic Time (TAI)*. It defines the *standard second* and is based on atomic oscillators.
- *Coordinated Universal Time (UTC)*: is based on TAI, but is kept in step with astronomical time (by occasionally inserting or deleting a "leap second").
- UTC signals are broadcast from satellites and land-based radio stations.

External and Internal Synchronization

External Synchronization

Synchronization with a time source external to the distributed systems, such as UTC broadcasting system.

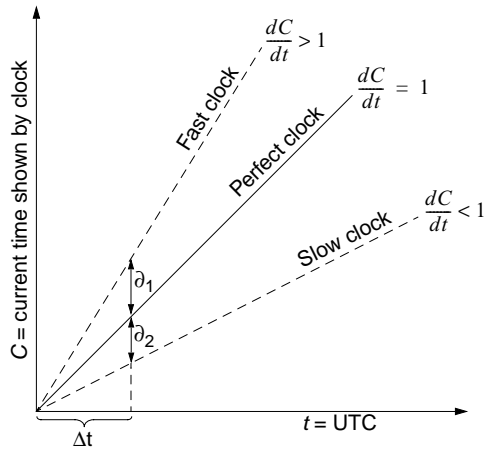
- One processor in the system (possibly several) is equipped with UTC receivers (time providers).
- By external synchronization the system is kept synchronous with the "real time". This allows to exchange consistently timing information with other systems and with users.

Internal Synchronization

Synchronization among processors of the system.

- It is needed in order to keep a consistent view of time over the system.
- A few processors synchronize externally and the whole system is kept consistent by internal synchronization.
- Sometimes only internal synchronization is performed (we don't care for the drift from external/real time).

Drifting of Clocks



∂_1 : drift of first clock after Δt .

∂_2 : drift of second clock after Δt .

$\partial_1 + \partial_2$: skew between clocks after Δt .

Drifting of Clocks (cont'd)

In an ideal case, the clock shows UTC: $C = t$.

$$\Downarrow$$

$$\frac{dC}{dt} = 1$$

In reality: $\frac{dC}{dt} = 1 \pm \rho$

ρ is the maximum drift rate, and should be specified by the manufacturer.

Two processors with similar clocks could be apart with:

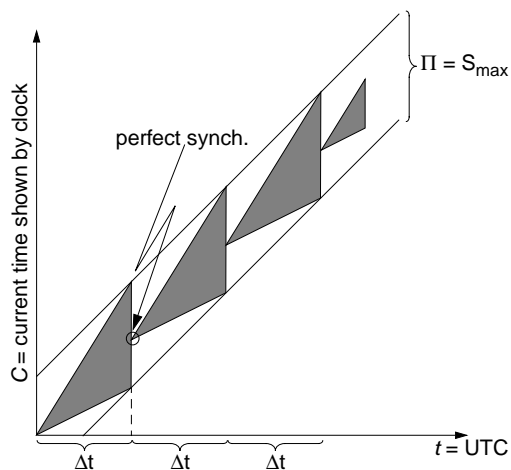
$$S = 2\rho\Delta t$$

☞ If we have to guarantee a *precision* Π (maximum difference between the indication of two clocks), the clocks have to be synchronized at an interval:

$$S_{\max} = \Pi$$

$$\Delta t \leq S_{\max}/2\rho = \Pi/2\rho$$

Drifting of Clocks (cont'd)



☞ Only if we assume that after synchronisation the clocks are perfectly aligned, then it is the case that:

$$S_{\max} = \Pi \quad \text{and}$$

$$\Delta t = \Pi/2\rho$$

Drifting of Clocks (cont'd)

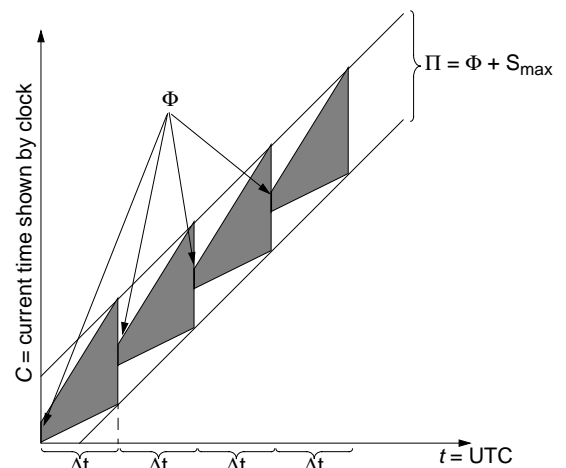
☞ In reality, the alignment after synchronisation is not perfect:
The *convergence function* Φ denotes the offset of the time values immediately after resynchronisation.



In order to achieve a certain precision Π :

$$\Phi + S_{\max} \leq \Pi$$

$$\Delta t = (\Pi - \Phi)/2\rho$$



Clock Synchronization Algorithms

Centralized Algorithms

- There exists one particular node, the so called *time server node*.
 - Passive time server:** the other machines ask periodically for the time. The goal is to keep the clocks of all other machines synchronized with the time server. This is often the case when the time server has an UTC receiver.
 - Active time server:** the time server is active, polling the other machines periodically. Based on time values received, it computes an update value of the clock, which is sent back to the machines.

Distributed Algorithms

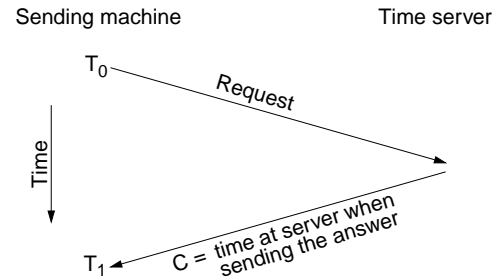
- There is no particular time server. The processors periodically reach an agreement on the clock value.
 - This can be used if no UTC receiver exists (no external synchronization is needed). Only internal synchronization is performed.
 - Several processors (possibly all) have an UTC receiver. However, this doesn't avoid clock skews; internal synchronization is performed using a distributed clock synchronization strategy.



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Cristian's Algorithm

- Cristian's algorithm is a centralized algorithm with passive time server. The server is supposed to be in possession of the correct time (has an UTC receiver).
- Periodically (with period less than $(\Pi - \Phi)/2\rho$) each machine sends a message to the time server asking for the current time.



- T_0 and T_1 are the time shown by the clock of the sending machine when sending the request and receiving the answer, respectively.



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Cristian's Algorithm (cont'd)

As a first possible approximation, after receiving the answer, the clock could be set to:

$$T_{rec} = C$$

However, it takes a certain time, T_{trans} , for the replay to get back to the process issuing the request:

$$T_{rec} = C + T_{trans}$$

How large is T_{trans} ? Possible estimation:

$$T_{rec} = C + (T_1 - T_0)/2$$

Problem:

The time to receive the answer can be very different for the one needed to transmit the request (because of congestion on network, for example).

Can we, at least, determine the accuracy of the time estimation?



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Cristian's Algorithm (cont'd)

- Suppose the *minimum* time t_{min} needed for a communication between the machine and the time server is known.

The exact value for T_{rec} is between:

$$T_{rec}^{min} = C + t_{min}, \text{ when the answer has been transmitted in minimum time,}$$

and

$$T_{rec}^{max} = C + (T_1 - T_0) - t_{min}, \text{ when the request has been transmitted in minimum time.}$$

$$\text{The range is: } T_{rec}^{max} - T_{rec}^{min} = (T_1 - T_0) - 2t_{min}$$



The time is set with an absolute accuracy of:

$$\pm ((T_1 - T_0)/2 - t_{min})$$

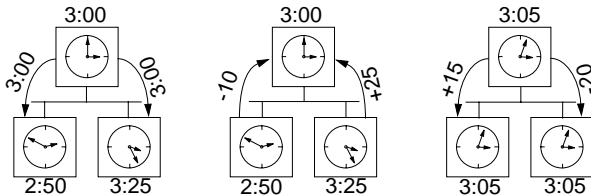
- In order to improve accuracy, several requests can be issued; the answer with the smallest $(T_1 - T_0)$ is used to update the clock.



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The Berkeley Algorithm

- ☞ The Berkeley algorithm is a centralized algorithm with active time server. It tries also to address the problem of possible faulty clocks.



- The server polls periodically every machine to ask for the current time.
- Based on received values, the server computes an average.
- The server, finally, tells each machine with which amount to advance or slow down its clock.

The Berkeley Algorithm (cont'd)

- ☞ The situation is more complicated than the figure on the previous slide shows:

- The server performs corrections, taking into consideration estimated propagation times for messages, before computing averages.
- If on a certain processor the clock has to be set back, this has to be performed in a special way, in order to avoid problems (see slide 37).
- The server tries to avoid taking into consideration values from clocks which are drifted badly or that have failed.



Only clock values are considered that do not differ from one another by more than a certain amount.

- If the master fails, another one has to be elected.

Distributed Clock Synchronization Algorithms

- ☞ With distributed clock synchronization there is no particular time server.

Distributed clock synchronization proceeds in three phases, which are repeated periodically:

1. Each node sends out information concerning its own time, and receives information from the other nodes concerning their local time.
 2. Every node analysis the collected information received from the other nodes and calculates a correction value for its own clock.
 3. The local clocks of the nodes are updated according to the values computed at step 2.
- The typical algorithm used at point 2 preforms the following:
 - The correction value for the local clock is based on an average of the received clock values.
 - When receiving time values from the other nodes, corrections are performed taking into consideration estimated delays due to message passing.
 - Only clock values are considered that do not differ from one another by more than a certain amount.

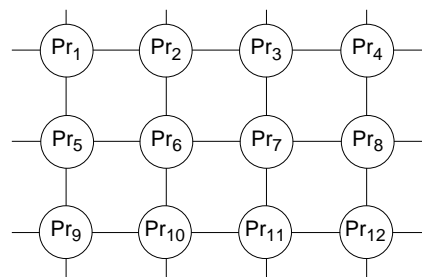
Distributed Clock Synchronization (cont'd)

Localized Averaging Algorithm

- With a large network it is impractical to perform synchronization among all nodes of the system. Broadcasting synchronization messages from each node to all other nodes generates a huge traffic.
- In large networks, nodes are logically structured into structures, like grid or ring. Each node synchronizes with its neighbours in the structure.

In the grid below:

Pr_6 synchronizes with Pr_2 , Pr_7 , Pr_{10} , and Pr_5 ;
 Pr_7 synchronizes with Pr_3 , Pr_8 , Pr_{11} , and Pr_6 .



Adjusting Drifted Clocks

The problem

Suppose that the current time on the processor is T_{curr} and, as result of clock synchronization, it has to be updated to T_{new} .

- If $T_{new} > T_{curr}$ the solution is simple. Advance the local clock to the new value T_{new} .
- If $T_{new} < T_{curr}$ we are not allowed to simply set back the local clock to T_{new} .

Setting back the clock can produce severe errors, like faulty time stamps to files (copies with identical time stamp, or later copies with smaller time stamp) and events.

It is not allowed to turn time back!

- Instead of turning the clock back, it is "slowed down" until it, progressively, reaches a desired value. This is performed by the software which handles the clock tick (see also slide 21).

Adjusting Drifted Clocks (cont'd)

At each clock tick an increment of the internal clock value θ is performed:

$$\theta = \theta + v \quad (v \text{ is the step by which the internal clock is incremented}).$$

In order to be able to perform time adjustment, the software time (T_{curr}), which is visible to the programs running on the processor, is not directly θ , but a software clock which is updated at each tick with a certain correction relative to θ :

$$T_{curr} := \theta(1 + a) + b \quad \text{if no adjustment is needed, } a = b = 0.$$

- The parameters a , and b are set when a certain adjustment is needed, and used for the period the adjustment is going on.

Adjusting Drifted Clocks (cont'd)

Suppose at a certain moment:

- The internal clock shows θ
- The software clock shows T_{curr}
- The clock has to be adjusted to T_{new} .
- The adjustment has to be performed "smoothly" over a period of N clock ticks.

Now we have to fix a and b that are to be used during the adjustment period:

- For the starting point we have to have:

$$T_{curr} = \theta(1 + a) + b \quad (1)$$

- after N ticks the "real" time will be: $T_{new} + Nv$.
- after N ticks, the software clock will show:
 $(\theta + Nv)(1 + a) + b$

If after N ticks the time adjustment is to be finished:

$$(\theta + Nv)(1 + a) + b = T_{new} + Nv \quad (2)$$

From (1) and (2) above we get:

$$a = (T_{new} - T_{curr})/Nv$$

$$b = T_{curr} - (1 + a)\theta$$

Adjusting Drifted Clocks (cont'd)

☞ The above strategy works regardless if the adjustment has to be performed forward ($T_{new} > T_{curr}$) or backward ($T_{new} < T_{curr}$).

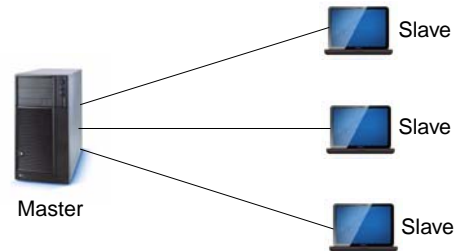
☞ If the adjustment is forward, it can be performed directly by updating the clock.

☞ If the adjustment is backward the clock has to be changed smoothly, like shown above.

The Precision Time Protocol (PTP)

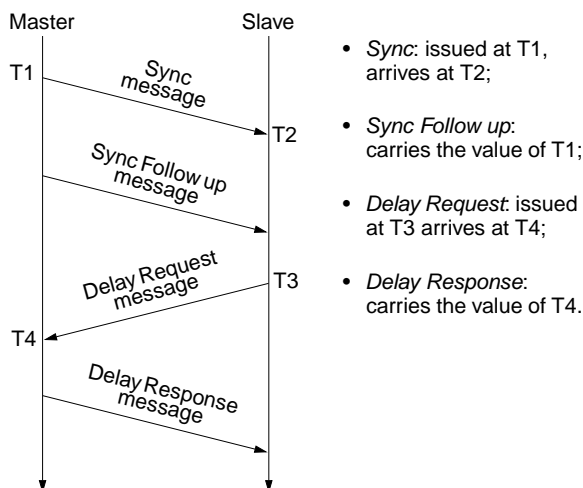
- ☞ The Precision Time Protocol - PTP (IEEE-1588 standard) provides a method to precisely synchronise distributed computer clocks over a Local Area Network with an accuracy of less than 1 microsecond.
- ☞ PTP is primarily intended for use in special-purpose networks for industrial automation, measurement systems, robotics, automotive technology, etc.
- ☞ The synchronisation approach in PTP is based on a *centralised technique*: a master synchronises one or several slaves connected to it (remember e.g. Cristian's algorithm, slide 30).

The Precision Time Protocol (cont'd)



- The *Master* is provider of time; the *Slave* synchronises to the *Master*.
- The time of the Master is reported to the Slave as accurately as possible. The goal of the employed algorithm is to compensate for the processing times on the nodes and for communication delays.

The Precision Time Protocol (cont'd)



- ☞ **Question:** Why does not the *Sync* message carry the value of T1?
- Answer:** The value of T1 is the exact moment when the *Sync* message *has left* the master. This is later than the moment when the message is assembled and issued for transmission; thus, the value of T1 cannot be written into the message. By this policy the interval T1-T2 does not contain any delay due to running the protocol stack or due to the queuing time of the message in the case of congestion.

The Precision Time Protocol (cont'd)

- Using timestamps T1, T2, T3, T4 the slave is able to accurately synchronise its clock.

This is performed in two phases:

Phase 1: offset calculation
consists of the *Sync* and *Sync Follow up* messages;

Phase 2: delay measurement
consists of the *Delay Request* and *Delay Response* messages:

The Precision Time Protocol (cont'd)

☞ A naive approach:

Calculate the master→slave offset Θ_{MS} :

$$\Theta_{MS} = T_2 - T_1$$

Update the server clock:

$$T_S = T_S - \Theta_{MS} = T_S - (T_2 - T_1)$$

The offset Θ_{MS} , calculated above, also includes the communication delay of the *Sync* message \Rightarrow The above synchronisation is accurate *only if this delay would be zero* (which, of course, it is not)!

☞ An accurate approach:

Estimate the communication delay:

$$T_2 - T_1 = \Theta_{MS} + \delta$$

$$T_4 - T_3 = \Theta_{SM} + \delta$$

$$\delta = (T_2 - T_1 + T_4 - T_3)/2$$

Update the server clock:

$$\Theta_{MS} = T_2 - T_1 - \delta$$

$$T_S = T_S - \Theta_{MS} = T_S - (T_2 - T_1) + \delta$$



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The Precision Time Protocol (cont'd)

- Phase 1 is performed typically every 2 seconds.
- Phase 2 is performed at greater intervals (between 4 and 60 seconds).
- Between two runs of Phase 2, the last update of delay δ is used for the server clock update after each run of Phase 1.

☞ The calculation of δ assumes that the communication delay was identical on the way master to slave and slave to master!

This is true if we have a direct connection between master and slave; if there are switches/routers on the way between master and slave, this is not true any more (queuing delay, congestion etc. can produce significant fluctuation).

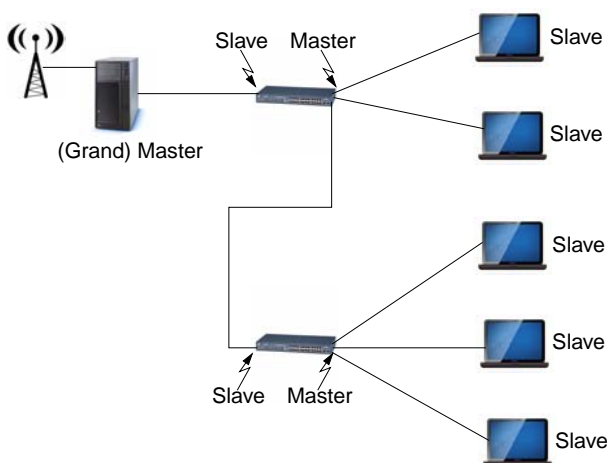


Boundary clock switches solve this!



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The Precision Time Protocol (cont'd)



- Boundary clock (BC) switches contain a clock that is synchronised to a connected master; they behave themselves as masters on all other ports where its clock is used for synchronisation by connected slaves.
- Using BC switches, all synchronisations are over point to point connections.
- A Best Master Algorithm (BMA) determines master slave relations depending on accuracies of clocks. As result, a tree structure is determined automatically, with the node containing the best available clock - the *grand master*, as root.



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The Precision Time Protocol (cont'd)

☞ In order to achieve high precision, the time stamping has to be implemented with hardware support.

- Software implementation - a PTP software daemon running on non-standard hardware: Synchronisation in the range 10 -100 microseconds is achievable.
- Hardware implementation - hardware timestamping at master and slave plus PTP enabled network switches (with boundary clock): synchronisation in the range 10 - 100 nanoseconds is achievable.



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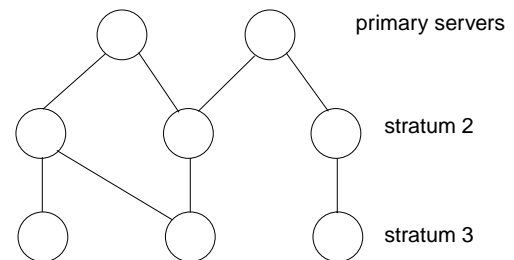
The Network Time Protocol (NTP)

- ☞ The NTP has been adopted as a de facto standard for clock synchronisation in general purpose UNIX, Windows, etc. workstations and servers.
- Connection over global Internet by standard routers and gateways is assumed (no specialised hardware).
- There are no particular hardware requirements and customised components.
- Accuracy at the level of milliseconds can be achieved.
- Network overhead is an issue with NTP, since the network is shared with demanding Internet applications (as speech, video); clock update intervals can be in the range of minutes (even hours).
- The actual master-slave synchronisation algorithm is, in broad terms, similar to that used in PTP.

The Network Time Protocol (cont'd)

- NTP allows for the implementation of a network architecture in which primary and secondary time servers are defined.
 - Primary time servers are directly connected to a time reference source (e.g. UTC receiver).
 - Secondary time servers synchronize themselves (possibly via other secondary servers) to primary servers over the network.

Based on the accuracy of each time server, a structure of servers is defined, with primary servers at the top, and secondary servers on the levels (*strata*) below.



The Network Time Protocol (cont'd)

- Primary servers have the highest accuracy of their clocks. As the stratus level increases, the accuracy degrades.
- A lower accuracy of a certain server can be caused by the network paths towards the higher accuracy stations, and/or by the stability of its local clock.

The Network Time Protocol (cont'd)

One of the principal goals of NTP is to achieve *robustness*. This is different from PTP, since NTP is used in very large, open, general purpose, and vulnerable networks. Robustness implies resistance in the case of:

- Faulty clocks.
- Damaged network paths.
- Broken servers
- Malicious intruders (protection, for example, by authentication).

In order to maintain acceptable accuracy (even in the case of faults):

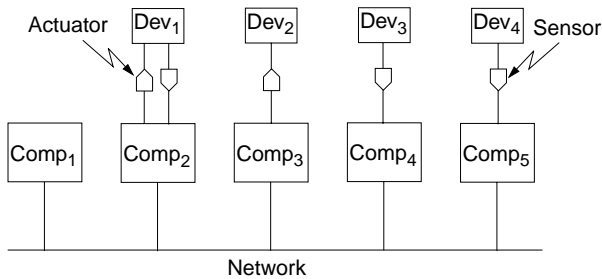
1. Filtering algorithm:

By this, the estimation of the offset of the local clock, relative to a certain other server (called peer) is improved. Repeated synchronization attempts are performed and the low quality estimates are dropped out.

2. Selection algorithm:

Periodic attempts to find, for a given server, the most reliable servers (peers) to be used as clock source for synchronization.

Real-Time Communication



Data flows

- from sensors and control panels to processors
- between processors
- from processors to actuators and displays

☞ In order to achieve predictability: hard real-time systems need communication protocols that allow the communication overhead to be bounded.

Time/Event Triggered Communication

☞ Time-triggered communication

The sender and receiver agree on a cyclic, time-controlled, conflict-free communication schedule.



Each message transmission is started at a certain, pre-defined, moment in time; conflicts are avoided per definition.

Examples:

- TDMA
- FlexRay (static phase)

} Predictable;
appropriate for real-time

☞ Event-triggered communication

Messages can be sent whenever a significant event happened at the sender (task terminated, interrupt, etc.).



No pre-defined moments in time for communication; potential conflicts for bus access.

Examples:

- Ethernet - is not predictable;
- CAN
- Token ring
- FlexRay (dynamic phase)

} Predictable;
appropriate for real-time

Ethernet Protocol

☞ Ethernet is a Carrier Sense Multiple Access/Collision Detection (CSMA/CD) protocol.

- On Ethernet, any device can try to send a frame at any time. Each device senses whether the line is idle and therefore available to be used. If it is, the device begins to transmit.
- If two or more devices have tried to send at the same time, a collision is said to occur and the frames are discarded. *Each device then waits a random amount of time and retries until successful in getting its transmission sent.*



Ethernet is inherently stochastic. It cannot provide a known upper bound on transmission time.

Ethernet is not suitable for real-time applications.

☞ The above is true for the original "vintage" Ethernet. New, Ethernet based solutions have been proposed (e.g. full-duplex switched Ethernet, that avoids collision) which provide support for predictability and could be applied for real-time communication.

Protocols for Real-Time Communication

- CAN protocol
- Token Ring
- TDMA protocol
- FlexRay protocol

TDMA is mostly suitable for applications with regular data flow (constant rate).

It is the most reliable and predictable.

The CAN protocol provides a higher degree of flexibility in the case of irregular flow.

FlexRay is a heterogeneous time&event triggered protocol; potentially combines advantages of TDMA and CAN

CAN Protocol

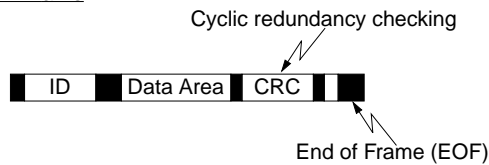
CAN = Control Area Network

👉 CAN is a Carrier Sense Multiple Access/Collision Avoidance (CSMA/CA) protocol.

CAN is widely used in automotive applications.

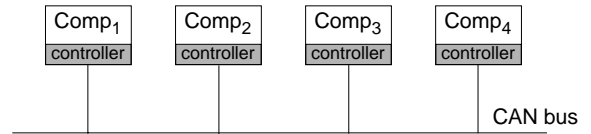
- In the CAN protocol, collisions are avoided by arbitration based on *priorities* assigned to messages.
- CAN communication is based on the transfer of packages of data called *frames*

A CAN frame



- The identifier (ID) field is used for two purposes:
 1. To distinguish between different frames.
 2. To assign relative priorities to the frames

CAN Protocol (cont'd)



A CAN controller is attached to each processor in the system. It ensures that:

- The highest priority frame (smallest identifier) waiting to be transmitted from the respective processor is entering the arbitration for the bus.
- The arbitration procedure performed in cooperation by the controllers, guarantees access to the message with highest priority.

➡ The arbitration is based on the existence of a *dominant* and a *recessive* bit: 0 is the dominant, 1 is the recessive:

- In CAN, if several controllers transmit and at least one transmits 0, the bus is at 0; if all controllers write 1, the bus is at 1.

CAN Protocol (cont'd)

Frame node_1:	00001100011	- - - - -	EOF
Frame node_2:	00100010101	- - - - -	EOF
Frame node_3:	00001010101	- - - - -	EOF
Frame node_4:	00001010111	- - - - -	EOF

node 1	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
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ID based arbitration:
Frame node_3 has priority

- During the arbitration phase, the controllers write the frame ID to the bus, bit by bit, as long as they read from the bus the same value they have written; once a controller reads different, it continues by writing 1s until it reads EOF from the bus.
- Node 3 has got priority; it writes its frame to the bus.
- After the EOF, nodes which were unsuccessful retry with the same frames.

CAN Protocol (cont'd)

If the following assumptions are fulfilled, message communication times can be bounded using techniques similar to those developed for priority based process scheduling (see slide 16):

- A given message is generated periodically, and a worst case (minimal) period is known.
- The maximum size of each frame is known.
- The software overhead connected to handling of messages is known.
- The maximum overhead due to errors and re-transmission is estimated.

Token Ring

- The right to transmit is contained in a special control message, the token. Whoever has the token, is allowed to transmit.
- Processors are logically organized in a ring, on which the token is continuously passed.

With a token ring protocol maximum bounds on message delay can be established.

The following are the essential parameters:

- The token hold time:** the longest time a node may hold the token.
This can be derived from communication speed on bus and the maximum bound on the message length.
- The token rotation time:** the longest time needed for a full rotation of the token.
This can be derived as $k * T_h$, where k is the number of processors, and T_h is the token hold time.

☞ Fault tolerance can be a problem: if one node fails, the traffic is disrupted.

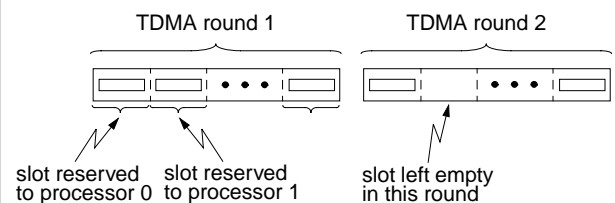


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TDMA Protocol

TDMA = Time Division Multiple Access

- The total channel (bus) capacity is statically divided into a number of slots. Each slot is assigned to a certain node (processor).
- With a system of N processors, the sequence of N slots is called a *TDMA round*. One processor can send one frame in a TDMA round. The frame is placed into the slot assigned to that processor.
- If no frame is to be sent by a processor, an empty slot will be sent in that round.
- The duration of one TDMA round is the TDMA period.



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TDMA Protocol (cont'd)

- TDMA practically means a static partitioning of access time to the bus. Each processor knows in advance when and for how long it is allowed to access the bus.
TDMA implies the availability of a global physical time base (clock synchronisation).
- Collisions are avoided as processors know when they have guaranteed exclusive access to the bus.
- Message passing delay is bounded: a message is split into a certain number of frames which are transmitted in successive slots (one per TDMA round).
- Not all slots have to be of identical length. The slot length corresponding to a given node is however identical for all rounds. This length is determined by the designer, depending on the particularities of the processes running on each node (considering, for example, the length and number of messages generated)

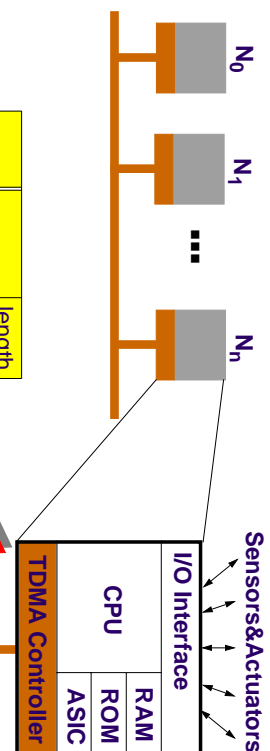


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TDMA Protocol (cont'd)

Over a TDMA cycle

Time	Action	length (bits)
t_1	rd-frame	16
t_2	wr-frame	32
t_3	rd-frame	32
t_x	wr-frame	16



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TDMA Protocol (cont'd)

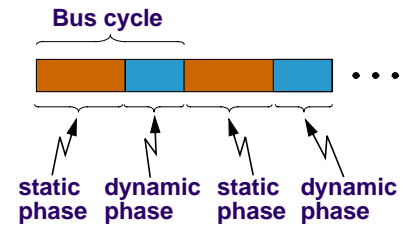
Advantages:

- High degree of predictability
- Well suited to safety critical applications.

Disadvantages:

- Can lead to poor utilisation of the available bus bandwidth (e.g. empty slots).
- Low degree of flexibility \Rightarrow problems with irregular flows.

FlexRay A Heterogeneous Communication Protocol



☞ FlexRay is a combination of two protocols: an event triggered and a time triggered. It combines some of the advantages of the two approaches.

☞ FlexRay has been designed by the "FlexRay" consortium for automotive applications.

FlexRay (cont'd)

☞ The FlexRay bus cycle is divided into two phases (the length of each phase is fixed by the designer):

- Static phase

During the static phase the bus works according to a TDMA policy (the static phase consists of slots assigned to processors).

- Dynamic phase

During the dynamic phase the bus works according to an event triggered protocol, somewhat similar to CAN (messages are arbitrated according to their priority).

☞ As a combination of two predictable approaches, FlexRay is suitable for real-time applications.

Summary

- A real-time system is a computer system in which the correctness of the system behavior depends not only on the logical results of the computations but also on the time when the results are produced.
- Many real-time applications are implemented as distributed systems.
- Deadlines in distributed systems can be soft and hard. Hard deadlines have to be met strictly.
- Predictability is the most important property of a real-time system.
- By scheduling it has to be determined which process or communication to be executed at a certain moment.
- With static scheduling, activation times are determined a-priori.
Advantage: high predictability.
Problem: no flexibility.
- With dynamic scheduling it is more difficult to guarantee that time constraints are fulfilled. Under certain restrictions, however, this is possible.
- Specific issues with distributed embedded systems: clock synchronization and real-time communication.
- External clock synchronization: synchronization with time sources external to the system.
Internal clock synchronization: synchronization among processors of the system.

Summary (cont'd)

- Centralized algorithms for clock synchronization are based on the existence of a time server node. Such are: Cristian's algorithm and the Berkeley algorithm.
- Distributed algorithms for clock synchronization don't need the presence of a particular server node.
- The Precision Time Protocol and the Network Time Protocol are used as standards for time synchronization.
- The main problem with communication in distributed real-time systems is predictability.
- The Ethernet is an example of protocol which cannot be used with real-time applications because no bounds can be derived on communication time.
- The CAN protocol is used in many automotive applications. Predictability is provided by a priority based handling of messages.
- The token ring protocol provides an upper bound on communication time. Efficiency is low for many applications. Fault tolerance can be a problem (if one node fails, the traffic is disrupted).
- TDMA is based on a division of the channel capacity into time slots which are assigned to the particular processors. TDMA provides high predictability and the potential for safety critical applications.

