

Rugged Board

SPI

<https://community.ruggedboard.com>

Since the SPI dev interface was introduced into the kernel, it is easy to access the SPI device in user space via the device node “/dev/spidev”.

The device node below is used to access the SPI bus driver. The first number 3 indicates bus number 3, the second 0 indicates the CS number

/dev/spidev3.0

RuggedBoard Mikro Bus
MOSI MISO
loopback connection



```
root@rugged-board-a5d2x-sd1:~# ls -al /dev/spidev3.0
crw----- 1 root    root      153,  0 Jan  1 1970 /dev/spidev3.0
root@rugged-board-a5d2x-sd1:~#
```

Sample Code Logic

```
struct spi_ioc_transfer trx;  
uint32_t scratch32;  
open("/dev/spidev3.0 ", O_RDWR);  
ioctl(fd, SPI_IOC_RD_MODE32, &scratch32)  
ioctl(fd, SPI_IOC_WR_MODE32, &scratch32)  
ioctl(fd, SPI_IOC_RD_MAX_SPEED_HZ, &scratch32)  
ioctl(fd, SPI_IOC_WR_MAX_SPEED_HZ, &scratch32)  
ioctl(fd, SPI_IOC_MESSAGE(1), &trx)
```

Open Discussions



Developer
Wiki





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