```
/**************************************************************************/
/       filename:  Makefile
/
/    description:  To rebuild the executable if any of the dependent files change
/
/         author:  Sunkaralakunta Venkatarama Reddy, Rakesh
/       login id:  SP_19_CPS444_11
/
/          class:  CPS 444
/     instructor:  Perugini
/     assignment:  Homework #4
/
/       assigned:  February 11, 2019
/            due:  February 13, 2019
/
/**************************************************************************/

CC = gcc
APP = flip
CFLAG = -DBSD -DNDEBUG -O -c

all: $(APP) man

$(APP): $(APP).o getopt.o
        @gcc -s -o $(APP) $(APP).o getopt.o

$(APP).o: $(APP).c $(APP).h
        @gcc $(CFLAG) $(APP).c

getopt.o: getopt.c $(APP).h
        @gcc $(CFLAG) getopt.c

man: $(APP).1
        @nroff -man $(APP).1 > $(APP).man

clean:
        -rm $(APP) *.o $(APP).man
```