

EEE Suggestions

(V.K. Mehta)

(i) JFET + MOSFET — 406 page ~~exam~~ 405

(graph math) + model output characteristics

(ii) Switching Circuit [principle - mono stable ***]
multibrator.

(iii) OP-amp (full)

(iv) OP-amp amplification } op-amp Integrator
 } op-amp differentiator

(v) Oscillator (Hartley) (working principle)

(vi) Chapter 13 (article 13.4)

(vii) Linear (non) Circuit :

(Comparator)
(peak Detector)
(Precision Rectifier) } (उपलब्ध सर्किट)

17/20 MARKS

MATH SUGGESTIONS

- ① { JFET \rightarrow 19.1, 19.2, 19.3, 19.5
MOSFET \rightarrow Boylstad Page 405, 406.
- ② Switching Ckt \rightarrow 18.1, 18.2, 18.3, 18.4
- ③ OP-Amp \rightarrow 25.25-25.28, 25.32-25.34,
25.44-25.47, 25.50, 25.51,
25.54, 25.55.
- ④ OP-Amp Applications \rightarrow 13.2-13.5, 14.5, 14.6
- ⑤ Non-linear \rightarrow 2 Maths from Sheet.