### **Morality Development Program (MDP)**

Semester End Examination, Spring -2022 Semester: 5<sup>th</sup>

Course Code: MDP-3505, Course Title: Concept on Moral Development-II
Time: 2.00 hours
Full Marks: 50

#### Answer any five of the following questions.

- 1. Explain the concept of recreation in the light of Qur'an and Sunnah. How much the recreation and entertainment is permissible in Islam? What are the guidelines should be followed in Recreation?
- 2. Briefly explain the rules and regulations of dress code for both male and female from Islamic perspective.
- 3. Define malaise? What actions to be taken by Muslims to cure this malaise? Explain. 10
- 4. What do you mean by Islamic culture? Explain the characteristics of Islamic culture.

  Distinguish between Islamic culture and western culture.
- 5. Briefly explain the Day of Judgment with reference from Quran and Hadith. 10
- 6 "Islam ensures peaceful co-existence"-Justify this statement in light of Quran and Sunnah.
  - 7. What are the basic human rights in Islam? Explain the rights of women in light of Quran and Sunnah.

10

Center for General Education (CGED)

Semester End Examination, Spring-2022

Course Code: URED-3503 (URED-3101 for LLB)
Course Title: Political Thoughts and Social Behavior

Full Marks: 50 Time: 2:30 Hours

# Answer the following questions (All questions are of equal value)

#	Questions	Marks	CLOs	Bloom's taxonomy domain
1	Decide the procedures of marriage and criteria of selecting the bride and the groom according to the holy <i>Qur'an</i> and <i>Sunnah</i> .	10	4	Evaluate
2	Develop a comparative discussion between Islam and other religions regarding women rights.  Or	10	3	Create & Analyze
	Analyze the status of women in Islam and the same given to them by other existing communities.			
3	Evaluate the five fundamental principles of Islamic Economic System.	10	5	Evaluate
4	Islamic law has provided mankind with appropriate instructions and guideline regarding dress for man and woman. Elaborate those guidelines after mentioning the purposes of dress in Islam according to the <i>ayah</i> 26 of <i>surah al-a'raf</i> .	10	4	Create
5.	Duties of parents towards their children is more important than the duties of children towards their parents. Explain the statement with proper arguments.  Or  Write very briefly regarding what we should say in the following situations (any five):  a. When we go to bed for asleep  b. When we go to the wash room	10	3	Evaluate & Create
	<ul> <li>When we enter the Masjid</li> <li>When we go down by stair / lift or any way</li> <li>If a sneezer said 'Alhamdu lillah'</li> <li>If someone gives us salam</li> <li>If we want to do something later.</li> </ul>			

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Department of Computer Science and Engineering

B. Sc. in CSE

## Final Exam, Spring-2022

Course Code: CSE 3527

Course Title: Compiler

Full Marks: 50

Time: 2 hours 30 minutes

(i) The figures in the right had a second or the second of t

(i) The figures in the right-hand margin indicate full marks

(ii) Course Outcomes and Bloom's Levels are mentioned in additional Columns

	Part A [Answer the questions from the followings]	
1. b)	Define LL(1) grammar and LR(k) parser. Why LR parsers are used in compiler design?  Construct CLR(1) and LALR(1) data flow diagram and parsing table for the following grammar:  S → CC C → cC C → cC C → d	CLO R 3 2 CLO U 7 2
)2,—a)	Calculate the LR(0) Parsing for the given grammar-  E -> E + T   T  T -> T * F   F  F -> (E)   id	CLO App 5
3-6)	What do you mean by Operator Precedence Grammar?  With the help of following grammar parse the input string:- "id + id * id "\$	CLO E 1+4 2 =5
2. a)	Or, For the following sub-problems, consider the following context-free grammar:	CLO App 1+4
	$S \rightarrow A\$$ $A \rightarrow xAx$ $A \rightarrow C$ $B \rightarrow yBy$ $B \rightarrow C$ $C \rightarrow zBz$ $C \rightarrow wAw$ $C \rightarrow \lambda$ $(1)$ $(3)$ $(4)$ $(5)$ $(5)$ $(6)$ $(7)$ $(8)$	3 =5

	i. What are the terminals and non-terminals of this grammar?		7	9
		*: -	V	
	ii. Show the derivation of the string xzzx\$ starting from S (specify which production you used at each step), and give the parse tree according to that	1. 1. a. a.t.		Y
2 b)	derivation.  Construct a CLR parsing table for the given context-free grammar	CLO 2	E	5
	S>AA			
	A>aA   b			
	Part B			
	[Answer the questions from the followings]			
3. a)	Define the synthesized attributes and inherited attributes with example. Using syntax-directed definition draw the annotated parse tree and dependency graph for the sentence (3*5+2)*2n.	CLO 1	C	5
	$S \rightarrow En$ $E \rightarrow E + T$			
	$E \to T$ $T \to T * F$ $T \to F$			
	$F \rightarrow (E)$ $F \rightarrow digit$		-	
3. b)	Write some examples of top-down parsing and bottom-up parsing.  Consider the following grammar-	CLO 2	E	1+4 =5
	$E \to E - E$ $E \to E \times E$			
<b>*</b>	$E \rightarrow id$ Parse the input string id – id x id using a shift-reduce parser.			*.
4a)	What is code optimization? Consider the following code:	CLO	App	1+4
	a = b * -c + b * -c Write down the Quadruples of the given Code?	3		=5
4. b)-	Write quadruple, triples and indirect triples for following expression:	CLO	U	5
/	(x + y) * (y + z) + (x + y + z)	2		•
	t1 = 4 * I			
5. a)	t2 = a[t1] t3 = 4 * 1	CLO 3	An	5 '
•	t4 = b [t3] t5 = t2 * t4	. 10		
	t6 = prod + t5 t7 = I + 1			
	I = t7			
	If $I \le 20$ goto $I$			

Write down the DAG to get the above information.

2

What do you know about the identifying the Basic blocks in target code generation? What do you know about the Peep hole optimization? Or, Consider the following C codes and answer the questions (i) to (iii):-CLO int a[100],i,n=10,steps=0,temp; do{i=steps+1 do {if(a[steps]>a[i]){ temp=a[steps]; a[steps]=a[i]; data[i]=temp; i++; )while(steps<n);</pre> steps++; }while(steps<n);</pre> (i) Translate the C code into three-address code. (ii) Identify the basic block in three-address code. (iii) Construct the flow graph from the three-address code. Draw the syntax tree and DAG for the expression: CLO An

a+a+a(a\*a)+a+a+a\*(b-c)+(b-c)\*d

3

Department of Computer Science & Engineering S.Sc. in CSE, Semester Final Exam, Spring 2022

Course Code: CSE 3523 Course Title: Microprocessors, Microcontrollers & Embedded Systems Total Marks: 50 Time: 2.5 Hours

[Answer all the five questions. Figures in the right-hand margin indicate full marks.]

Course Outcomes and Bloom's Levels are mentioned in additional Columns

•	PART A	CLO	Mark	
1(a)	In the early years of computing, the processor has to waste a number of clock cycles just for checking the signal in the system, by this processor will become busy unnecessarily. If any signal came for the process, the processor will take some time to process the signal due to the checking which is known as the polling method. So system performance also will be			
. *	degraded and the response time of the system will also decrease.			
e Nac	Suggest a better process to handle the signal. Explain a general process of it.	3	5	
(þ))	Can your suggested process in 1(a) be handled in parallel? Explain it.  OR	3	5	
,	How interrupt vector type number 80H is applied to the data bus in response to an INTR using three state buffer.			
2.(a)	Write short note on:  i. L1 and L2 Cache, ii.BOUND	1	4	
(b)	Draw the block diagram of 8255A.	1	4	
	OR OR Draw and describe the pin out diagram of 8259A programmable interrupt controller.			
(c)	What are the difference between hardware interrupt and software interrupt.	2	2	
	PART B			
3(a)	Suppose you are building an hardware project named digital alarm clock. Will you use Microprocessor or Microcontroller? Explain the reasons behind it using diagrams.	4	3	
(b)	Explain the basic DMA operation with timing diagram	4	4	
(c)	Write the differences between IO-mapped I/O and Memory-mapped I/O.	4	. 3	
4.(a)	Recently, Amit and Arafat learned about Microcontroller 8051. However, they are unsure of how to access RAM or ROM due to the fact that the device contains both internal and external RAM and ROM. Help them by answering the following questions —	2	6	
08. H	<ul><li>i. To interface a 16-bit Memory what port(s) they can use? Describe the port(s).</li><li>ii. How can they access external RAM with 8 and 16 bits of addressing?</li></ul>		,	
	Design a burglar alarm system using 8051 microcontrollers. When a burglar will come to your house then alarm sounds are generated. Draw the circuit diagram of burglar alarm system where port 1 is used as input and port 3 is used as output port. Also draw the flowchart when zone 2 is activated.			
(b)	Describe the architecture of 8051 micro-controller with figure.	1	4	
5, (a)	Explain why hardware-software co-design is better than traditional design. Describe it with proper diagram.	1	4	
	OR			
(b)	How can you optimize an embedded system?  Draw a general block diagram for an embedded system.	1	3	
(c)	Explain interrupt latency and how can we decrease it	.1	3,	

Department of Computer Science & Engineering

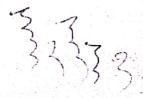
## B.Sc. in CSE, Semester Final Exam, Spring 2022

## Course Code: EEE-2421 Course Title: Electrical Drives and Instrumentations

Total Marks: 50 Time: 2.5 Hours [Answer all the five questions. Figures in the right-hand margin indicate full marks.]

Course Outcomes and Bloom's Levels are mentioned in additional Columns.

	PART A	PLO	CLO	Marks
1.(a)	What is induction motor and why is it called?	4	3 C2	2
(b).	Explain the operation of capacitor start induction motor with example.	4	3 C2	3
	OR .			
	<ul> <li>Briefly explain the working principle of the induction motor. Also draw a neat diagram of its exact equivalent circuit.</li> </ul>			
(c)	Explain how induction motor rotor rotates.			
		4	3 C2	2
( <u>d</u> )	A 3-phase induction motor is wound for 4 poles and is supplied from 50Hz system. Calculate i. Synchronous speed, ii. The rotor speed, when slip is 4% and iii. Rotor frequency when runs at 600rpm.	.4	3 C3	3
2.(a)	Explain the full step operation of stepper motor and discuss the significance of stepper motor in modern automation industries	4	3 C3	5
	OR OR			
	Escribe Half wave mode Driving technique for Stepper motor with neat diagram.			
(p) .	Why synchronous motor has no self-starting torque? Write some methodes to make SM Self-Starting.	4	3 C3	2.5
(c) /	Suppose you are working in an industry. You need a motor with constanat speed and can operate in lagging to leading power factor for some specific applications. Select an appropriate motor for yor desired application and explain the reasons of your selection.  PART B	4	3 C3	2.5
3.(a)	Write the process of measuring system with an appropriate example (based on the lab project done by yourself).	2	4 C4	<b>3</b>
(b)	What do you understand by the term measurement and instrumentation? Why measurement is so important in our life?		4 C4	3
(c)	Explain the significance of Measurement error combinations. If following voltage			
S	measurements are considered from two instruments. $V_1 = 90 \pm 3 \%$ , $V_2 = 80 \pm 5 \%$ . Calculate		4 C4	4
	<ul><li>a. the maximum percentage error of two voltage measurements.</li><li>b. the maximum percentage error in the difference of two voltages</li></ul>		<b>.</b>	
	Explain Common Anode and Common Cathode "7-segment Display" with necessary diagram.	2	4 C4	4



	4.(a)	Explain with the help of a functional block diagram, the principle of operation of a digital frequency meter	2	5 C4	5
		OR	44		
		For the following given data, calculate	2	5	5
		i) Arithmetic mean, ii) Deviation of each value, iii) Algebraic sum of the deviations,		C4	
		iv) Calculate the average deviation and v) Calculate the standard deviation			Pile
T.		Given, $Z1 = 70.7$ , $Z2 = 71.1$ , $Z3 = 70.2$ , $Z4 = 69.6$ and $Z5 = 71.7$			
	(b)	<ul><li>(i) What are the advantages of digital systems over analog?</li><li>(ii) Explain the following terms as applied to digital measurement.</li></ul>	2	5 C4	5
		(a) Resolution (b) Sensitivity of digital meter (c) Accuracy specification of digital meters			
		그 사람들이 살아보는 그 살아		4 996	
	5(a) 1	Write a difference between thermocouple, RTD and Thermistor. Suppose you need to measure temperature from -190°C to 450°C, which device will be suitable for measuring.	3	5 C4	5
	(b) [	During experiments with a copper-constantan thermocouple it was found that $c = 3.75 \times 10^{-2}$ mV/0C and $k = 4.5 \times 10^{-5}$ mV/0C2. If $T_1 = 1000$ C and the cold junction $T_2$ is kept in ice, compute the resultant electromotive force.	1	5 C3	2
		OR			
	l I	How Electricity is produced in a PV Panel? Write the prospects of Solar energy in Bangladesh.	1	5 C3	2
	(c) t	What is transducer? Why Transducer is important? Explain different optoelectronic transducer in brief.	1	5 C3	3
				1	

Dept. of Computer Science & Engineering (CSE)

Final Examination: Spring-2021 Program: B.Sc. in CSE

Course Code: CSE-3529

Course Title: System Analysis & Design.

Time: 2.5 Hours

Full Marks: 50

#### All questions should be answered.

#### Group A

1.(a) The passenger is required to fill in a reservation form giving details of his journey. The counter clerk ensures whether the place is available. If so, entries are made in the register ,tickets are prepared ,amount is computed and cash is accepted. A booking statement is prepared in triplicate from the reservation register. One copy of it is retained as office copy ,the other is pasted on the compartment and the third is passing on to the train conductor .Besides booking statement, cash statement is prepared at the end of each shift. Prepare a DFD to describe this situation., Give the following terms for the above "Railway Reservation System": (i) Input design of the system (ii) Draw Data Flow Diagram(DFD) (iii) Draw context free diagram (iv) Output design of the system

CO3 1.(b) Construct a decision tree and calculate Expected Value (EV) based on the values in the

table below: Formula:  $EV = \sum P(Xi) \times Xi$ 

Alternatives	Growing	Declining
Stocks	70	-13
Mutual Funds	53	-5
Bonds	20	20
Probability	0.4	0.6

Or

			TACE !	_	
	CO3	1 (a)	When CASE keyword is used in Structure English? Describe with example. Name the popular CASE tools.	5	
		. (a)	State the advantages and Disadvantages of Indexed Sequential File Organization and Direct File		
			Organization		
	CO3	1(b)	Draw a decision tree and decision table for a project management	5	
	003	1(0)			
	CO3	2 (a)	What is input design and its objectives	3	
	CO3	2.(h)	What is form design? Objectives of good form design.	4	
	CO3	2.(c)	What is database design? What is the logical design of a database?	3	
	0.5.5	2.(0)	Group B		
	7 . 2		· · · · · · · · · · · · · · · · · · ·	3	
	CO3	3.(a)	What is system testing? Is testing compulsory for a system or not? Justify your answer	3	
	CO3	3.(b)	What is white box testing and black box testing? Describe with example.	2	
	CO3	3.(c)	What are the difference between Alpha testing and Beta testing?	4	
				•	
	CO4	4.(a)	What is system maintenance? Why it is necessary?	3	
	CO4	4.(b)	Discuss the process of system maintenance.	3	
	CO4	4.(c)	What are the types of system maintenance? Distinguish between adaptive and corrective maintenance?	4	
		` '	그 그 그는 일이 그 하는 그는 이 나는 그렇게 하는 그는 것이 하는 것은 사람이 되고 있었다.		
•	CO2	5 (a)	Write some criteria for software selection and hardware selection	2	
1	CO <sub>2</sub>	5 (b)	What is Threats? Briefly discuss about physical threat and non-physical threat.	4	
	CO2	5 (6)	Write short notes on the following: i)Network security ii) End-point security iii) Internet security iv) Cloud	4	
	**	3.(0)	security		
			- 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1		
	CO2	5 (a)	What is meant by "Project Management"? Why it is needed: Write short note on. (1) Live (mines)	3	
	CO2	5-(h	Prepare Data Dictionary entries for i)The data elements Basic Pay Professional Tax ii)The data stores	4.	
		2.10	Salary Summery File, Arrears Files iii)The data flows Earning details, Deduction details		
	COT	5.(a	Write the importance of documentation in software development,	3	
			수의 역간 이번 이번 위에 가게 하고 하겠다면 경기에서 가장 보는 그는 아니는 아니는 그는 그를 보고 있는데 얼굴에 되었다. 그를 하셨다면 없는데 그를 하는데 없다면 없었다면 없었다.	1 150	

# International Islamic University Chittagong Department of Computer Science and Engineering B. Sc. in CSE Final Term Examination, Spring 2022 Course Code: CSE 3528

Course Title: Compiler Lab Total marks: 50 Time: 3 hours

	GROUP A			
1.	Define Left factoring and Left recursion.		5	CL01
	Consider the following grammar		1	0201
	E->E+T T			
	T->T*F F			
	F->(E) l id	***		
	After removing the left recursion and the left factoring what will be the	Rules?		170
2.			5	CLO2
	$S \rightarrow ACB / CbB / Ba$			1
	$A \rightarrow da/BC$			
	$B \rightarrow g$ /. $\in$			
	$C \rightarrow h/\epsilon$			
			_	
3.	Calculate the LR(0) Parsing for the given grammar-		5	CLO3
	$E \rightarrow E + T \uparrow T$			1
	$T \rightarrow T * F \mid F$			
	$F - > (E) \mid id$			
	NII		5	
4.	What do you mean by Operator Precedence Grammar?  With the help of following grammar parse the input string:- "id + id * i	id "\$	_	CLO2
	T->T+T/T*T/id		. *	
5.	For the following sub-problems, consider the following context-free grants	ammar:	5	
٥.	$S \to S^{*}A\$$	(1)		
		(2)		
	$A \mapsto xAx$	(3)		
	$A \mapsto C$	•		
	$B \mapsto yBy$	(4)		
	$B \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	(5)		
	$C'  \forall  zBz$	(6)		
	$C \longrightarrow wAw$	(7)		
	$C \rightarrow \mathcal{U}$	(8)		
	i. What are the terminals and non-terminals of this grammar?		Ven 18	g 37.
	ii. Show the derivation of the string xzzx\$ starting from S (specify whi	ch production		And the state of
	you used at each step), and give the parse tree according to that deriva			
6.	Construct a CL'R parsing table for the given context-free grammar		5	01.02
	S>AA	3047		CLO3
	A>aA   b	e		
	GROUP B			
7.	Viva		20	

#### Bismillahir Rahmanir Rahimi

# International Islamic University Chittagong Department of Computer Science & Engineering

B. Sc. in CSE Semester Final Examination, Spring 2022

#### Course Code: CSE 3521 Course Title: Computer Architecture

Total marks: 50 Time: 2.5 hours

[Answer all the questions; Separate answer script must be fixed for Group-A and Group-B.]

#### Group-A

1.a) b)	What are the basic elements in an MIPS data path implementation?  Suppose you are given an instruction sub \$s2,\$t2,\$t3. Describe the complete operation of the datapath using the instruction with a figure.	3	CO1 CO1
c).	Why a single-cycle implementation is not used? justify the answer	3	CO2
2.a) b) c)	Design a single cycle data path for handling R-type instruction of MIPS.  Describe all temporary register and multiplexors of Multicycle Datapath  Do you think that the functions of memory data register and instruction register are the same? If not then how?	3 4 3	CO1 CO2 CO3
2. a) b)	Are instruction and microinstruction same? If not then why?  Illustrate the following instruction with three bus and two bus CPU system and explain which is better?	3 .	CO1 CO2
c) `	ADD R3, R2, R1  Set the control words for the following microinstructions:  i) SUB R1, R2, R0, ii) MAR← PC  Group-B	3	CO3
3.a)	Does any Pipeline Stall arise here for the following instructions, explain: MUL R1, R2, R3 SUB R3, 1, R4 ADD R4, R5, R6	4	CO2.5
b) c)	Which is advantageous between sequential processing and pipeline processing, explain?  How does data dependency effect in pipelining?	2 4	CO2 CO2
4.a)	Suppose you have 6 I/O devices in your computer. An output operation will be performed to the Device 4 (could be the keyboard) in the case of shared I/O arrangement. Explain with diagram how	5	co3
<b>6</b>	the operation will be performed.  Suppose you would like to bi-pass the CPU to perform a job. How a DMA can work in this regard?	5	CÓI
5.a) · b)	What is DMA controller? Describe the working principle of DMA controller? What is TLB? How can you reduce page fault explain logically?	3	CO1 CO3
c)	Write short note on handshaking protocol.  Or,	4	COI
5a)	Write short note on memory hierarchy.	3	COI
b)	Define cache hit, cache miss, hit rate, tag in caching.	. 4	COL
(i)	Suppose you would like to bi-pass the CPU to perform a job. How a DMA can work in this regard?	3	CO3