

I Add 25 and 35

MVI

MVI A, 25
MVI B, 35

ADD B

STA 50H

HLT

(Store the result in
50H memory location)

ADI

MVI A, 25

ADI 35H

STA 50H

HLT

MOV

-1) MOV A, M

MOV B, M

ADD B

STA 50H

HLT

MOV

MOV A, B

MOV C, D

ADD C

STA 50H

HLT

assume 8 bit data contents
of memory location pointed by
HL pair

content of B is 25
content of D is 35

memory

MVI H 20

MVI L 50

MOV A M

MVI H 22

MVI L 52

MOV B M

ADD B

STA 50H

HLT