TAFITA RAKOTOZANDRY

EMBEDDED SYSTEM ENGINEERING STUDENT

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EXPERIENCES

Design of a Heartbeat Monitor in FPGA, *Lafayette College*   Aug.2019-Dec.2019

- School Project
- Design and implementation of a FPGA based module that reads and display the heartbeat data on a 7 segments display

Technological Assistance, *ITS Department, USA*   Aug. 2018-Dec 2019

- Partial job on campus
- Technical assistance related to IT problems on campus

Development of a Monitoring System, *LAAS, France*   Feb. 2018-Mai 2018

- Last year school internship in LAAS
- Design and implementation of a network solution to monitor the Uninterruptible Power Supplies of the laboratory

Design of a Piezoelectric Sole, *Antananarivo*   Mar. 2017-Jul. 2017

- Personal Project
- Design and implementation of a prototype of a piezoelectric sole to convert the muscular displacement energy into an electrical energy

EDUCATION

Aug. 2018-Present

- **LAFAYETTE COLLEGE,** Easton PA

  Major : Electrical and Computer Engineering (2nd year)

Mar. 2015-Jui. 2018

- **ECOLE SUPERIEURE POLYTECHNIQUE,** Madagascar

  Licence en Génie Electrique

SKILLS

Analysis and calculation	Analog and Digital Circuit
Programming Languages	System Verilog (FPGA), C++, Java
Modeling and circuit design software	MATLAB, KiCad, Proteus
Microcontroller and nano-computer	Arduino, Raspberry Pi

LANGUAGES

English	Fluent	French	Fluent
German	Intermediate	Malagasy	Native