

Chapter 5a: CPUScheduling



Operating System Concepts – 10th Edition Silberschatz, Galvin and Gagne ©2018



Outline

- Basic Concepts

- Scheduling Criteria
- Scheduling Algorithms



Operating System Concepts – 10th Edition 5a.2 Silberschatz, Galvin and Gagne ©2018



Objectives

- Describe various CPU scheduling algorithms
- Assess CPU scheduling algorithms based on scheduling criteria
- Explain the issues related to multiprocessor and multicore scheduling
- Describe various real-time scheduling algorithms
- Describe the scheduling algorithms used in the Windows, Linux, and Solaris operating systems
- Apply modeling and simulations to evaluate CPU scheduling algorithms

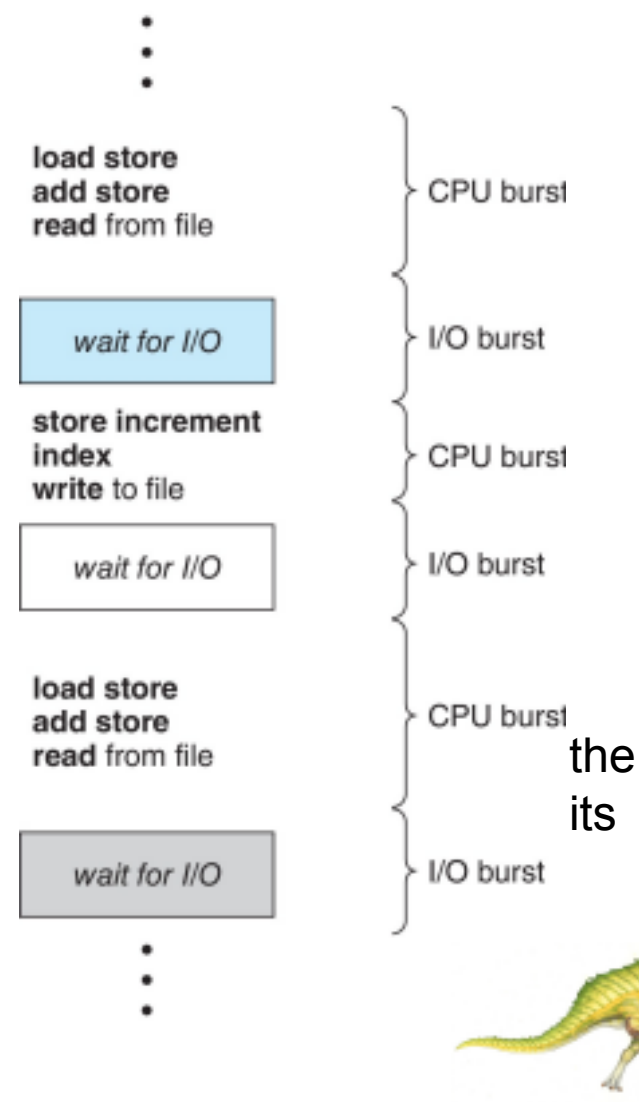


Operating System Concepts – 10th Edition
5a.3 Silberschatz, Galvin and Gagne ©2018



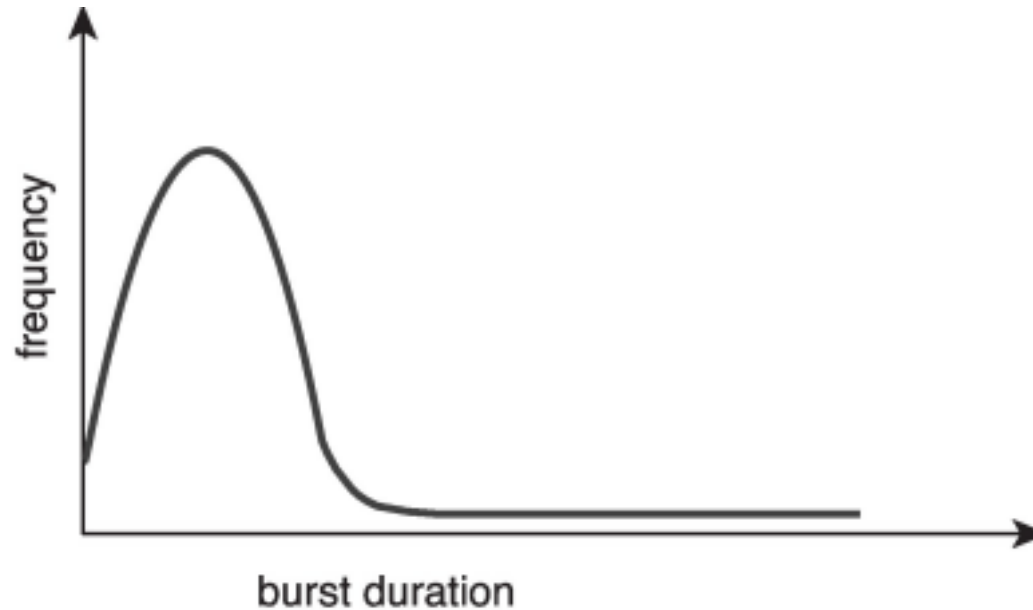
Basic Concepts

- The objective of multiprogramming is to have some process running at all times in processor, to maximize CPU utilization.
- All resources are scheduled before they are used.
- Scheduling is central to operating-system design
- Process execution consists of a cycle of CPU execution and I/O wait. ■ The state of process under execution is called CPU burst and state of process under I/O request & handling is called I/O burst.
- Processes alternate between these two states.



Histogram of CPU-burst Times

- Many short CPU bursts and few longer CPU bursts
- An I/O bound program would have many short CPU bursts
- CPU bound program might have few very long CPU bursts



Operating System Concepts – 10th Edition 5a.5 Silberschatz, Galvin and Gagne ©2018



CPU Scheduler

- The **short term scheduler** selects from among the processes in ready queue, and allocates a CPU core to one of them
- Queue not necessarily be FIFO, may be ordered in various ways using priority queue, a tree or unordered LL
- Records in queues are PCBs of ready processes
- CPU scheduling decisions may take place when a process:
 1. Switches from running to waiting state (I/O request, child process to complete)
 2. Switches from running to ready state (Interrupt occurs)
 3. Switches from waiting to ready (completion of I/O)
 4. Terminates
- For situations 1 and 4, there is no choice in terms of scheduling. A new process (if one exists in the ready queue) must be selected for execution.



- For situations 2 and 3, however, there is a choice.

Operating System Concepts – 10th Edition 5a.6 Silberschatz, Galvin and Gagne ©2018



Preemptive and Nonpreemptive Scheduling

- When scheduling takes place only under circumstances 1 and 4,

the scheduling scheme is **nonpreemptive**.

- Otherwise, it is **preemptive**.
- **Non - Preemptive Scheduling** – once the CPU has been allocated to a process, the process keeps the CPU until it releases the CPU either by terminating or by switching to the waiting state.
- Virtually all modern operating systems including Windows, MacOS, Linux, and UNIX use preemptive scheduling algorithms.



Operating System Concepts – 10th Edition 5a.7 Silberschatz, Galvin and Gagne ©2018



Preemptive Scheduling and Race Conditions

- **Preemptive Scheduling** – The process under execution, may be released from the CPU, in

- the middle of execution due to some inconsistent state of the process.
- Preemptive scheduling can result in race conditions when data are shared among several processes.
- Consider the case of two processes that share data. While one process is updating the data, it is preempted so that the second process can run. The second process then tries to read the data, which are in an inconsistent state.



Operating System Concepts – 10th Edition 5a.8 Silberschatz, Galvin and Gagne ©2018



Dispatcher

- Dispatcher is the module that gives control of the CPU to the process selected by the short term scheduler.;

this involves:

- Switching context
- Switching to user mode
- Jumping to the proper location in the

user program to restart that program ■ **Dispatch latency** – time it takes for the dispatcher to stop one process and start another running



Operating System Concepts – 10th Edition 5a.9 Silberschatz, Galvin and Gagne ©2018



Scheduling Criteria

- **CPU utilization** – keep the CPU as busy as possible. CPU Utilization can

range from 0 to 100 percent. In a real system, it should range from 40 to 90 percent

- **Throughput** – # of processes that complete their execution per time unit
- **Turnaround time** – amount of time to execute a particular process; The interval from the time of submission of a process to the time of completion is the turnaround time. Turnaround time is the sum of the periods spent waiting to get into memory, waiting in the ready queue, executing on the CPU, and doing I/O.
- **Waiting time** – amount of time a process has been waiting in the ready queue
- **Response time** – amount of time it takes from when a request was submitted until the first response is produced.



Operating System Concepts – 10th Edition
5a.10 Silberschatz, Galvin and Gagne ©2018



Scheduling Algorithm Optimization Criteria

- Max CPU utilization
- Max throughput

- Min turnaround time
- Min waiting time
- Min response time



Operating System Concepts – 10th Edition
5a.11 Silberschatz, Galvin and Gagne ©2018



First- Come, First-Served(FCFS) Scheduling

- Other names of this algorithm are: ■ First-In-First-Out (FIFO)
- Run-to-Completion
- Run-Until-Done
- First-Come-First-Served algorithm is the simplest scheduling algorithm. Processes are dispatched according to their arrival time on the ready queue. This algorithm is always nonpreemptive, once a process is assigned to CPU, it runs to completion.



First- Come, First-Served(FCFS) Scheduling

Process Burst Time

P_1 24

P_2 3

P_3 3

- Suppose that the processes arrive in the order: P_1, P_2, P_3 The Gantt Chart for the schedule is:



- Waiting time for $P_1 = 0$; $P_2 = 24$; $P_3 = 27$ ■ Average waiting time: $(0 + 24 + 27)/3 = 17$





FCFS Scheduling(Cont.)

Suppose that the processes arrive in the order:

P_2, P_3, P_1

- The Gantt chart for the schedule is:



(3) (3) (24)

- Waiting time for $P_1 = 6$; $P_2 = 0$; $P_3 = 3$
- Average waiting time: $(6 + 0 + 3)/3 = 3$ ■ Much better than previous case
- **Convoy effect** - short process behind long process • Consider one CPU-bound and many I/O-bound processes





FCFS Scheduling

- **Advantages :**

- more predictable than other schemes since it offers time
- code for FCFS scheduling is simple to write and understand

- **Disadvantages:**

- Short jobs(process) may have to wait for long time
- Important jobs (with higher priority) have to wait
- cannot guarantee good response time
- average waiting time and turn around time is often quite long
- lower CPU and device utilization.





Shortest-Job-First (SJF) Scheduling

- Associate with each process the length of its next CPU burst • Use these lengths to schedule the process with the shortest time
- SJF is optimal – gives minimum average waiting time for a given set of processes
 - The difficulty is knowing the length of the next CPU request
 - Could ask the user





Shortest-Job-First (SJF) Scheduling

- Associate with each process the length of its next CPUburst • Use these lengths to schedule the process with the shortest time
- SJF is optimal – gives minimum average waiting time for a given set of processes
- Preemptive version called **shortest-remaining-time-first** ■ How do we determine the length of the next CPUburst? • Could ask the user
 - Estimate





Example of SJF

Process

Burst Time

P_1 6

P_2 8

P_3 7

P_4 3



(3) (6) (7) (8)

Average waiting time = $(3 + 16 + 9 + 0) / 4 = 7$





Determining Length of Next CPU Burst

- Can only estimate the length – should be similar to the previous one
- Then pick process with shortest predicted next CPU burst
- Can be done by using the length of previous CPU bursts, using exponential averaging

$$\begin{aligned}
 & t_n \quad \text{1. actual length of CPU burst } t_n \\
 & \quad \quad \quad = \\
 & \quad \quad \quad \text{2. predicted value for the next CPU burst } \tau_{n+1} \\
 & \quad \quad \quad + \\
 & \quad \quad \quad \text{3. } \alpha \tau_{n-1} + (1 - \alpha) t_n \\
 & \quad \quad \quad \leq \tau_{n+1} \leq t_n
 \end{aligned}$$

4. Define:



Commonly, α set to $\frac{1}{2}$



Prediction of the Length of the Next CPU Burst



Operating System Concepts – 10th Edition 5a.20 Silberschatz, Galvin and Gagne ©2018



Examples of Exponential Averaging

- $\alpha = 0$
 - $T_{n+1} = T_n$
 - Recent history does not count
- $\alpha = 1$
 - $T_{n+1} = \alpha t_n$
 - Only the actual last CPU burst counts
- If we expand the formula, we get:

$$T_{n+1} = \alpha t_n + (1 - \alpha)\alpha t_{n-1} + \dots + (1 - \alpha)^j \alpha t_{n-j} + \dots + (1 - \alpha)^{n+1} T_0$$

- Since both α and $(1 - \alpha)$ are less than or equal to 1, each successive term has less weight than its predecessor



Example of Shortest-remaining-time-first

- Now we add the concepts of varying arrival times and preemption to the analysis

<u>Process</u>	<u>Arrival Time</u>	<u>Burst Time</u>
P_1	0	8

P_2 1 4

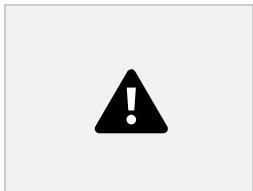
P_3 2 9

P_4 3 5

- Preemptive* SJF Gantt Chart



- Average waiting time = $[(10-1)+(1-1)+(17-2)+(5-3)]/4 = 26/4 = 6.5$



Round Robin(RR)

- Each process gets a small unit of CPU time (**time quantum** q), usually 10-100 milliseconds. After this time has elapsed, the process is preempted and added to the end of the ready queue.
- If there are n processes in the ready queue and the time quantum is q , then each process gets $1/n$ of the CPU time in chunks of at most q time units at once. No process waits more than $(n-1)q$ time units.
- Timer interrupts every quantum to schedule next process

Performance

- q large \Rightarrow FIFO
- q small \Rightarrow q must be large with respect to context switch, otherwise overhead is too high



Example of RR with Time Quantum=4

Process Burst Time

P_1 24

P_2 3

P_3 3

- The Gantt chart is:



- Typically, higher average turnaround than SJF, but better **response** ■ q should be large compared to context switch time • q usually 10 milliseconds to 100 milliseconds,
- Context switch < 10 microseconds





Time Quantum and Context Switch Time



ies WithTheTimeQuantum



80%of CPUbursts
should be shorter thanq





Priority Scheduling

- A priority number (integer) is associated with each process
- The CPU is allocated to the process with the highest priority (smallest integer \equiv highest priority)
 - Preemptive
 - Nonpreemptive
- SJF is priority scheduling where priority is the inverse of predicted next CPU burst time
 - Problem \equiv **Starvation** – low priority processes may never execute
- Solution \equiv **Aging** – as time progresses increase the priority of the process





Example of Priority Scheduling

<u>Process</u>	<u>Burst Time</u>	<u>Priority</u>
P_1	10	3
P_2	1	1
P_3	2	4
P_4	1	5
P_5	5	2

- Priority scheduling Gantt Chart



- Average waiting time = 8.2





Priority Schedulingw/ Round-Robin

Process

Burst Time

Priority

P_1 4 3

P_2 5 2

P_3 8 2

P_4 7 1

P_5 3 3

- Run the process with the highest priority. Processes with the same priority run round-robin
- Gantt Chart with time quantum = 2





Multilevel Queue ■ With priority scheduling, have separate queues for each priority. ■ Schedule the process in the highest-priority queue!





Multilevel Queue

■ Prioritization based
upon process type





Multilevel Feedback Queue

- A process can move between the various queues. ▪

Multilevel-feedback-queue scheduler defined by the following parameters:

- Number of queues
 - Scheduling algorithms for each queue
 - Method used to determine when to upgrade a process
 - Method used to determine when to demote a process
 - Method used to determine which queue a process will enter when that process needs service
- Aging can be implemented using multilevel feedback queue

Example of Multilevel Feedback Queue

- Three queues:
 - Q_0 – RR with time quantum 8 milliseconds
 - Q_1 – RR time quantum 16 milliseconds
 - Q_2 – FCFS

- Scheduling

- A new process enters queue Q_0
served in RR
 - 4 When it gains CPU, the process
receives 8 milliseconds
 - 4 If it does not finish in 8
milliseconds, the

which is
receives 8

- process is moved to queue Q_1
- At Q_1 job is again served in RR and receives 16 additional milliseconds
 - 4 If it still does not complete, it is preempted and moved to queue Q_2



Operating System Concepts – 10th Edition 5a.33 Silberschatz, Galvin and Gagne ©2018

End of Chapter 5a





Example of Multilevel Feedback Queue

Three queues:

- Q_0 – RR with time quantum 8 milliseconds
- Q_1 – RR time quantum 16 milliseconds
- Q_2 – FCFS

■ Scheduling

- A new process enters queue Q_0 which is served RR. When it gains CPU, the process receives 8 milliseconds

4 If it does not finish in 8 milliseconds, it is moved to queue Q_1

- At Q_1 the process is again served RR and receives 16 additional milliseconds

4 If it still does not complete, it is preempted and moved to queue Q_2

