

# OVS Offload using ASAP<sup>2</sup> Direct on ConnectX-4 / ConnectX-4 Lx Release Notes

**Rev 2.0** 

www.mellanox.com

#### NOTE:

THIS HARDWARE, SOFTWARE OR TEST SUITE PRODUCT ("PRODUCT(S)") AND ITS RELATED DOCUMENTATION ARE PROVIDED BY MELLANOX TECHNOLOGIES "AS-IS" WITH ALL FAULTS OF ANY KIND AND SOLELY FOR THE PURPOSE OF AIDING THE CUSTOMER IN TESTING APPLICATIONS THAT USE THE PRODUCTS IN DESIGNATED SOLUTIONS. THE CUSTOMER'S MANUFACTURING TEST ENVIRONMENT HAS NOT MET THE STANDARDS SET BY MELLANOX TECHNOLOGIES TO FULLY QUALIFY THE PRODUCT(S) AND/OR THE SYSTEM USING IT. THEREFORE, MELLANOX TECHNOLOGIES CANNOT AND DOES NOT GUARANTEE OR WARRANT THAT THE PRODUCTS WILL OPERATE WITH THE HIGHEST QUALITY. ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT ARE DISCLAIMED. IN NO EVENT SHALL MELLANOX BE LIABLE TO CUSTOMER OR ANY THIRD PARTIES FOR ANY DIRECT, INDIRECT, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES OF ANY KIND (INCLUDING, BUT NOT LIMITED TO, PAYMENT FOR PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY FROM THE USE OF THE PRODUCT(S) AND RELATED DOCUMENTATION EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.



Mellanox Technologies 350 Oakmead Parkway Suite 100 Sunnyvale, CA 94085 U.S.A. www.mellanox.com

Tel: (408) 970-3400 Fax: (408) 970-3403

© Copyright 2016. Mellanox Technologies Ltd. All Rights Reserved.

Mellanox®, Mellanox logo, Accelio®, BridgeX®, CloudX logo, CompustorX®, Connect-IB®, ConnectX®, CoolBox®, CORE-Direct®, EZchip®, EZchip logo, EZappliance®, EZdesign®, EZdriver®, EZsystem®, GPUDirect®, InfiniHost®, InfiniScale®, Kotura®, Kotura®, Kotura logo, Mellanox Federal Systems®, Mellanox Open Ethernet®, Mellanox ScalableHPC®, Mellanox TuneX®, Mellanox Connect Accelerate Outperform logo, Mellanox Virtual Modular Switch®, MetroDX®, MetroX®, MLNX-OS®, NP-1c®, NP-2®, NP-3®, Open Ethernet logo, PhyX®, PSIPHY®, SwitchX®, Tilera®, Tilera logo, TestX®, TuneX®, The Generation of Open Ethernet logo, UFM®, Virtual Protocol Interconnect®, Voltaire® and Voltaire logo are registered trademarks of Mellanox Technologies, Ltd.

All other trademarks are property of their respective owners.

For the most updated list of Mellanox trademarks, visit http://www.mellanox.com/page/trademarks

## **Table of Contents**

Table of Co	ntents	1
	es	
Release Up	date History	2
Chapter 1	Overview	3
•	1.1 Package Contents	
	1.2 Supported Platforms and Operating Systems	3
	1.2.1 Supported Hypervisors	3
	1.2.2 Supported Guest OSs	3
	1.2.3 Firmware Versions	4
Chapter 2	Changes and New Features in Rev 2.0	5
Chapter 3	Known Issues	6
Chapter 4	Bug Fixes History	7
	Change Log History	
	Offload Limitations	

## **List of Tables**

Table 1:	Release Update History	. 2
Table 2:	Supported Hypervisors (KVM / x86_64)	.3
Table 3:	Supported Guest (x86_64)	.3
Table 4:	Firmware Versions	.4
Table 5:	Changes and New Feature in Rev 2.0	. 5
Table 6:	Known Issues	.6
Table 7:	Fixed Bugs List	. 7
Table 8:	History of Changes and New Features	. 8

## **Release Update History**

Table 1 - Release Update History

Release	Date	Description
2.0	July 2016	Initial release of this version

#### 1 Overview

These are the release notes of Open vSwitch (OVS) offload using Mellanox "Accelerated Switching And Packet Processing" (ASAP<sup>2</sup>) Direct technology for Linux and ConnectX-4 / ConnectX-4 Lx cards.



This release was optimized for up to 10000 flows. Future releases will further optimize current performance and address larger flow count.

#### 1.1 Package Contents

OVS Offload using ASAP<sup>2</sup> Direct for Linux and ConnectX-4/ConnectX-4 Lx cards software contains the following components:

- Firmware (for the supported versions, please refer to Section 1.2.3, "Firmware Versions", on page 4)
- Host kernel DEB/RPM v4.2.3\_ovs\_offload\_r24 (based on upstream kernel 4.2.x)
- Mellanox OFED package for the VM, v3.2-2.0.3.0.7
- MLNX\_DPDK\_2.2.0-MR

#### 1.2 Supported Platforms and Operating Systems

#### 1.2.1 Supported Hypervisors

The following are the supported Hypervisors distributions (KVM/x86\_64) over which the OVS kernel should be installed:

Table 2 - Supported Hypervisors (KVM / x86 64)

<b>Operating System</b>	Platform
RHEL7.2	x86_64
Ubuntu 15.04	x86_64
Ubuntu 15.10	x86_64

#### 1.2.2 Supported Guest OSs

The following are the supported Guest distributions (x86\_64) over which MLNX OFED and DPDK should be installed:

Table 3 - Supported Guest (x86\_64)

Operating System	Platform
RHEL7.2	x86_64
Ubuntu 15.04	x86_64
Ubuntu 15.10	x86_64

#### 1.2.3 Firmware Versions

OVS Offload using  $ASAP^2$  Direct on ConnectX-4/ConnectX-4 Lx Rev 2.0 requires the following firmware for Mellanox NICs:

Table 4 - Firmware Versions

NICs	Recommended Firmware Rev.	Additional Firmware Rev. Supported
ConnectX®-4	12.16.2204 (at beta level)	N/A
ConnectX®-4 Lx	14.16.2204 (at beta level)	N/A

## 2 Changes and New Features in Rev 2.0

Table 5 - Changes and New Feature in Rev 2.0

Feature/Change	Description
Offloading Scalability Enhancements	Added support for offloading large amounts of flows (optimized for up to ten thousand flows) by optimizing firmware and driver. This includes lockless and bulking approaches for efficient gathering of HW flow statistics under a large scale of offloaded flows in the driver, and optimized tables and rules setup in the firmware.

## 3 Known Issues

The following table provides a list of known issues in regards to this release.

Table 6 - Known Issues

Internal Ref.	Issue
768065	<b>Description:</b> On some VF drivers, the VF MTU cannot be reduced below the current PF MTU.
	<b>WA</b> : Return the Physical Function (PF) MTU to 1500 bytes after reducing the VF MTU.
	Keywords: MTU
-	<b>Description:</b> libmlx5 (e.g used by DPDK) running over VFs is not aware to the required minimal inline size as configured by the PF, unless directed by the VM user.
	<b>Note:</b> This value needs to be set according to the "flow_offload_min_inline" parameter configured on the Hypervisor as the HW will enforce it.  This means:
	<ul> <li>Kernel: The driver in MLNX_OFED v3.2-2.0.3.0.3 package will perform this automatically</li> <li>Userspace: The SW needs to be aware of it and copy headers inline accordingly. This</li> </ul>
	parameter will perform it in case of DPDK.
	WA: If non-default (L3 or L4) matching on outgoing VF traffic is required, export the MLX-5_WQE_MIN_INLINE_SIZE=N environment variable to the processes running libmlx5. Where N is the number of header bytes needed for the required matching:  • for L3 matching use 34 (14+20)  • for L4 UDP port matching use 42 (14+20+8)  • for L4 TCP port matching use 74 (14+20+40) if TCP time-stamping is enabled and  • 66 (14+20+32) if TCP TS is disabled
	Keywords: libmlx5, minimal inline size
-	<b>Description:</b> Learning a few thousand new flows per second may not work properly.
	WA: Avoid getting OVS to learn over 2000 new flows per second
	Keywords: Offload flows

## 4 Bug Fixes History

Table 7 lists the bugs fixed in this release.

Table 7 - Fixed Bugs List

Internal Ref.	Issue
758724	<b>Description:</b> Fixed the issue where MLNX_OFED v3.2 kernel driver failed to start Virtual Function (VF) netdevs when the host ran in OVS offloads mode.
	Keywords: VF netdevs, MLNX_OFED, OVS offloads mode
	Discovered in Release: 1.0
	Fixed in Release: 2.0
766780	<b>Description:</b> Fixed the issue where learning new flows under very heavy packet rate might have not worked properly.
	Keywords: Heavy packet rate, Offload flows
	Discovered in Release: 1.0
	Fixed in Release: 2.0

## 5 Change Log History

Table 8 - History of Changes and New Features

Rev.	Feature/ Change	Description
1.0	OVS Offload	Mellanox Accelerated Switching And Packet Processing (ASAP <sup>2</sup> ) Direct technology allows to offload OVS by handling OVS data-plain in Mellanox ConnectX-4/ConnectX-4 Lx NIC hardware (Mellanox Embedded Switch or eSwitch) while maintaining OVS control-plain unmodified. The current actions supported by ASAP <sup>2</sup> Direct include packet parsing and matching, forward, drop along with VLAN push/pop or VXLAN encap/decap and HW based packet/byte flow statistics.
	VXLAN encap/decap	Virtual Extensible LAN (VXLAN) is a network virtualization technology that improves scalability problems associated with large cloud computing deployments. It tunnels Ethernet frames within Ethernet + IP + UDP frames.  Mellanox implements VXLAN encapsulation and decapsulation in hardware.

### 6 Offload Limitations

The following is a list of offload limitations in regards to this release.

- Changing SR-IOV e-switch offloads mode (on --> off and off --> on) requires all VFs to be unbinded
- VXLAN encap/decap offloading of OVS actions is supported only in ConnectX-4 Lx
- UDP destination port for offloading VXLAN must be 4789
- When offloading VXLAN, the Guest MTU must be reduced to account for the encap headers (1500 --> 1450), as IP fragmentation for encap offloads is not supported. If this is not applied, VF packets are silently dropped at the port
- RoCE for VFs is not supported when VXLAN encap/decap offloads are enabled
- FIB updates are not supported for VXLAN offloads. If a flow is offloaded and then the IP of the PF is changed, it will not be un-offloaded, and vice versa
- Only a single VLAN per VM is supported for offloading VLAN push/pop actions
- Offloaded flows must match on the destination MAC
- Using OVS in a non-learning manner (e.g by deleting the Open-Flow normal rule) causes packets that do not match any of the rules to be dropped, and not be offloaded because the derived data-path rule does not match on dmac
- Offloading matches on L3 or L4 (TCP or UDP) headers for outgoing flows (the source port is a VF, not wire), requires administrative directive to the PF driver to set "min inline size" for the VFs
- Flows containing the following matching attributes are not offloaded: ARPs, IPv6, TCP flags, IP fragment type