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printable/es1.s
              Thu Sep 19 12:24:50 2019
# File: es1.s
   Contains the Assembly translation for esl.cpp.
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 Created on 24/08/2019.
#******************
#-----
.GLOBAL _ZN2clC1EcR3st1
                                            # cl::cl(char c, st1 & s2)
#-----
# activation record:
# &s2 -24
# c -16
# C
# i
            -12
# &this -8
# %rbp 0
            -8
#-----
_ZN2clC1EcR3st1:
# set stack locations labels
   .set this, -8
.set i, -12
.set c, -16
   .set s2, -24
# prologue: activation frame
   pushq %rbp
   movq %rsp, %rbp
   subq $24, %rsp
                               # reserve stack space for actual arguments
# copy actual arguments to the stack
   movq %rdi, this(%rbp)
   movq %rsi, c(%rbp)
   movq %rdx, s2(%rbp)
# for loop 1 initialization
   movl $0, i(%rbp)
                              \# i = 0
for1:
   cmpl $8, i(%rbp)
                              \# check if i < 4
                              \# end for loop (i >= 8)
   jge finefor1
# for loop 1 body
   movq this(%rbp), %rdi # this -> %rdi
   movslq i(%rbp), %rcx
   movb c(%rbp), %al addb i(%rbp), %al
                          # c -> %al
                              # c + i -> %al
   movb %al, (%rdi, %rcx, 1)
                             # s.vc[i] = c + i
                              # i++
   incq i(%rbp)
        for1
                              # loop again
   qmj
finefor1:
# for loop 2, initialization
                             \# i = 0
   movl $0, i(%rbp)
for2:
   cmpl $4, i(%rbp)
                              # check if i < 4</pre>
   jge finefor2
                             \# end for loop (i >= 4)
# for loop 2, body
   movq this(%rbp), %rdi  # this -> %rdi  movslq i(%rbp), %rcx  # i -> %rcx  # i -> %rci  # 562 -> %rci
   movq s2(%rbp), %rsi
                              # &s2 -> %rsi
   movb (%rsi, %rcx, 1), %al # s.vc[i] -> %al subb (%rdi, %rcx, 1), %al # s2.vc[i] - s.vc[i] -> %al
   movb
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   movsbl %al, %eax
                                  # %al -> %eax
   movl %eax, 8(%rdi, %rcx, 4) # v[i] = s2.vc[i] - s.vc[i];
   incl i(%rbp)
                                 # i++
         for2
   jmp
                                  # loop again
finefor2:
   movq this(%rbp), %rax
                                # return initialized object address
   leave
                                  # movq %rbp, %rsp; popq %rbp
.GLOBAL _ZN2cl5elab1E3st1R3st2 # void cl::elab1(st1 s1, st2 & s2)
#-----
# activation record:
            -52
        -32
-48
-40
# cla_s
# cla_v
  &s2
             -24
# s1 LSB -10
# s1 MSB -12
# &this -8
# %rbp 0
  s1 LSB
             -16
            -12
#-----
_ZN2cl5elab1E3st1R3st2:
# set stack locations labels
   .set this, -8
   .set s1, -16
.set s2, -24
   .set cla_v, -40
   .set cla_s, -48
   .set i,
             -52
# prologue: activation frame
   pushq %rbp
   movq %rsp, %rbp
   subq $56, %rsp
                                  # reserve stack space for actual arguments
# copy actual arguments to the stack
   movq %rdi, this(%rbp)
   movq %rsi, s1(%rbp)
   movq %rdx, s2(%rbp)
# prepare actual arguments to call constructor
   leaq -48(%rbp), %rdi  # &cla
movb $'f', %sil  # 'f'
   leaq s1(%rbp), %rdx
call _ZN2clC1EcR3st1
                                  # s1
                                  # cl cla('f', s1);
# for loop, initialization
   movl $0, i(%rbp)
                                  # i = 0
for:
   cmpl $4, i(%rbp)
                                  # i < 4
   jge finefor
                                  \# end loop (i >= 4)
# for loop, body
   movslq i(%rbp), %rcx
   movq this(%rbp), %rsi
   leaq s1(%rbp), %rdi
                              # s1.vc[i] -> %bl
# s.vc[i] -> %al
   movb (%rdi, %rcx, 1), %bl
   movb (%rsi, %rcx, 1), %al
   cmpb %al, %bl
                                  # if (s.vc[i] > s1.vc[i])
   jl
         fineif1
                                  # exit if
# if1 body
   leaq cla_s(%rbp), %rdi
                                  # &cla.s.vc[i] -> %rsi
```

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          (%rdi, %rcx, 1), %al
                                   # cla.s.vc[i] -> %cl
   movb
   movb (%rdi, %rcx, 1), %al # cla.s.vc[i] -> %cl
movb %al, (%rsi, %rcx, 1) # s.vc[i] = cla.s.vc[i];
fineif1:
#if2:
   leaq cla_v(%rbp), %rsi
   movl (%rsi, %rcx, 4), %eax
   movq this(%rbp), %rdi
   movl 8(%rdi, %rcx, 4), %ebx
                                 # this.v[i] -> %ebx
   cmpl %ebx, %eax
                                   # if (v[i] > cla.v[i])
   jl fineif2
                                   # exit if
# if2 body
   addl i(%rbp), %eax
                                   # cla.v[i] + i -> %eax
   movl %eax, 8(%rdi, %rcx, 4)
fineif2:
   incl i(%rbp)
jmp for
                                    # i++
                                    # loop again
finefor:
                                    # movq %rbp, %rsp; popq %rbp
   leave
   ret
#***********************
```