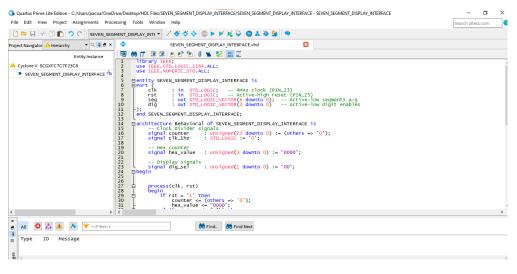
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VHDL CODE FOR SEVEN SEGMENT DISPLAY INTERFACE



```
CODE:
library IEEE;
use IEEE.STD LOGIC 1164.ALL;
use IEEE.NUMERIC STD.ALL;
entity SEVEN SEGMENT DISPLAY INTERFACE
                                                   is
Port (
  clk
       : in STD LOGIC; -- 4MHz clock (PIN 23)
       : in STD LOGIC; -- Active-high reset (PIN 25)
  rst
       : out STD LOGIC VECTOR(6 downto 0); -- Active-low segments a-g
  seg
       : out STD LOGIC VECTOR(3 downto 0) -- Active-low digit enables
  dig
);
end SEVEN SEGMENT DISPLAY INTERFACE;
architecture Behavioral of SEVEN SEGMENT DISPLAY INTERFACE is
  -- Clock divider signals
  signal counter : unsigned(23 downto 0) := (others \Rightarrow '0');
  signal clk 1hz
                : STD LOGIC := '0';
  -- Hex counter
  signal hex value : unsigned(3 downto 0) := "0000";
  -- Display signals
  signal dig sel
                : unsigned(1 downto 0) := "00";
begin
```

```
process(clk, rst)
begin
  if rst = '1' then
    counter \leq (others \Rightarrow '0');
    hex value \leq 0000";
  elsif rising edge(clk) then
    if counter = x"F42400" then -- 0.25Hz (4 seconds per count)
       counter \leq (others \Rightarrow '0');
       hex value \leq hex value + 1;
    else
       counter <= counter + 1;
    end if;
  end if;
end process;
-- Digit scanner (244Hz refresh)
dig sel \leq counter(15 downto 14); -- 4MHz/2^16 = \sim244Hz
-- Single-digit display (all digits show same value)
dig \le "1110" when dig sel = "00" else -- DIG1
    "1101" when dig sel = "01" else -- DIG2
    "1011" when dig sel = "10" else -- DIG3
    "0111";
                            -- DIG4
-- Active-low hex decoder (common cathode)
with hex value select
  seg \le "0000001" when x"0", -- 0
      "1001111" when x"1", -- 1
      "0010010" when x"2", -- 2
      "0000110" when x"3", -- 3
      "1001100" when x"4", -- 4
      "0100100" when x"5", -- 5
      "0100000" when x"6", -- 6
      "0001111" when x"7", -- 7
      "0000000" when x"8", -- 8
      "0000100" when x"9", -- 9
      "0001000" when x"A", -- A
      "1100000" when x"B", -- B
      "0110001" when x"C", -- C
      "1000010" when x"D", -- D
      "0110000" when x"E", -- E
```

"0111000" when others; -- F end Behavioral;