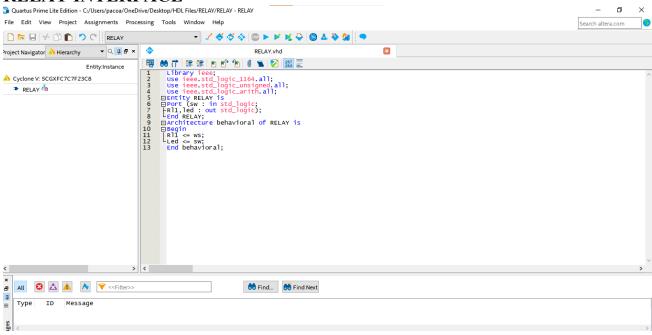
## PACO, ARMIN R. BSCpE – 3A

## **RELAY INTERFACE**



## **CODE:**

Library ieee;

Use ieee.std\_logic\_1164.all;

Use ieee.std logic unsigned.all;

Use ieee.std logic arith.all;

Entity relay is

Port (sw : in std\_logic;

Rl1,led: out std\_logic);

End relay;

Architecture behavioral of relay is

Begin

 $R11 \le ws;$ 

 $Led \le sw;$ 

End behavioral;

## PIN ASSIGNMENT

XC3128TQ144			
Connector	Device pin	Property	
P 18/3	41	Sw	
P 18/5	43	RI1	
P 18/21	62	Led	

XC2S100TQ144-5		
Connector	Device pin	Property
P 18/3	1	Sw
P 18/5	5	RI1
P 18/21	23	Led