



# Learning Maps

Cadence Training Services learning maps provide a comprehensive visual overview of the learning opportunities for Cadence customers. They provide recommended course flows as well as tool experience and knowledge levels to guide students through a complete learning plan. Learning Maps cover all Cadence® technologies and reference courses available worldwide. For course names, descriptions, and schedules, please select the Browse Catalog button at <https://www.cadence.com/training>.

## Contents

- PCB Design and Analysis
- Custom IC, Analog, and RF Design
- Digital Design and Signoff
- System Design and Analysis
- IC Package Design and Analysis
- Tensilica® Processor IP
- Computational Fluid Dynamics

# PCB Design and Analysis Learning Map

Beginner

Advanced



Beginner

Advanced



New Course



Number of days for instructor-led course



Online Course Available



Digital Badge Available

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# IC Package Design and Analysis Learning Map

Beginner

Advanced

## IC Package Design

SiP Layout



Allegro® Package Designer



Allegro FPGA System Planner



Allegro Sigrity Package Assessment and Model Extraction



OrbitIO™ System Planner



Advanced Design Verification with the RAVEL Programming Language NEW



Allegro Package Designer Plus



## SI/PI Analysis

Allegro Sigrity™ SI Foundations



Allegro Sigrity PI



Sigrity PowerDC™ and OptimizePI™



SystemSI for Parallel Bus and Serial Link Analysis NEW



Model Generation and Analysis using PowerSI and Broadband SPICE



Clarity 3D Solver



Celsius Thermal Solver



Beginner

Advanced

NEW

New Course



Number of days for instructor-led course



Online Course Available



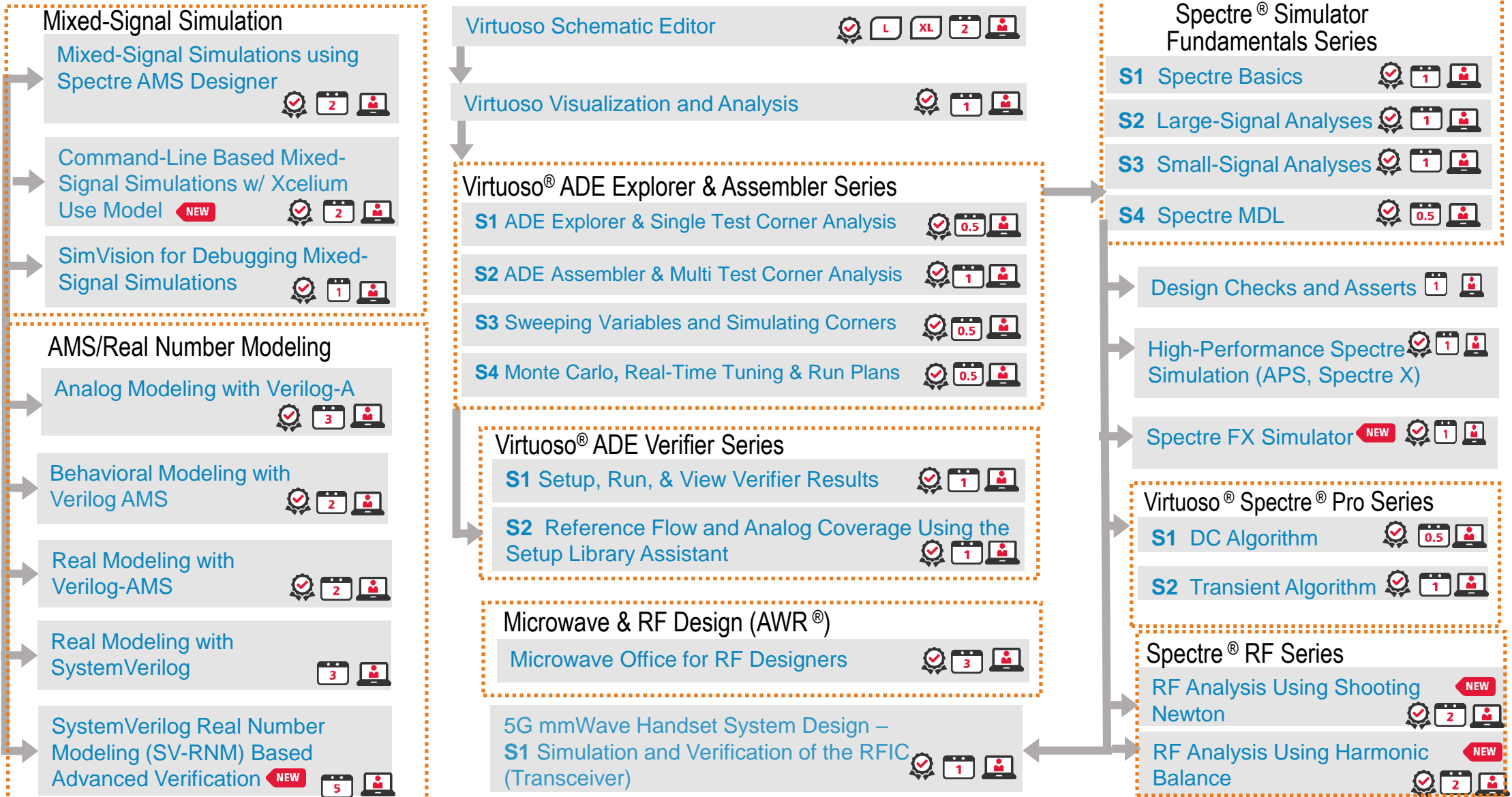
Digital Badge Available

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Beginner  
Advanced

## Circuit Modeling, Analog/Mixed-Signal/RF Circuit Design and Simulation

Beginner  
Advanced

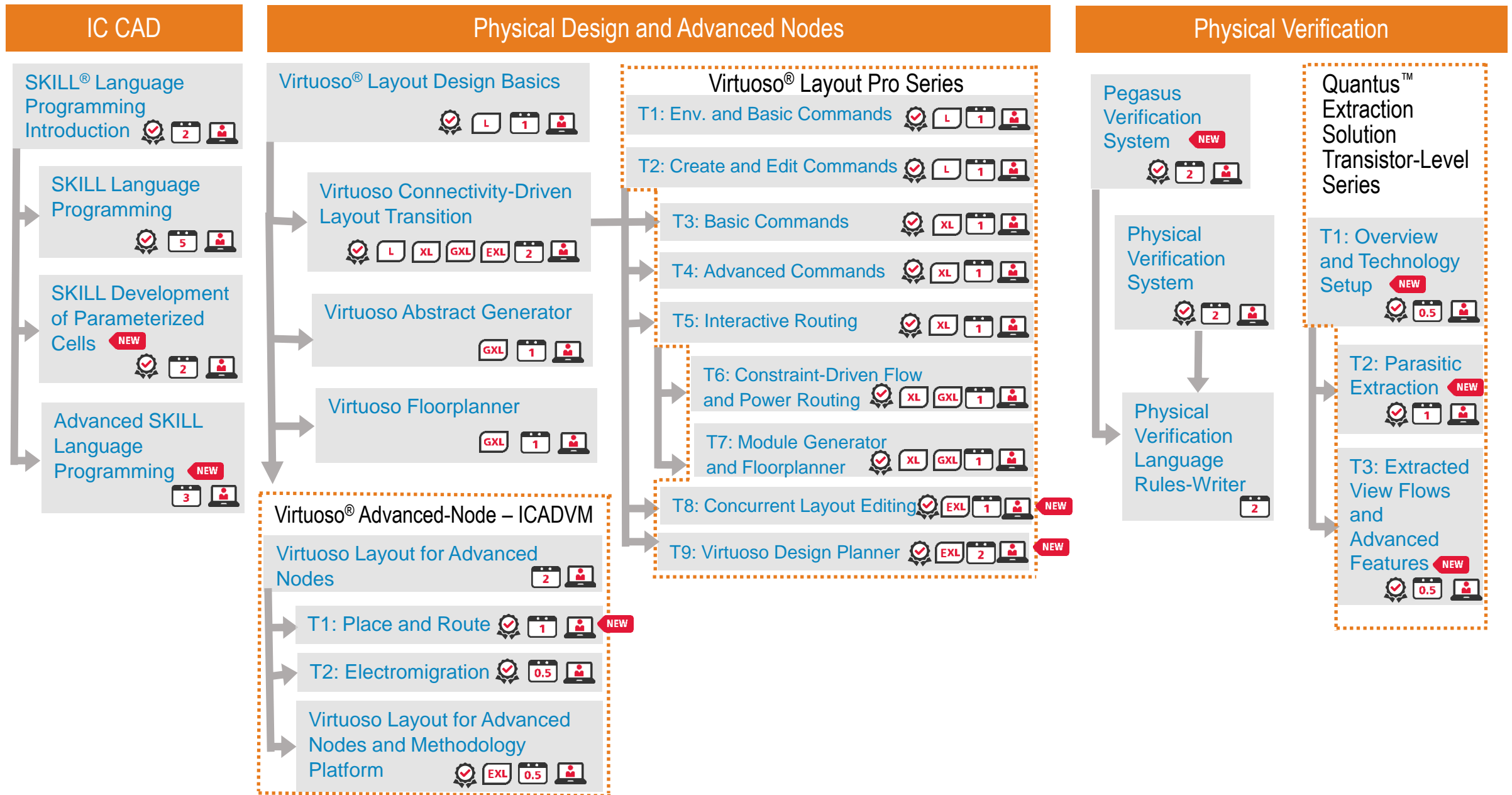


Beginner

Advanced

Beginner

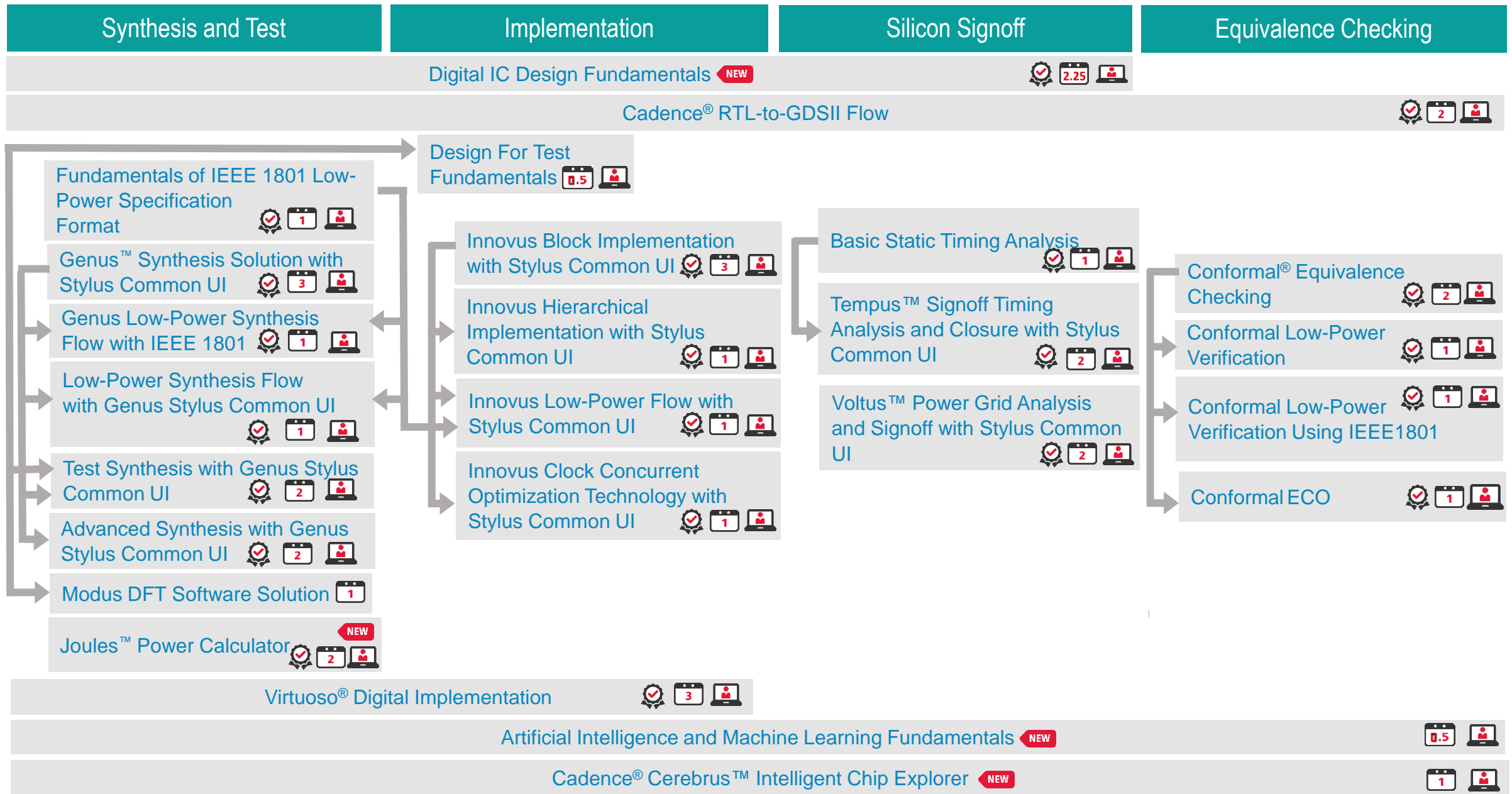
Advanced





# Digital Design and Signoff Learning Map

Beginner



Beginner

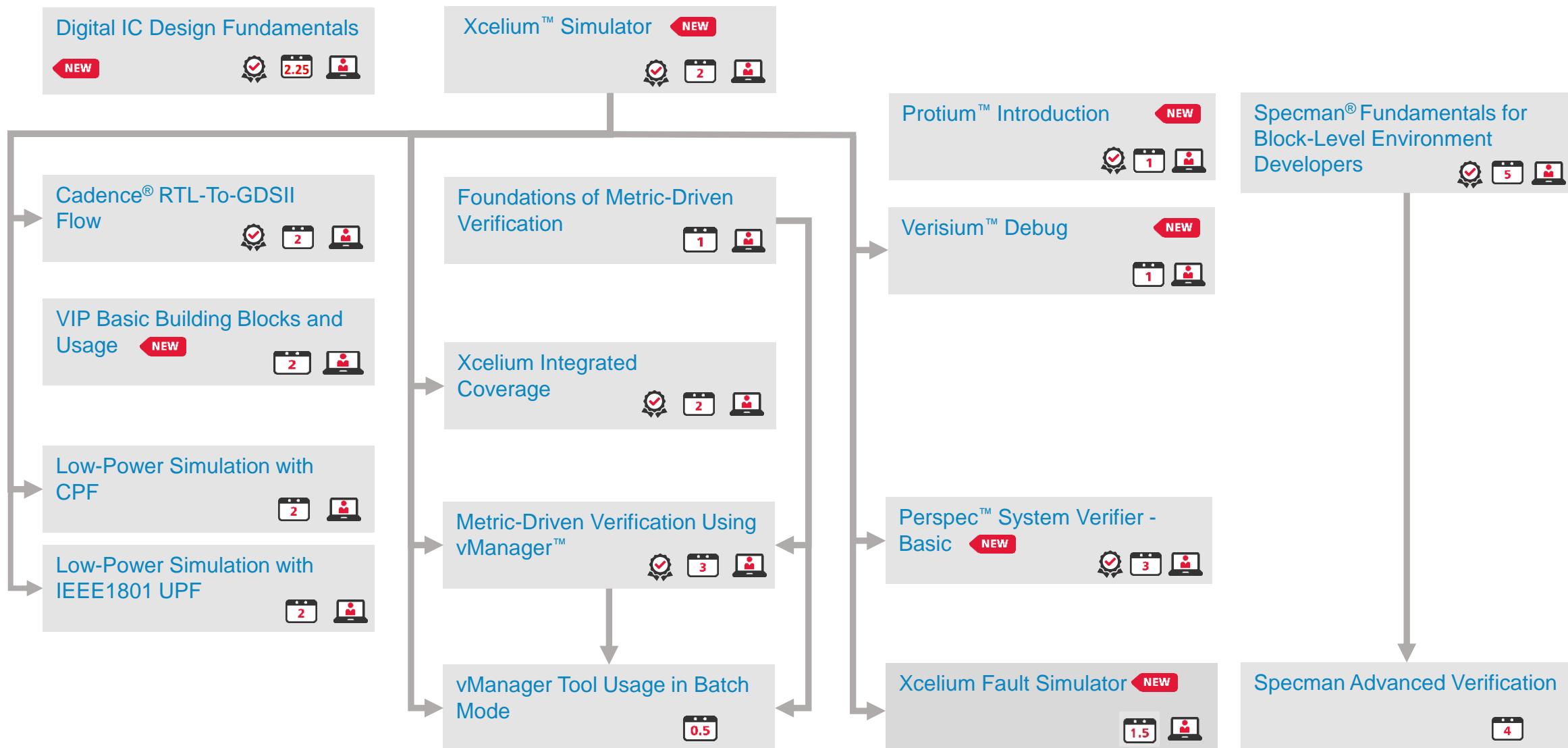
Advanced

# System Design and Verification Learning Map

Beginner

Advanced

## Simulation, Coverage and Debug

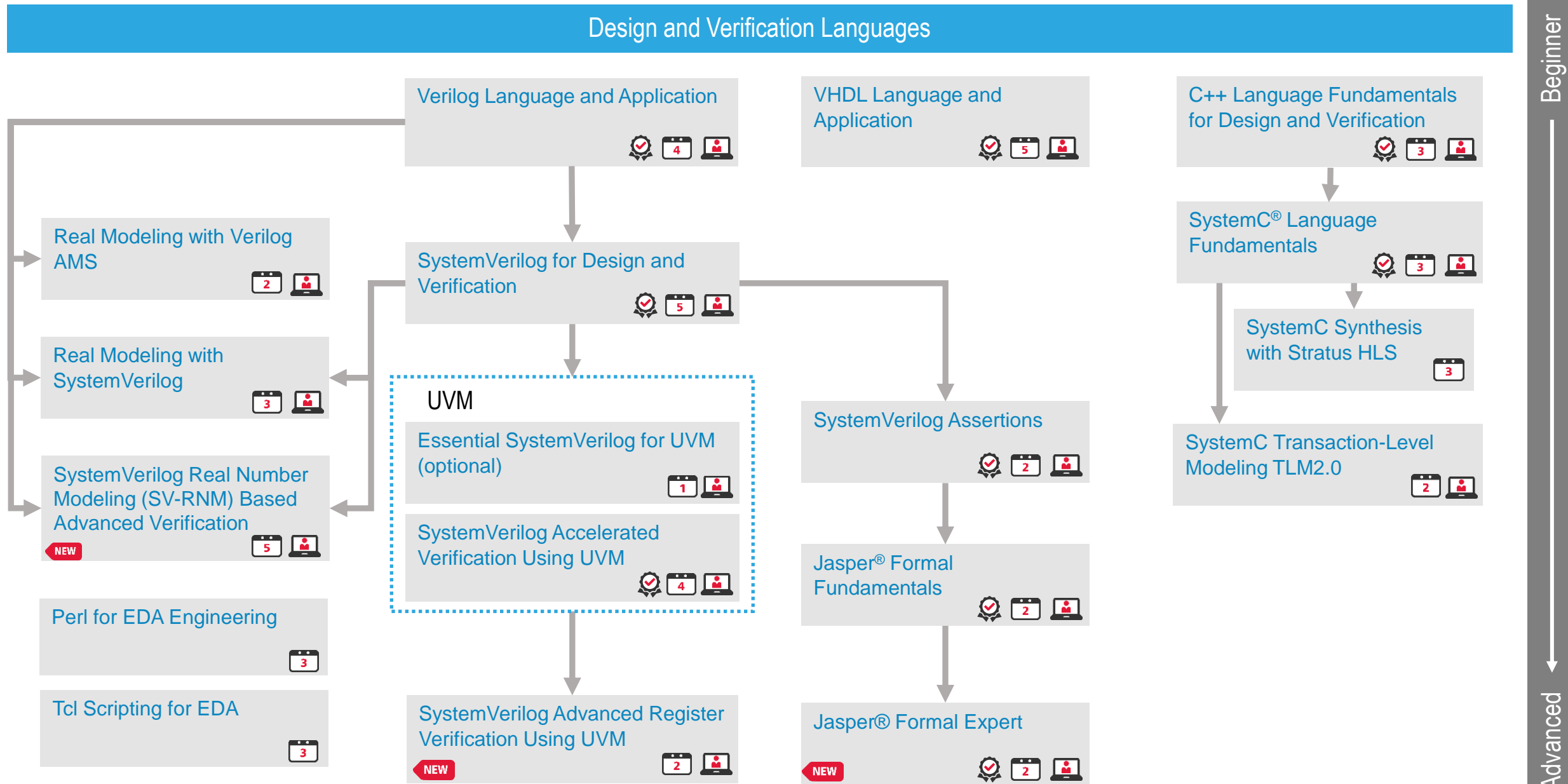


Beginner

Advanced

# System Design and Verification Learning Map

Beginner



Beginner

Advanced



# Safety and Reliability Platform Learning Map

Beginner

Beginner

## MIDAS Safety Platform

MIDAS™ Safety Platform Introduction

NEW



Midas™ Safety Analysis Authoring

NEW



Xcelium Fault Simulator

NEW



Functional Safety Implementation with  
Midas™

NEW



Advanced

Advanced

NEW

New Course



Number of days for instructor-led course



Tiers of Cadence products used in course



Online Course Available



Digital Badge Available

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# Tensilica Processor IP Learning Map

1 of 2 – see next page



## Tensilica Xtensa LX

## ConnX DSP

## Fusion & FloatingPoint DSP

## HiFi Audio DSP

## Vision DSP

Tensilica® Xtensa® LX  
Processor Fundamentals



Tensilica Xtensa LX  
Processor Interfaces



Tensilica Xtensa LX  
Hardware Verification and  
EDA



Tensilica Instruction  
Extension Language and  
Design



Tensilica System  
Modeling using XTSC



Tensilica ConnX BBE16EP  
Baseband Engine



Tensilica ConnX BBE32EP  
Baseband Engine



Tensilica ConnX BBE64EP  
Baseband Engine



Tensilica ConnX 110 and  
120 DSP Family



Tensilica Fusion F1 DSP



Tensilica Fusion G3 DSP



Tensilica Fusion G6 DSP



Tensilica FloatingPoint  
DSP Family



Tensilica Audio Codec API



Tensilica HiFi 2/EP/Mini  
Audio Engine ISA



Tensilica HiFi 3 Audio  
Engine ISA



Tensilica HiFi 4 DSP



Tensilica HiFi 5 DSP



Tensilica Xtensa Audio  
Framework



Tensilica Vision DSP Family



Tensilica Xtensa Neural  
Network Compiler v2



Tensilica DNA 100  
Architecture and  
Programming



NEW

New Course



Number of days for instructor-led course



Online Course Available

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## Tensilica Xtensa NX

## ConnX DSP

## Vision DSP

Tensilica® Xtensa® NX  
Processor Fundamentals



Tensilica Xtensa NX  
Processor Interfaces



Tensilica Xtensa NX  
Hardware Verification and  
EDA



Tensilica Instruction  
Extension Language and  
Design



Tensilica System  
Modeling using XTSC



Tensilica ConnX B10  
DSP



Tensilica ConnX B20  
DSP



Tensilica Vision DSP Family



Tensilica Xtensa Neural  
Network Compiler v2



# Computational Fluid Dynamics

Beginner

Advanced

Fidelity		
Turbomachinery	Meshing	Auto Aero

Fine
Marine

Fidelity Turbo: Introduction



Fidelity Automesh for  
Unstructured Meshing



Fidelity Flow



Fine Marine for Beginners



Fine Marine for Advanced Users



Beginner

Advanced



New Course



Number of days for instructor-led course



Online Course Available

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