

① Assembly  
Instruction  
MVI A, 0BH  
~~00~~

SIM

EI

JMP 2004H

Memory  
Address (Hex)

Machine  
Language (Hex)

2000

3E

2001

0B

2002

30

2003

FB

2004

C3

2005

04

2006

20

CALL Eror

2100

CD

2101

55

2102

04

HLT

2103

76

JMP 2100H

27BD

C3

27BE

00

27BF

21



For RST 7.5, it is feasible using 27BDH memory location <sup>to jump</sup> for interrupt service routine. We executed the following interrupt service routine with the help of VEC INT key and also using jump wires to give +5V to 7.5 pin and the following service routine works. We have stored 0BH at accumulator to demask the M7.5 interrupt mask.

For RST 6.5 it is feasible using 27B7H memory location to jump for interrupt service routine. We executed the following interrupt service routine with the help of jump wires to give +5V to 6.5 pin and the following service routine works. We have stored ~~0BH~~ 0DH at accumulator to demask M 6.5 interrupt mask.

A.J  
30/08/2024