

EMC Test Report For

< John Doe >

ULR - TC52281A000000XXF

Test Report No.: TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY



Certificate No.: TC-5228

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EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 1 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

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EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 2 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Amendment History

Revision No.	Date of Amend.	Amendment made	Reasons	Approved by
1.0	01/03/2021	Laboratory name and logo, Tata power sed is replaced with Tata advanced systems limited	Due to organization name change from TPSED to TASL	QM
2.0	05/04/2021	Reference no updated for use of NABL Accredited CAB combined ILAC MRA Mark ,included Test Report prepared by & EMC 32 software version is updated to 10.6	To update Ref no provided by NABL for use of NABL Accredited CAB combined ILAC MRA Mark & Internal review of test report format	QM

EMC CENTRE ACCREDITATION DETAILS

The accreditation details in the below table

Accreditation Bodies	Certificate Number
National Accreditation Board for Testing and Calibration Laboratories (NABL) ,as per ISO/IEC 17025 :2017	TC-5228
Agreement for use of NABL Accredited CAB combined ILAC MRA Mark	Ref No: NABL/ILAC/0876
Telecommunication Engineering Centre (TEC) Department of Telecommunications Government of India	TEC/MRA/CAB/IND-D/15 CAB Identification : IND015

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 2 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Table of Contents

1. General Information.....	
2. Test Summary.....	
3. Equipment under Test Description.....	
4. Performance Monitoring Parameters.....	
Annexure 1: CE01/CE101- Conducted Emissions, Power Leads.....	
Annexure 2: CE03/CE102- Conducted Emissions, Power Leads.....	
Annexure 3: CE06/CE106- Conducted Emissions, Antenna Terminal.....	
Annexure 4: RE01/RE101- Radiated Emissions, Magnetic field.....	
Annexure 5: RE02/RE102 - Radiated Emissions, Electric field.....	
Annexure 6: CS01/CS02/CS101 - Conducted Susceptibility, Power Leads.....	
Annexure 7: CS03/CS103, CS04/CS104,CS05/CS105 - Conducted Susceptibility, Antenna port-Intermodulation/ rejection of un desired signal/ Cross modulation.....	
Annexure 8: CS06/CS106 - Conducted Susceptibility, Transients, Power Leads.....	
Annexure 9: CS114 - Conducted Susceptibility, Bulk Cable injection.....	
Annexure 10: CS115 - Conducted Susceptibility, Bulk Cable injection, Impulse Excitation.....	
Annexure 11: CS116, Conducted Susceptibility, Damped sinusoidal Transients.....	
Annexure 12: CS118, Personal borne Electrostatic Discharge.....	
Annexure 13: RS01/RS101,Radiated Susceptibility,Magnetic Field.....	
Annexure 14: RS03/RS103,Radiated Susceptibility, Electric Field.....	
Annexure 15: LDC102, Normal steady state limits for voltage.....	
Annexure 16: LDC 104,Total Ripple.....	
Annexure 17: LDC 301, Abnormal Steady state Limits for voltage.....	
Annexure 18: LDC 401, Emergency Steady state Limits for voltage.....	
Annexure 19: LDC 601, Power Failure.....	
Annexure 20: LDC 602, Phase reversal.....	

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 3 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

LIST OF FIGURES

Figure 1: Block diagram for EUT.....	12
Figure 2: Photograph of EUT.....	12
Figure 3: EUT Name and Serial No.....	12
Figure 4: EUT Configuration.....	13
Figure 5: Photograph of EUT Cable.....	13
Figure 6: EUT Performance Monitoring.....	14
Figure 7: CE01 / CE101 Limit.....	15
Figure 8: CE01 / CE101 Test Setup Photograph.....	16
Figure 9: CE03 / CE102 Limit.....	17
Figure 10: CE03 / CE102 Test Setup Photograph.....	18
Figure 11: CE06 / CE106 Limit.....	19
Figure 122: CE06 /CE106 Test Setup Photograph.....	20
Figure 13: RE01 / RE101 Limit.....	21
Figure 14: <RE01/RE101> Test Setup Photograph.....	23
Figure 15: RE02 / RE102 Limit.....	24
Figure 16: RE02 / RE102 Test Setup Photograph.....	26
Figure 17: CS01 / CS101 Limit.....	27
Figure 188: CS 01/ CS101 Test Setup Photograph.....	28
Figure 199: CS03/CS103, CS04/CS104, and CS05/CS105 –Test Setup Photograph.....	30
Figure 20: CS06 / CS106 Limit.....	32
Figure 21: CS 06/ CS106 Test Setup Photograph.....	32
Figure 22: CS114 Limit Curve#X.....	34
Figure 23: CS 114 Test Setup Photograph.....	34
Figure 24: CS115 Limit.....	36

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 4 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Figure 25: CS115 Test Setup Photograph.....	36
Figure 26: CS116 Limit.....	38
Figure 27: CS116 Test Setup Photograph.....	38
Figure 28: Photographs of ESD Test Setup.....	40
Figure 29: RS01 / RS101 Limit.....	42
Figure 30: <RS01/RS101> Test Setup Photograph.....	43
Figure 31: RS03 / RS103 Test Setup Photograph.....	45
Figure 32: LDC102 -1.....	47
Figure 33: LDC102 Test Setup Photograph 1.....	47
Figure 34: LDC102 Test Setup Photograph 2.....	47
Figure 35: LDC104 Test Setup Photograph 1.....	51
Figure 36: LDC104 Test Setup Photograph 2.....	51
Figure 37: LDC301 Test Setup Photograph.....	54
Figure 38: LDC401 Test Setup Photograph.....	57
Figure 39: LDC601 Test Setup Photograph.....	58
Figure 40: LDC602 Test Setup Photograph.....	60

LIST OF GRAPHS

Graph 1: CE01 / CE101 – Ambient Graph.....	15
Graph 2: CE01 / CE101 - Positive or Phase.....	15
Graph 3: CE01 / CE101 - Négative or Phase.....	15
Graph 4: CE03 / CE102– Ambient Graph.....	17
Graph 5: CE03 / CE102- Positive or Phase.....	17
Graph 6: CE03 / CE102 - Négative or Neutral.....	17
Graph 7: CE06 / CE106– <1MHz- 40GHz>.....	19

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 5 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Graph 8: RE01 / RE101 – Ambient Graph.....	21
Graph 9: <RE01 /RE101>.....	22
Graph 10: RE02 / RE102– Ambient Graph.....	24
Graph 11: RE02 / RE102– Vertical Polarization.....	25
Graph 12: RE02 / RE102 – Horizontal Polarization.....	25
Graph 13: CS01/CS101 – Sensor Level (dBµV).....	28
Graph 14: CS03/CS103, CS04/CS104, CS05/CS105 –<10khz to 20GHz>.....	29
Graph 15: CS114 – Immunity Level (dBµA).....	34
Graph 16: CS114 – Forward power (dBm).....	34
Graph 17: RS01 / RS101 – Immunity level Magnetic Field Level (dBpT).....	42
Graph 18: RS03 / RS103 – Field Level (dBµV /V/m).....	45
Graph 19: RS03 / RS103 – Antenna Forward Power (dBm).....	45
Graph 20: LDC104 -1.....	51
Graph 21: LDC301 -1.....	54
Graph 22: LDC401 -1.....	56
Graph 23: LDC601 -1.....	58
Graph 24: LDC602 -1.....	60

1. General Information

Name of the Applicant	Mr/Ms. <.....>
Contact Name	<.....>
Contact No	<.....>
Email id	<.....>
EUT Manufacturer Name and Address	M/s. <.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 6 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

EUT Name	<.....>	
Model No	<.....>	
Serial No	<.....>	
Supply Voltage & Current Ratings	<.....>	
Test Location	M/s.<.....>	
Tests conducted	<.....>	
Test Standard	<.....>	
Status of EUT on receipt	EUT was received in Good Condition	
EUT Received on	DD/MM/YYYY	
Dates of Test	DD/MM/YYYY to DD/MM/YYYY	
Test Report Issued on	DD/MM/YYYY	
Test witnessed by	Mr. <.....>(Ms.<.....>)	
Test Result	Pass/Fail	
Statement of conformity	Declaration of conformity of the results is based as per the standard limits-	
Test Report Prepared By	Mr/Ms<.....>	
Test Engineer and Reviewer Details		
Tested by <.....> Test Engineer	Reviewed by <.....> Lab in Charge	Authorized by <.....> Technical Manager

Note: This report is digitally signed by the approving authority through a secured workflow
Text Style: Arial, Text Size: 10, Picture size(H): 3.65inch or 9.3cm

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 7 of 57
	ULR - TC5228XX000000YYY	Serial No: <.....>	

2. Test Summary

I. ELECTRONICS TESTING

1. EMC TESTING

Emission Tests			
Sl. No	Name of the Tests	Limits	Results
1.	CE01/CE101-Conducted Emissions, Power Leads, 30Hz to 10kHz	CE101-1/2/3/4 ,Submarine applications ,Navy ASW aircraft and Army aircraft	Emissions are within the Limit / Emissions are exceeding the limit or Pass / Fail Refer Annex 1
2.	CE03/CE102-Conducted Emissions, Power Leads, 10kHz to 10MHz	CE102 -1 for all application	Emissions are within the Limit / Emissions are exceeding the limit or Pass / Fail Refer Annex 2
3.	CE06/CE106- Conducted emissions, antenna terminal ,10kHz to 40GHz	34 dBµV	Emissions are within the Limit / Emissions are exceeding the limit or Pass / Fail Refer Annex 3
4.	RE01/RE101-Radiated Emissions, Magnetic field, 30Hz to 100kHz	RE101-1/2, all Army/Navy Applications	Emissions are within the Limit / Emissions are exceeding the limit or Pass / Fail Refer Annex 4

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 8 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Emission Tests			
Sl. No	Name of the Tests	Limits	Results
5.	RE02/RE102- Radiated Emissions, Electric field, 10kHz to 18GHz	RE102-1/2/3/4, Surface ship/sub marine /aircraft and space system/ground army /ground air force applications	Emissions are within the Limit / Emissions are exceeding the limit or Pass / Fail Refer Annex 5

Susceptibility Tests			
Sl. No	Name of the Test	Limits	Results
1.	CS01/CS02/CS101- Conducted Susceptibility, Power Leads, 30Hz to 150kHz	CS101 -1,voltage limit for all application or CS101-2, power limit for all application or Curve#1,Curve #2	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 6
2.	CS03/CS103,CS04/CS104, CS05/CS105 /-Conducted susceptibility, antenna port, intermodulation ,15kHz to 10GHz rejection of undesired signal/ cross modulation 30Hz to 20GHz	-14 dBm- 80dB	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 7
3.	CS06/CS106-Conducted Susceptibility, transients, power leads	Vpeak = 400 volt peak tr = 1.5 μ sec, \pm 0.5 μ sec tf = 3.5 μ sec, \pm 0.5 μ sec td = 5.0 μ sec, \pm 22% Vsag \leq 120 volt peak (maximum)	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 9 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Susceptibility Tests			
Sl. No	Name of the Test	Limits	Results
		tsag ≤ 20 μsec	Refer Annex 8
4.	CS114-Conducted Susceptibility, Bulk Cable injection, 10kHz to 200MHz	CS114 -1 ,Curve #3	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 9
5.	CS115-Conducted Susceptibility, Bulk Cable Injection, Impulse Excitation	CS115-1, Imax = 5Amps	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 10
6.	CS116 - Conducted Susceptibility, Damped sinusoidal Transients, Cable and power Leads, 10kHz to 100MHz	CS116-2, Imax = 10Amps	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 11
7.	CS118 -Personnel borne electrostatic discharge.	±8KV- Contact Discharge ±15KV-Air Discharge	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 12

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 10 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Susceptibility Tests			
Sl. No	Name of the Test	Limits	Results
8.	RS01/RS101 - Radiated Susceptibility, Magnetic field, 2MHz to 18GHz	RS101-1/2 all Navy/ all Army applications.	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 13
9.	RS03/RS103 - Radiated Susceptibility, Electric field, 2MHz to 18GHz	10/50/200V/m	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail or Compliance / Incompliance Refer Annex 14
10.	LDC102- Normal steady state limits for voltage	28VDC NLSS-22VDC NHSS-29VDC	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail Refer Annex 15
11.	LDC104-Total ripple	DC Voltage distortion: Voltage ripple 1.5Volts peak to average Ripple frequency & Amplitude- 1.2kHz to 16.8KHz – 0.8Vrms to 0.06Vrms	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail Refer Annex 16
12.	LDC301-Abnormal steady state limits for voltage	Voltage NLSS-20.0VDC Voltage NHSS-31.5VDC	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail Refer Annex 17

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 11 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Susceptibility Tests			
Sl. No	Name of the Test	Limits	Results
13.	LDC401-Emergency steady state voltage	Voltage ELSS-18.0VDC Voltage EHSS-29.0VDC	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail Refer Annex 18
14.	LDC601-Power Failure	28VDC - 7 Sec Duration of power failure : 100ms, 500ms, 3 s, 7s	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail Refer Annex 19
15.	LDC602-Phase reversal	28VDC- 30 min	EUT Performance found satisfactory / EUT Performance found not satisfactory or Pass / Fail Refer Annex 20

2.1 MEASUREMENT UNCERTAINTY

The following measurement uncertainties are applicable to the relevant tests that are mentioned below:

Test		Uncertainty (±)
Conducted Emission		
Radiated Emission	Below 1GHz	
	Above 1GHz	


TATA ADVANCED SYSTEMS			
EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 13 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Figure 4: EUT Configuration

Figure 5: Photograph of EUT Cable

4. Performance Monitoring Parameters

Detailed explanation of performance monitoring parameters with tolerance given by the customer and photographs

During susceptibility test the following parameters are monitored.

Sl.No.	Monitored Parameters	Observed Conditions
1.		
2.		
3.		
4.		
5.		
6.		

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 14 of 57
	ULR - TC52281A000000XXF	Serial No: <....>	

Figure 6: EUT Performance Monitoring

Annexure 1: CE01/CE101- Conducted Emissions, Power Leads**Common Information:**

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_8.54
 Temperature :°C
 Humidity :

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Receiver Settings

Subrange	Detectors	IF Bandwidth	Meas. Time	Receiver
30Hz- 1kHz	Peak	10 Hz	0.15s	ESU40
1kHz- 10kHz	Peak	100 Hz	0.015s	ESU40

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 15 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Figure 7: CE01 / CE101 Limit

Graph 1: CE01 / CE101 – Ambient Graph

Graph 2: CE01 / CE101 - Positive or Phase

Graph 3: CE01 / CE101 - Négative or Phase

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 16 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Figure 8: CE01 / CE101 Test Setup Photograph

Test Result	Pass/Fail or Emission are within Limit /exceeding limit or as per JRF Decision Rule
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Annexure 2: CE03/CE102- Conducted Emissions, Power Leads**Common Information:**

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_8.54
 Temperature :°C

Humidity :

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Receiver Settings

Subrange	Detectors	IF Bandwidth	Meas. Time	Receiver
10kHz- 150kHz	Peak	1 kHz	0.015s	ESU40
150kHz- 10MHz	Peak	10 kHz	0.015s	ESU40

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 17 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Figure 9: CE03 / CE102 Limit

Graph 4: CE03 / CE102– Ambient Graph

Graph 5: CE03 / CE102- Positive or Phase

Graph 6: CE03 / CE102 - Négative or Neutral

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 18 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Figure 10: CE03 / CE102 Test Setup Photograph

Test Result	Pass/Fail or Emission are within Limit /exceeding limit or as per JRF Decision Rule
-------------	--

Annexure 3: CE06/CE106- Conducted Emissions, Antenna Terminal

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_8.54
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 19 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Receiver Settings

Subrange	Detectors	IF Bandwidth	Meas. Time	Receiver
10kHz- 150kHz	Peak	1 kHz	0.015s	ESU40
150kHz- 30MHz	Peak	10 kHz	0.015s	ESU40
30MHz- 1GHz	Peak	100 kHz	0.015s	ESU40
Above 1GHz	Peak	1 MHz	0.015s	ESU40

Figure 11: CE06 / CE106 Limit

Graph 7: CE06 / CE106– <1MHz- 40GHz>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 20 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Figure 122: CE06 /CE106 Test Setup Photograph

Test Result	Pass/Fail or Emission are within Limit /exceeding limit or as per JRF Decision Rule
-------------	--

Annexure 4: RE01/RE101- Radiated Emissions, Magnetic field

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_8.54
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 21 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

3	<.....>	<.....>	<.....>	<.....>	<.....>
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Receiver Settings

Subrange	Detectors	IF Bandwidth	Meas. Time	Receiver
30Hz- 1kHz	Peak	1 kHz	0.015s	ESU40
1kHz- 10kHz	Peak	10 kHz	0.015s	ESU40
10kHz- 100kHz	Peak	1 kHz	0.015s	ESU40

Figure 13: RE01 / RE101 Limit

Graph 8: RE01 / RE101 – Ambient Graph

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 22 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Graph 9: <RE01 /RE101>

EUT Left, Right, Top, Bottom, Display, keyboard, Ventilation,etc....)

<u>*EUT Right Rear Bottom</u>	<u>*EUT Front Bottom</u>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 23 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

<u>*EUT Left top middle</u>	<u>*EUT Left Bottom</u>
-----------------------------	-------------------------

Figure 14: <RE01/RE101> Test Setup Photograph

Test Result	Pass/Fail or Emission are within Limit /exceeding limit or as per JRF Decision Rule
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Annexure 5: RE02/RE102 - Radiated Emissions, Electric field**Common Information:**

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_8.54
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 24 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>

Receiver Settings

Subrange	Detectors	IF Bandwidth	Meas. Time	Receiver
10kHz- 150kHz	Peak	1 kHz	0.015s	ESU40
150kHz-30MHz	Peak	10 kHz	0.015s	ESU40
30MHz-1GHz	Peak	100 kHz	0.015s	ESU40
1GHz to 18GHz	Peak	1 MHz	0.015s	ESU40

Figure 15: RE02 / RE102 Limit

Graph 10: RE02 / RE102– Ambient Graph

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 25 of 57
	ULR - TC52281A000000XXF	Serial No: <....>	

Graph 11: RE02 / RE102 – Vertical Polarization**Graph 12: RE02 / RE102 – Horizontal Polarization**

<u>10kHz to 30MHz</u>	<u>30MHz to 200MHz</u>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 26 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

200MHz to 1GHz	1GHz to 18GHz
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Figure 16: RE02 / RE102 Test Setup Photograph

Test Result	Pass/Fail or Emission are within Limit /exceeding limit or as per JRF Decision Rule
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Annexure 6: CS01/CS02/CS101 - Conducted Susceptibility, Power Leads

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_10.6
 Temperature :°C
 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
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EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 27 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>
7	<.....>	<.....>	<.....>	<.....>	<.....>
8	<.....>	<.....>	<.....>	<.....>	<.....>

Scan Settings

Subrange	Step Size	Dwell	Modulation
30Hz- 150kHz	5%	3 Sec	CW

The test carried out in the following cables

Cable No.	Cable Description

Figure 17: CS01 / CS101 Limit

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 28 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Graph 13: CS01/CS101 – Sensor Level (dBµV)

Figure 188: CS 01/ CS101 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

Annexure 7: CS03/CS103, CS04/CS104, CS05/CS105 - Conducted Susceptibility, Antenna port-Intermodulation/ rejection of un desired signal/ Cross modulation

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_10.6.
 Temperature :°C
 Humidity :%

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 29 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>
7	<.....>	<.....>	<.....>	<.....>	<.....>
8	<.....>	<.....>	<.....>	<.....>	<.....>

Graph 14: CS03/CS103, CS04/CS104, CS05/CS105 –<10khz to 20GHz>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 30 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Figure 199: CS03/CS103, CS04/CS104, and CS05/CS105 –Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

Annexure 8: CS06/CS106 - Conducted Susceptibility, Transients, Power Leads

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_10.6
 Temperature :°C
 Humidity :%

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 31 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>
7	<.....>	<.....>	<.....>	<.....>	<.....>
8	<.....>	<.....>	<.....>	<.....>	<.....>

The test carried out in the following cables

Cable No.	Cable Description

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 32 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Figure 20: CS06 / CS106 Limit

Figure 21: CS 06/ CS106 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

Annexure 9: CS114 - Conducted Susceptibility, Bulk Cable injection

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_10.6

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 33 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Temperature :°C

Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>
7	<.....>	<.....>	<.....>	<.....>	<.....>
8	<.....>	<.....>	<.....>	<.....>	<.....>

Scan Settings

Subrange	Step Size	Dwell	Modulation	Test Curve#
10kHz- 1MHz	5%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>
1MHz to 30 MHz	1%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>
30 MHz to 200 MHz	0.5%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>

The test carried out in the following cables

Cable No.	Cable Description

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 34 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Figure 22: CS114 Limit Curve#X

Graph 15: CS114 – Immunity Level (dBμA)

Graph 16: CS114 – Forward power (dBm)

Figure 23: CS 114 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	--

Annexure 10: CS115 - Conducted Susceptibility, Bulk Cable injection, Impulse Excitation

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 35 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test Standard : <.....>

Test Date : <dd/mm/yyyy>

Test mode : <.....>

Software used : EMC32 Ver_10.6

Temperature :°C

Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>

The test carried out in the following cables

Cable No.	Cable Description

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 36 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Figure 24: CS115 Limit

Figure 25: CS115 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

Annexure 11: CS116, Conducted Susceptibility, Damped sinusoidal Transients.

Common Information:

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 37 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Ref EMC Test Plan / QT / ATP : <.....>

Test Standard : <.....>

Test Date : <dd/mm/yyyy>

Test mode : <.....>

Software used : EMC32 Ver_10.6

Temperature :°C

Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>
7	<.....>	<.....>	<.....>	<.....>	<.....>

The test carried out in the following cables

Cable No.	Cable Description
-----------	-------------------

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 38 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Figure 26: CS116 Limit

Figure 27: CS116 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

Annexure 12: CS118, Personal borne Electrostatic Discharge

Common Information:

Reference Standard : <.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 39 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Basic Standard : <.....>

Test Date : <dd/mm/yyyy>

Test mode : <.....>

Temperature :°C

Humidity :%

Atmospheric Pressure : Start_....kPa

End_....kPa

Test Equipment :

Sl. No.	Equipment	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

Discharge points of Contact:

Sl.No.	Test Points
1	<.....>
2	<.....>
3....N	<.....>

Discharge points of Air:

Sl.No.	Test Points
1	<.....>
2	<.....>
3....N	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 40 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test Details: Contact Discharge	
Discharge Network : 150pF, 330Ω	Discharge Type: Contact (Direct)
Level : 8KV	Polarity : Positive & Negative
Number of Discharge per test points: ≥10 pulses	
Observation :	

Test Details: Contact Discharge	
Discharge Network : 150pF, 330Ω	Discharge Type: Contact (Indirect)
Level : 8KV	Polarity : Positive & Negative
Number of Discharge per test points: ≥10 pulses	Indirect discharge planes: VCP & HCP
Observation :	

Test Details: Air Discharge	
Discharge Network : 150pF, 330Ω	Discharge Type: Air
Level : 2kV,4kV,8kV and 15kV	Polarity : Positive & Negative
Observation :	

Figure 28: Photographs of ESD Test Setup

Test Result	Pass/Fail or EUT performance found satisfactory / Not
-------------	---

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 41 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

	Satisfactory or as per JRF Decision Rule
--	--

Annexure 13: RS01/RS101, Radiated Susceptibility, Magnetic Field

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used : EMC32 Ver_10.6
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>

Scan Settings

Subrange	Step Size	Dwell	Modulation
30Hz- 100kHz	5%	3 Sec	CW

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 42 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Figure 29: RS01 / RS101 Limit

Graph 17: RS01 / RS101 – Immunity level Magnetic Field Level (dBpT)

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 43 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

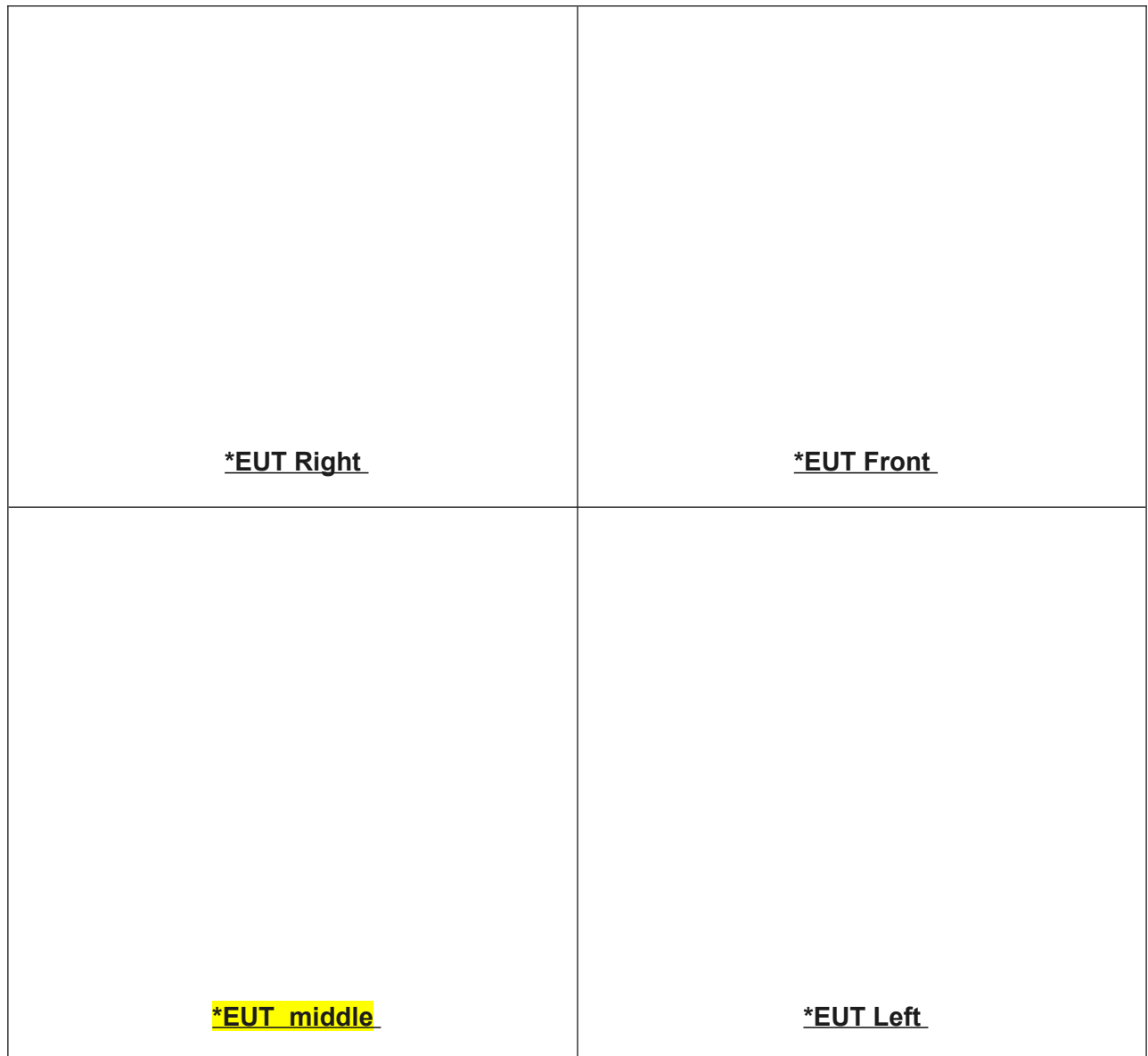


Figure 30: <RS01/RS101> Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not
-------------	---

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 44 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Satisfactory or as per JRF Decision Rule

Annexure 14: RS03/RS103, Radiated Susceptibility, Electric Field Common Information:

Ref EMC Test Plan / QT / ATP : <.....>

Test Standard : <.....>

Test Date : <dd/mm/yyyy>

Test mode : <.....>

Software used : EMC32 Ver_10.6

Temperature :°C

Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>
4	<.....>	<.....>	<.....>	<.....>	<.....>
5	<.....>	<.....>	<.....>	<.....>	<.....>
6	<.....>	<.....>	<.....>	<.....>	<.....>

Subrange	Step Size	Dwell	Modulation	Field Level(V/m)
10kHz- 1MHz	5%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>
1MHz to 30 MHz	1%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>
30 MHz to 1 GHz	0.5%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>
1 GHz to 8 GHz	0.1%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 45 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

8 GHz to 40 GHz	0.05%	3 Sec	PM,1 kHz 50% duty cycle.	<.....>
-----------------	-------	-------	-----------------------------	---------

Graph 18: RS03 / RS103 – Field Level (dB μ V /V/m)

Graph 19: RS03 / RS103 – Antenna Forward Power (dBm)

<u>10kHz to 80MHz</u>	<u>80MHz to 200MHz</u>
<u>200MHz to 1GHz</u>	<u>1GHz to 6GHz</u>

Figure 31: RS03 / RS103 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 46 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Annexure 15: LDC102, Normal steady state limits for voltage

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Method : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used :
 Temperature :°C
 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

LDC 102-1 MIL-STD-704 normal limits for steady state voltage

Normal limit	704A	704B	704C	704D	704E	704F
Voltage NLSS	<.....>	<.....>	<.....>	<.....>	<.....>	<.....>
Voltage NHSS	<.....>	<.....>	<.....>	<.....>	<.....>	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 47 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test condition	Voltage	Frequency	Time duration	Observation

Figure 32: LDC102 -1

Figure 33: LDC102 Test Setup Photograph 1

Figure 34: LDC102 Test Setup Photograph 2

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 48 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 49 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Annexure 16: LDC 104,Total Ripple

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Method : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used :
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

LDC104-1 MIL-STD-704 limits for ripple DC voltage distortion

Limit	704A	704B	704C	704D	704E	704F
Voltage Ripple	2Volts peak to Mean	1.5Volts peak to Average	1.5Volts peak to Average	1.5Volts peak to Average	1.5Volts peak to Average	1.5Volts peak to Average

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 50 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test condition	Ripple Frequency Components	Amplitude of Ripple Component (Vrms) MIL STD-704A/B/C/D/E/F
AHz	
Hz	
Hz	
Hz	
Hz	
Hz	
Hz	
BHz	
Hz	
Hz	
Hz	
Hz	
Hz	
Hz	

Test condition	Ripple Frequency Components	Amplitude of Ripple	Time duration	Observation

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 51 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Graph 20: LDC104 -1

Figure 35: LDC104 Test Setup Photograph 1

Figure 36: LDC104 Test Setup Photograph 2

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 52 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 53 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Annexure 17: LDC 301, Abnormal Steady state Limits for voltage

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
 Test Method : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used :
 Temperature :°C
 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

LDC301-1 MIL-STD-704 abnormal limits for steady state voltage

Abnormal limit	704A	704B	704C	704D	704E	704F
Voltage NLSS	<.....>	<.....>	<.....>	<.....>	<.....>	<.....>
Voltage NHSS	<.....>	<.....>	<.....>	<.....>	<.....>	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 54 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test condition	Voltage	Time duration	Observation

Graph 21: LDC301 -1

Figure 37: LDC301 Test Setup Photograph

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 55 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	---

Annexure 18: LDC 401, Emergency Steady state Limits for voltage

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
Test Method : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used :
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

LDC401-1 MIL-STD-704 emergency limits for steady state voltage

Abnormal limit	704A	704B	704C	704D	704E	704F
Voltage ELSS	<.....>	<.....>	<.....>	<.....>	<.....>	<.....>
Voltage EHSS	<.....>	<.....>	<.....>	<.....>	<.....>	<.....>

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 56 of 57
	ULR - TC52281A000000XXF	Serial No: <.....>	

Test condition	Voltage	Time duration	Observation

Graph 22: LDC401 -1

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYYY >	Model No: <...>	Page 57 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

Figure 38: LDC401 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
-------------	--

Annexure 19: LDC 601, Power Failure

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
Test Method : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used :
 Temperature :°C
 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

LDC601-1 MIL-STD-704 power failure limits

Limit	704A	704B	704C	704D	704E	704F
-------	------	------	------	------	------	------

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 58 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Power failure	7 Sec	7 Sec	7 Sec	7 Sec	7 Sec	7 Sec
------------------	-------	-------	-------	-------	-------	-------

Test condition	Voltage	Time duration	Observation

Graph 23: LDC601 -1

Figure 39: LDC601 Test Setup Photograph

Test Result	Pass/Fail or EUT performance found satisfactory / Not
-------------	---

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 59 of 57
	ULR - TC52281A000000XXXXF	Serial No: <.....>	

	Satisfactory or as per JRF Decision Rule
--	--

Annexure 20: LDC 602, Phase reversal

Common Information:

Ref EMC Test Plan / QT / ATP : <.....>
 Test Standard : <.....>
Test Method : <.....>
 Test Date : <dd/mm/yyyy>
 Test mode : <.....>
 Software used :
 Temperature :°C

 Humidity :%

Test Equipment Used :

Sl. No.	Description	Make	Model No.	Serial No.	Cal Due
1	<.....>	<.....>	<.....>	<.....>	<.....>
2	<.....>	<.....>	<.....>	<.....>	<.....>
3	<.....>	<.....>	<.....>	<.....>	<.....>

Specifications:

LDC602-1 MIL-STD-704 power failure limits

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 60 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Limit	704F
Phase reversal	Phase reversal does not cause damage

Test condition	Voltage	Time duration	Observation

Graph 24: LDC602 -1

Figure 40: LDC602 Test Setup Photograph

EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 61 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

Test Result	Pass/Fail or EUT performance found satisfactory / Not Satisfactory or as per JRF Decision Rule
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EMC Centre Test Report	Report No : < TASL/17025/EMC/TRP/MIL or DO/XXXX/YYY >	Model No: <...>	Page 62 of 57
	ULR - TC52281A000000XXXF	Serial No: <.....>	

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End of Test Report