Indian Institute of Technology Tirupati

Renigunta Road, Tirupati 517506, INDIA

Time Table (July – Dec 2018)

M. Tech/ MS/ PhD
ELECTRICAL ENGINEERING

DAY	1	2	3		4	5	6	7	8
	9.00 - 9.50am	10.00 - 10.50am	11.00 - 11.50am		1.00 - 1.50pm	2.00 - 2.50pm	3.00 - 3.50pm	3.55 - 4.45pm	4.50 - 5.40pm
Mon		B*	C	Lunch recess 12:00 – 1:00pm	D		B*		
Tue	B*	C	D		B*		EE5193 Image Processing Lab		
Wed	C	D			A*	-	EE5191 Advanced Signal Processing Lab		
Thu		A*							
Fri	A*				-	C	A*		

Slot	Course No. and Title	Faculty	Room No
B*	EE5111 Advanced Signal Analysis and Processing (3-0-0-3)	*Prof. K. M. M. Prabhu & Dr. Subrahmanyam Gorthi*	3
A*	EE5024 Machine Learning for Image Processing (3-0-0-3)	*Dr. Rama Krishna Gorthi*	3
C	MA5105 Linear Algebra and Probability (3-1-0-4)	*Dr. S. Rajesh / Dr. Ishapathik Das*	4
D	E1 Department Elective 3 (3-0-0-3) EE5022 Communication Networks	*Dr. K. P. Naveen*	10
D	CS5021 Advanced Computer Networks (3-0-0-3)	*Dr. B. Venkataramana & Dr. V. Mahendran*	5
	EE5193 Image Processing Lab	*Dr. Rama Krishna Gorthi*	
	EE5191 Advanced Signal Processing Lab	*Dr. Subrahmanyam Gorthi*	

Exam for EE5024 (A*-slot) will be conducted in the A-slot
Exam for EE5111 (B*-slot) will be conducted in the B-slot