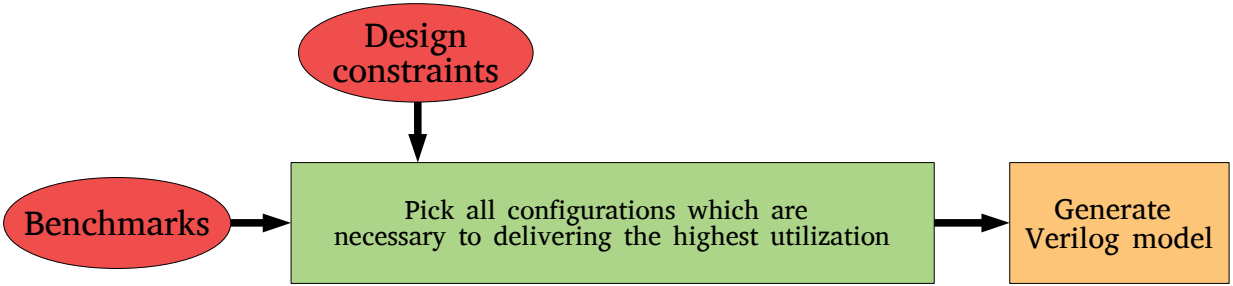
Design constraints
Benchmarks
Pick all configurations which are necessary to delivering the highest utilization
Generate Verilog model