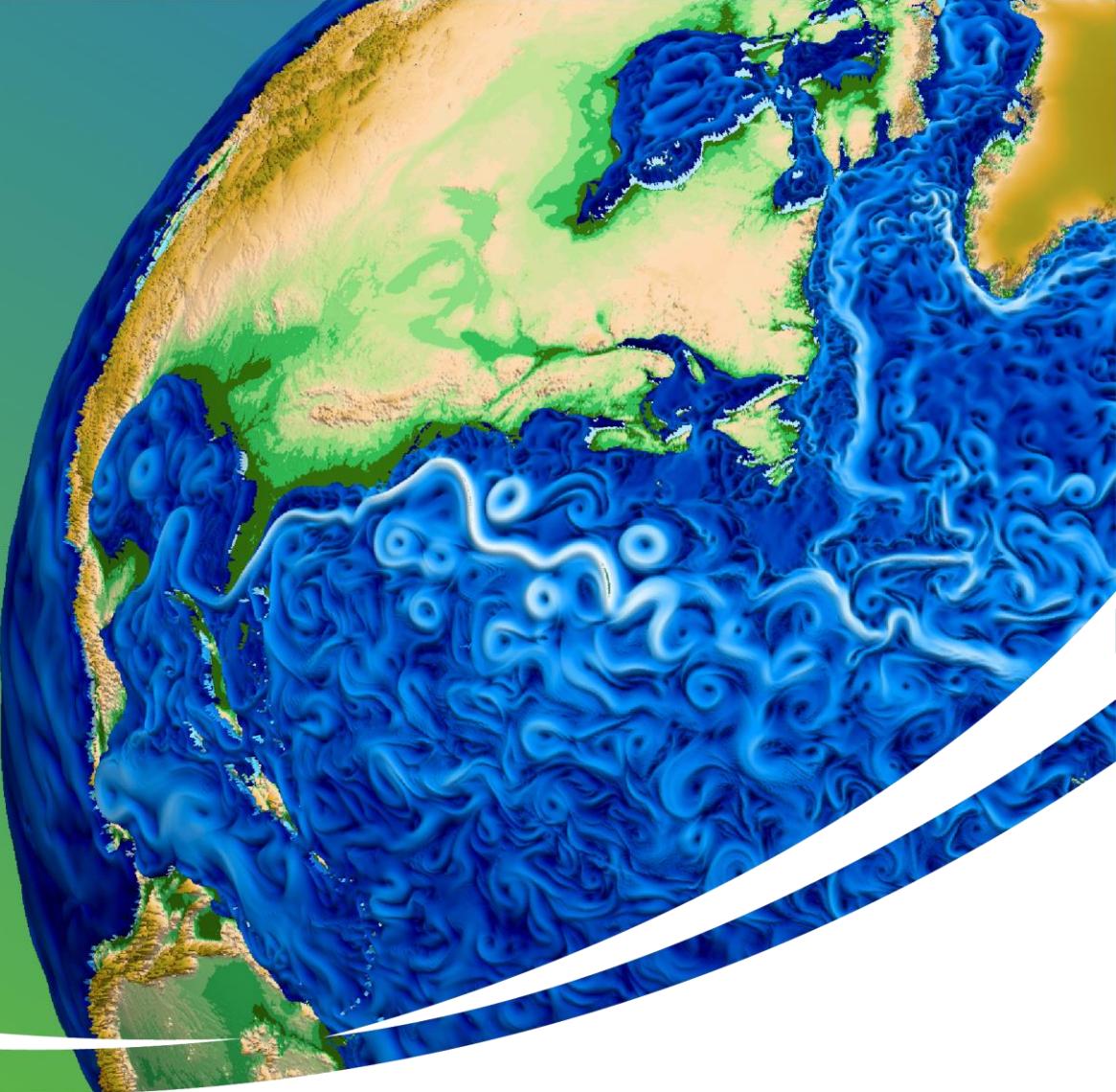


# High Performance Computing for Climate

Sarat Sreepathi

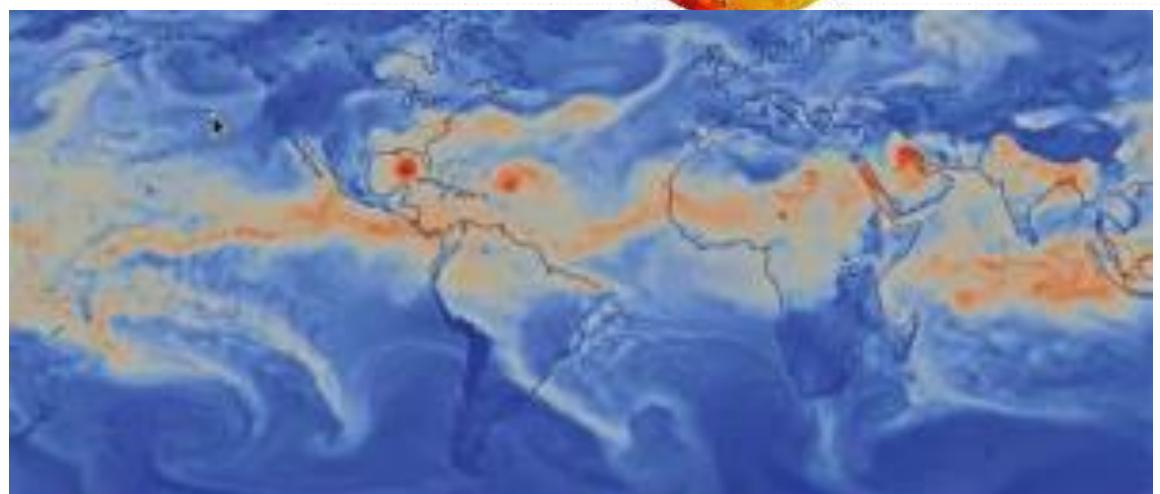
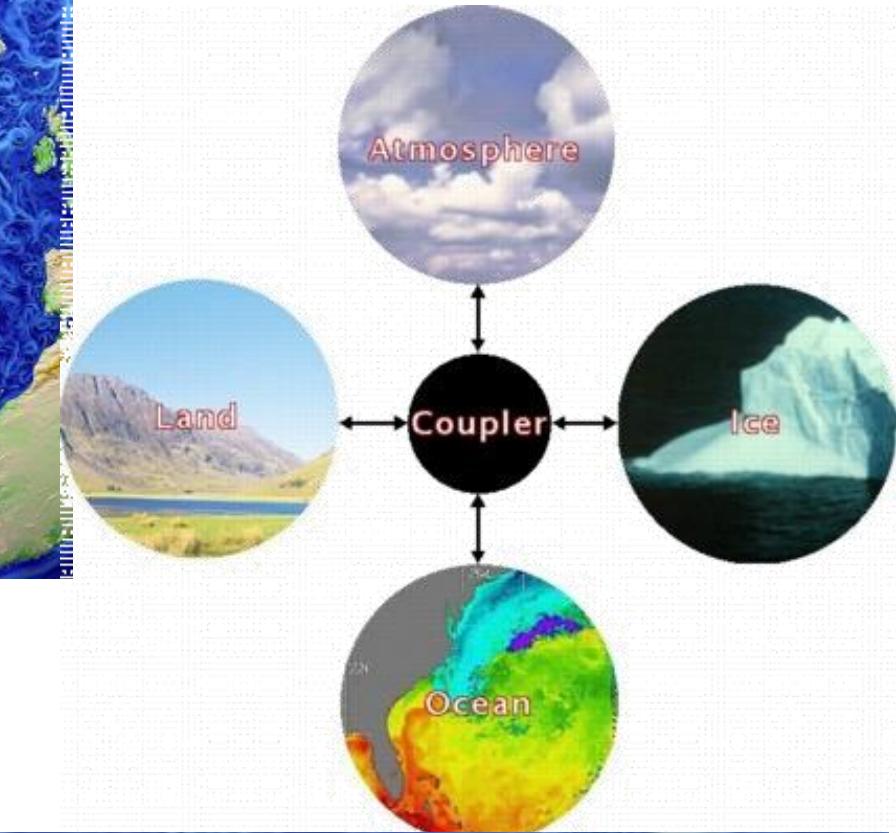
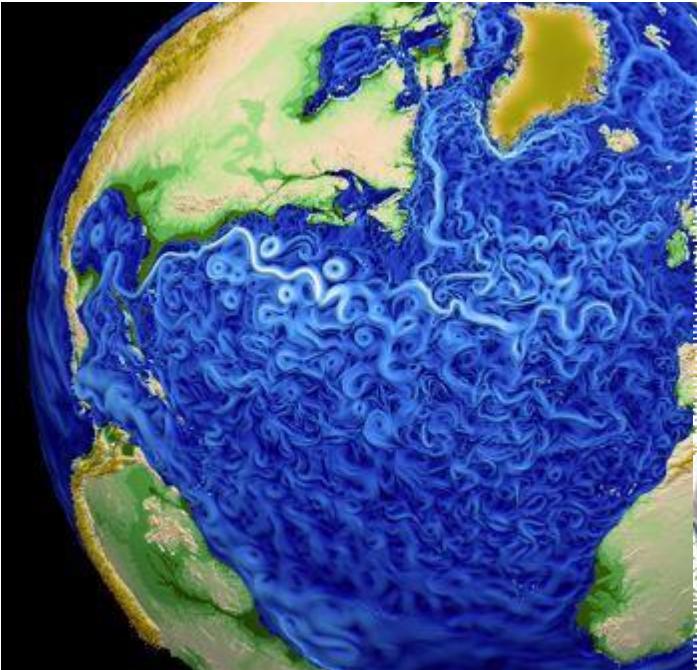
Oak Ridge National Laboratory

Performance Analytics for  
Computational Experiments



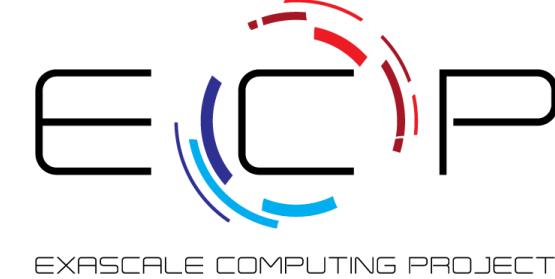
# Energy Exascale Earth System Model (E3SM)

- Global Earth System Model
- Atmosphere, Land, Ocean, Ice, ... component models
- 8 DOE labs, 12 university partners,...  
~\$30+ M/year
- Development driven by DOE mission interests: Energy/water issues looking out 40 years
- **Key computational goal: Ensure E3SM effectively utilizes DOE exascale supercomputers**
- E3SM is open source / open development
  - Website: [www.e3sm.org](http://www.e3sm.org)
  - Github: <https://github.com/E3SM-Project>

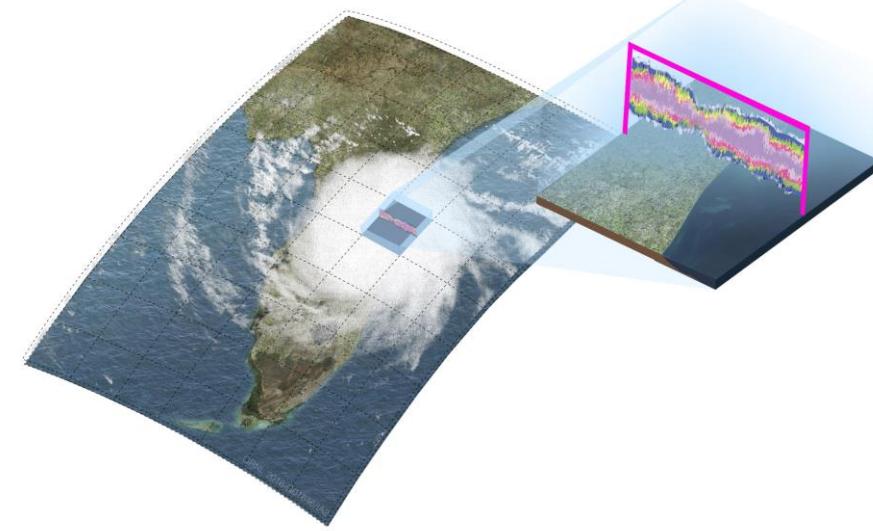
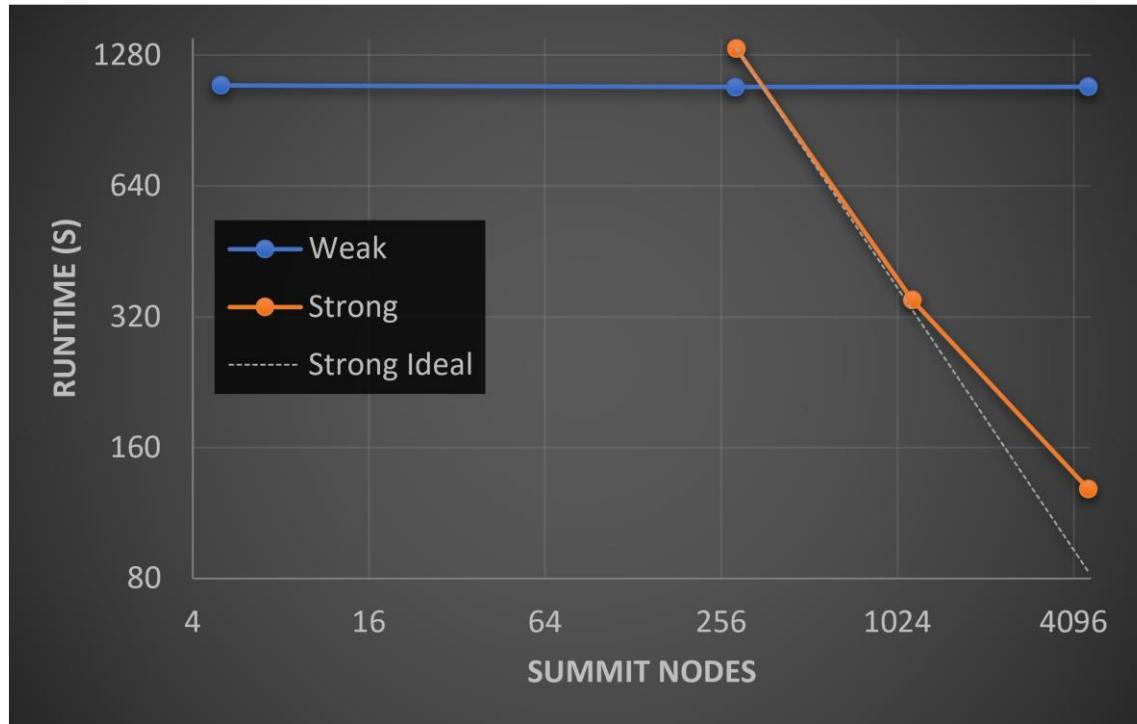


*Mission: Use exascale computing to carry out high-resolution Earth system modeling of natural, managed and man-made systems, to answer pressing problems for the DOE.*

# E3SM-MMF Cloud Resolving Climate Model



**Goal:** Develop capability to assess regional impacts of climate change on the water cycle that directly affect the US economy such as agriculture and energy production.



- Multiscale Modeling Framework (MMF) / Super-Parameterization
- Replaces traditional parameterizations with cloud resolving model within each grid cell of global climate model

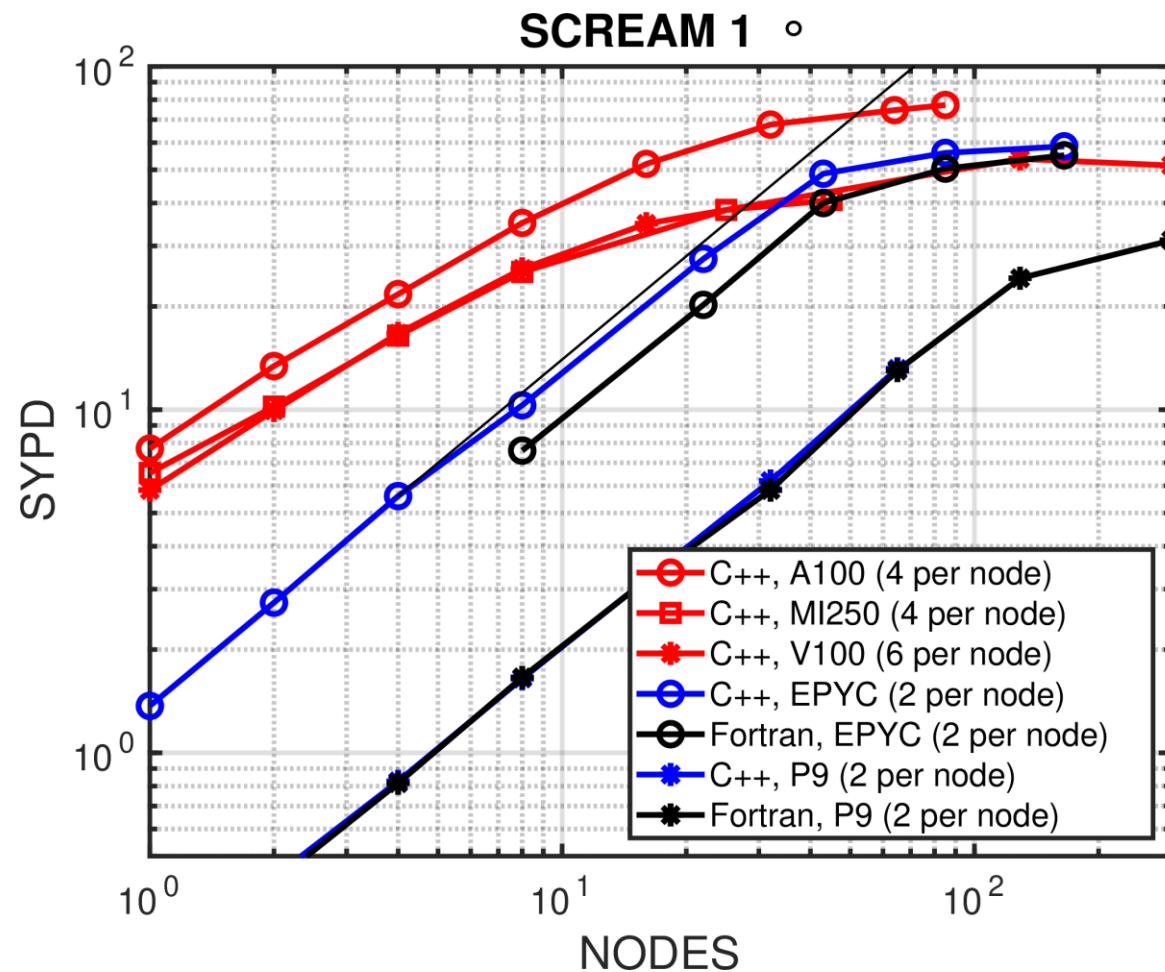
# Programming Models

- C++ with templates (Kokkos or YAKL)
  - Robust and well supported solution across most hardware
  - Requires minimal vendor support
- Fortran with OpenACC or OpenMP offload
  - Relies heavily on (lagging) vendor compiler support
  - Remains immature w.r.t. advanced Fortran features
  - Good performance requires major code refactoring
- Domain Specific Languages
  - Promising approach ( e.g. GT4Py/GridTools, PSyclone)
  - Need additional investments to support algorithms & meshes in E3SM components
  - Most experience within DOE labs is with C++

# C++/Kokkos: Performance Portability

E3SM's Atmosphere model (EAMXX in "SCREAM" configuration)  
 1 degree resolution: 128 vertical levels,  
 nonhydrostatic (NH) dycore, 10 tracers,  
 P3/SHOC physics with prescribed aerosols,  
 no convective parameterization

- Performance portability
  - IBM P9, AMD EYPC
  - NVIDIA V100, A100
  - AMD MI250
- CPU performance:
  - C++/Kokkos as fast or faster than Fortran
- GPU performance:
  - Large scaling range where GPU nodes are 4-10x faster than CPU nodes

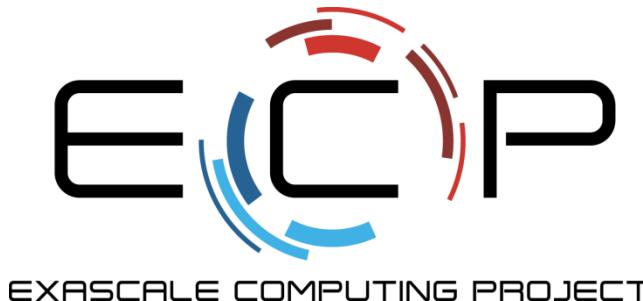


# Early Evaluation of Fugaku A64FX Architecture Using Climate Workloads

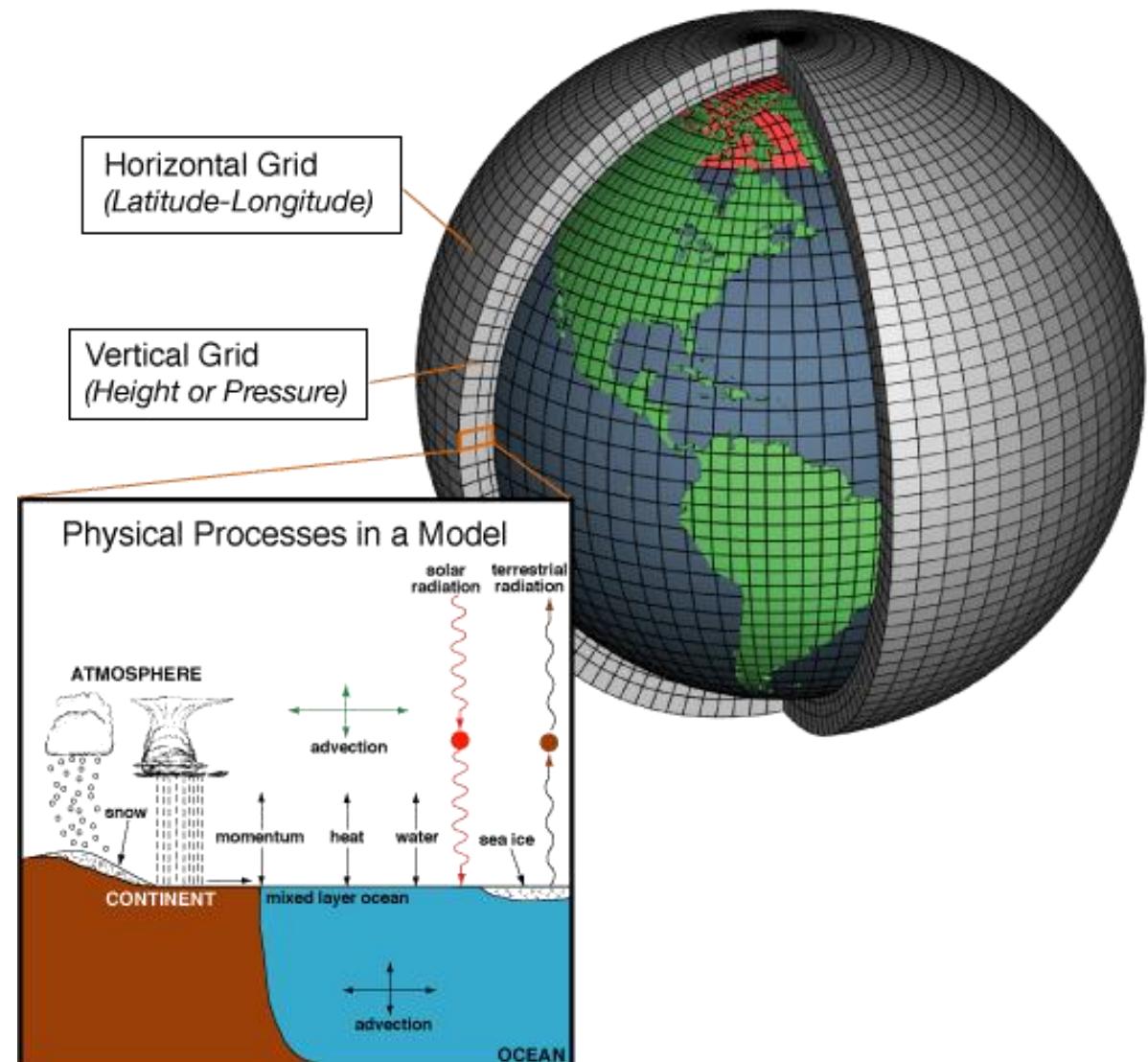
Sarat Sreepathi  
Oak Ridge National Laboratory

Mark Taylor  
Sandia National Laboratories

Adapted from talk given at  
EAHPC Workshop  
IEEE Cluster 2021  
September 7, 2021

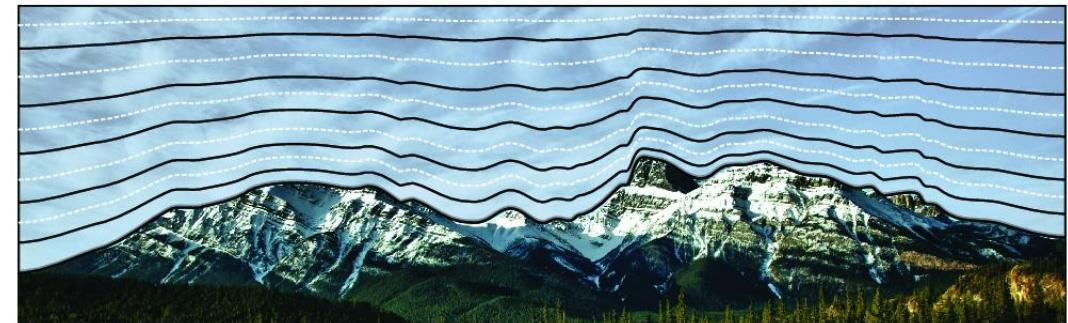


# Atmosphere Component



Terrain following figure: D. Hall, CU Boulder

Source: [http://celebrating200years.noaa.gov/breakthroughs/climate\\_model/welcome.html](http://celebrating200years.noaa.gov/breakthroughs/climate_model/welcome.html)



hydrostatic-pressure terrain-following coordinates

- **Dynamical Core**
  - Solves the Atmospheric Primitive Equations
  - Linear transport of 40 atmospheric species
  - 72 vertical levels – 0.8 km avg. spacing
  - Benchmark (two versions): Fortran (preqx) and C++ (preqx\_kokkos)

# Fugaku

- #2 on Top500
- RIKEN Center for Computational Science
- Key Characteristics of A64FX\*
  - Arm 64-bit with 512-bit SVE (Scalable Vector Extensions)
  - High Bandwidth Memory
  - Low Power

\*[https://www.fujitsu.com/downloads/SUPER/a64fx/a64fx\\_datasheet\\_en.pdf](https://www.fujitsu.com/downloads/SUPER/a64fx/a64fx_datasheet_en.pdf)



HOME LISTS STATISTICS RESOURCES ABOUT MEDIA KIT

Home »RIKEN Center for Computational Science »  
Supercomputer Fugaku - Supercomputer Fugaku, A64FX 48C 2.2GHz, Tofu...

## SUPERCOMPUTER FUGAKU - SUPERCOMPUTER FUGAKU, A64FX 48C 2.2GHZ, TOFU INTERCONNECT D

**Site:** RIKEN Center for Computational Science

**System URL:** <https://www.r-ccs.riken.jp/en/fugaku/project>

**Manufacturer:** Fujitsu

**Cores:** 7,630,848

**Memory:** 5,087,232 GB

**Processor:** A64FX 48C 2.2GHz

**Interconnect:** Tofu interconnect D

### Performance

**Linpack Performance (Rmax)** 442,010 TFlop/s

**Theoretical Peak (Rpeak)** 537,212 TFlop/s

**Nmax** 21,288,960

**HPCG [TFlop/s]** 16,004.5

### Power Consumption

**Power:** 29,899.23 kW [Optimized: 26248.36 kW]

**Power Measurement Level:** 2

<https://www.top500.org/system/179807/>



EXASCALE COMPUTING PROJECT

# Architecture Comparison: Metrics

- Single node workload for understanding h/w trends (ca 2012+)
- Performance Efficiency metric: number of element remap timesteps **per second**

$$E_{perf} = \frac{N_e * N_t}{(prim\_main\_loop * num\_devices)}$$

- $N_e$  is the number of spectral elements
- $N_t$  is the number of remap timesteps (34 for the Fugaku experiments)
- *prim\_main\_loop* is the main computation loop timer
- *num\_devices* is 1 for CPU nodes or the number of GPUs per node for GPU systems
- Power Efficiency metric: number of element remap timesteps **per Watt**

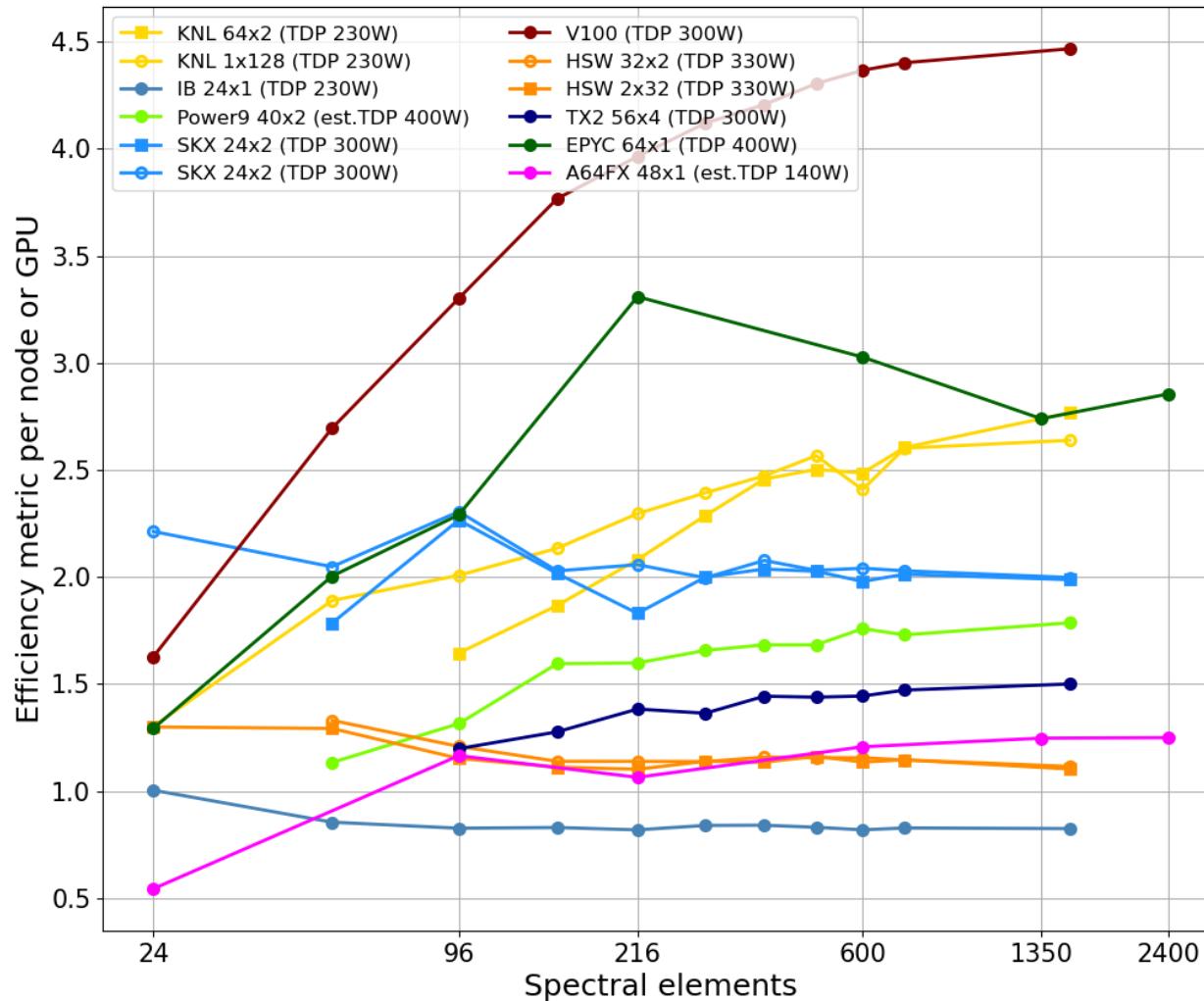
$$E_{power\_tdp} = \frac{E_{perf}}{\text{TDP}}$$

- Thermal Design Power (TDP)

# Architecture Comparison: Performance Efficiency

E3SM HOMME Dycore Benchmark: Cross-Architecture Comparison

(A64FX, EPYC results are preliminary)



Inform configurations where GPU systems can outperform CPU systems

Fugaku Node: Single A64FX socket  
GNU Fortran + MPI (48 ranks)

Note: Top Red (Volta V100), Pink (A64FX),  
Orange (Dual-socket Haswell)  
Higher is better

\* Plot of the efficiency metric normalized by power consumption on various hardware architectures. The legend includes a short descriptor for each architecture along with the number of parallel processes times (x) the number of threads and includes TDP in parenthesis. Specifically, the labels map as follows: KNL (Intel® Knights Landing), IB (Intel®Ivy Bridge), SKX (Intel® Skylake), V100 (NVIDIA® Volta), HSW (Intel® Haswell), A64FX (Fujitsu® A64FX), Power9 (IBM® POWER9), TX2 (Marvell®ThunderX2), EPYC (AMD® EPYC).

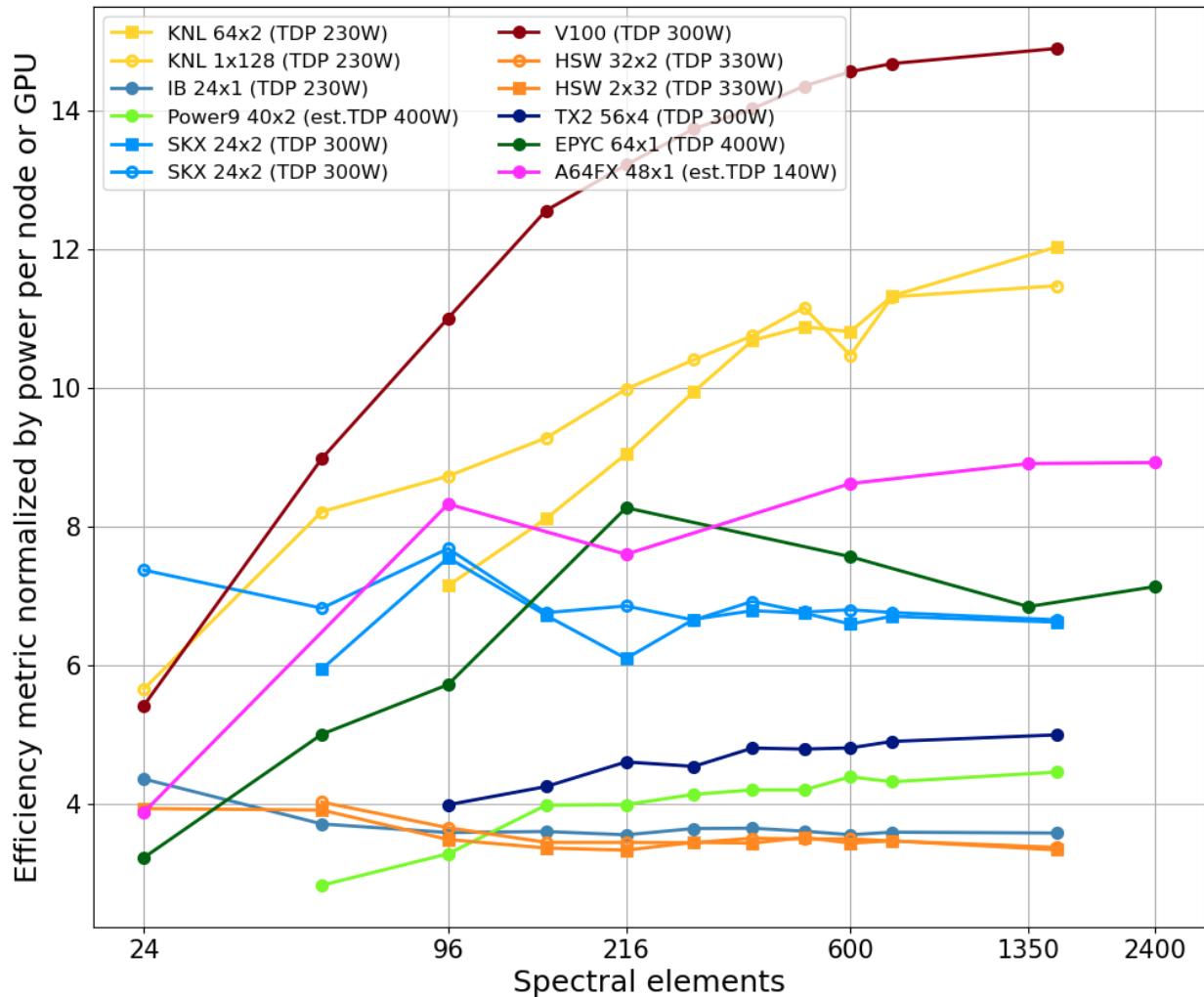


EXASCALE  
COMPUTING  
PROJECT

# Architecture Comparison: Power Efficiency

E3SM HOMME Dycore Benchmark: Cross-Architecture Comparison

(A64FX, EPYC results are preliminary)



A64FX: Promising performance/watt

Fugaku Node: Single A64FX socket  
GNU Fortran + MPI (48 ranks)

Note: Top Red (Volta V100), Pink (A64FX), Yellow (KNL)  
Higher is better

\* Plot of the efficiency metric normalized by power consumption on various hardware architectures. The legend includes a short descriptor for each architecture along with the number of parallel processes times (x) the number of threads and includes TDP in parenthesis. Specifically, the labels map as follows: KNL (Intel® Knights Landing), IB (Intel®Ivy Bridge), SKX (Intel® Skylake), V100 (NVIDIA® Volta), HSW (Intel® Haswell), A64FX (Fujitsu® A64FX), Power9 (IBM® POWER9), TX2 (Marvell®ThunderX2), EPYC (AMD® EPYC).



EXASCALE  
COMPUTING  
PROJECT

# Power and Performance tradeoffs

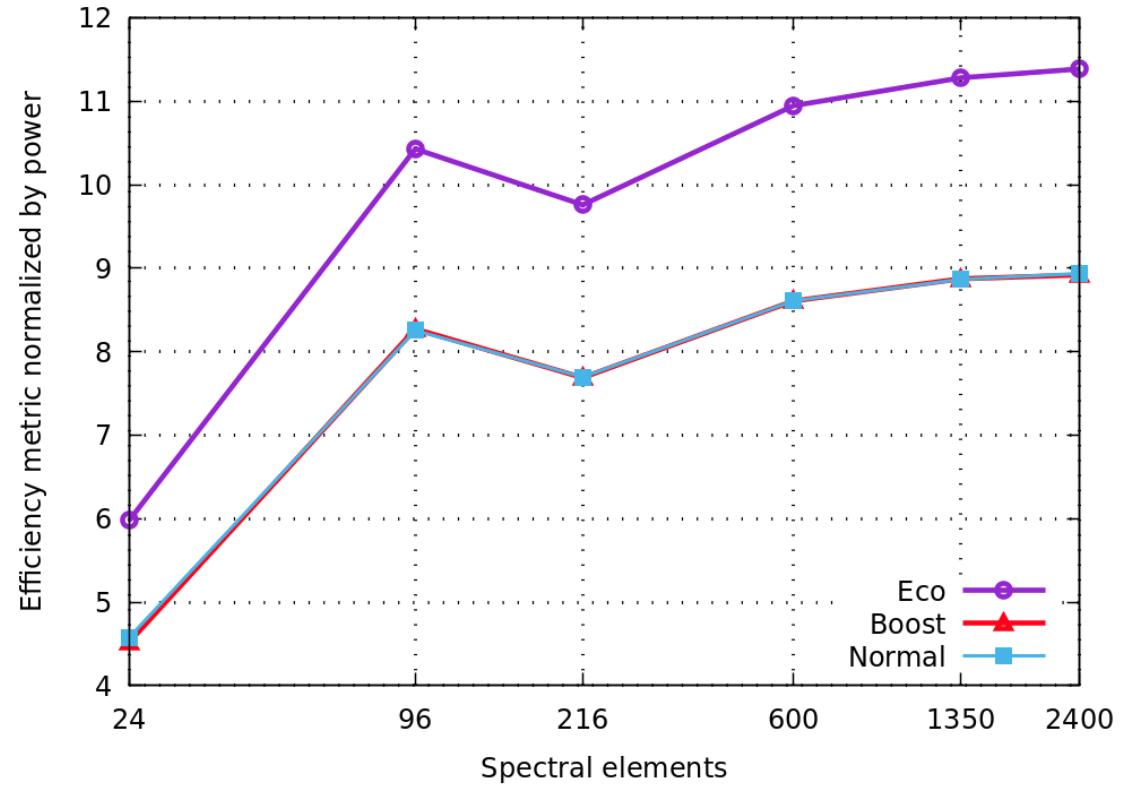
- Power Efficiency metric normalized by the measured power on the compute node

$$E_{\text{power\_measured}} = \frac{E_{\text{perf}}}{\text{measured\_power}}$$

- PowerAPI
- Three modes
  - Normal (2 GHz)
  - Boost (2.2 GHz)
  - Eco (2 GHz/eco\_state=2)
- Fortran version with GNU

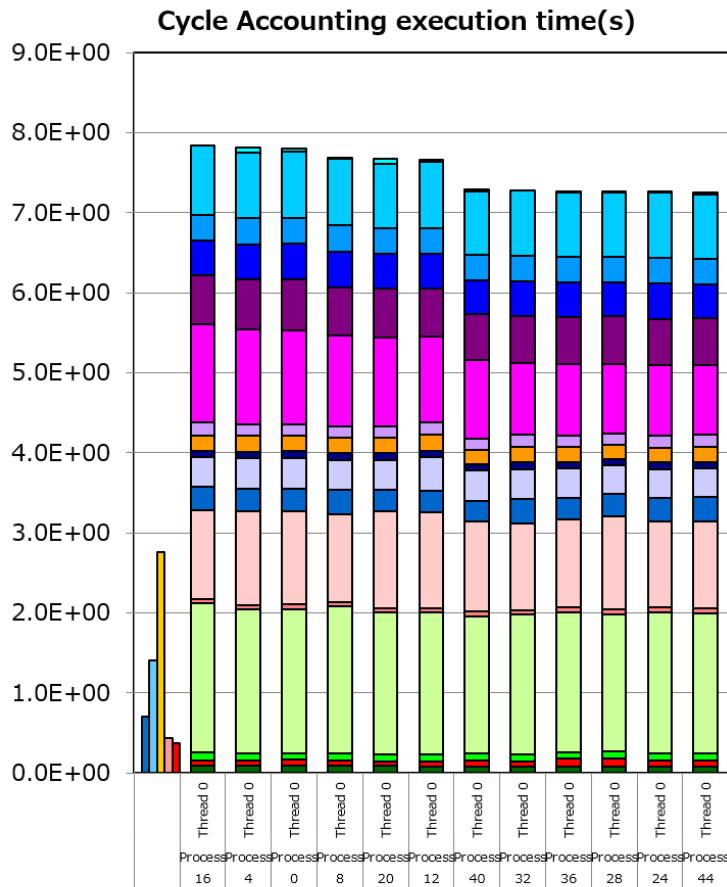
Higher is better

E3SM HOMME Dycore Benchmark: Power modes on Fugaku  
(Single node: 48 MPI ranks)



# Performance Characterization: Instruction mix

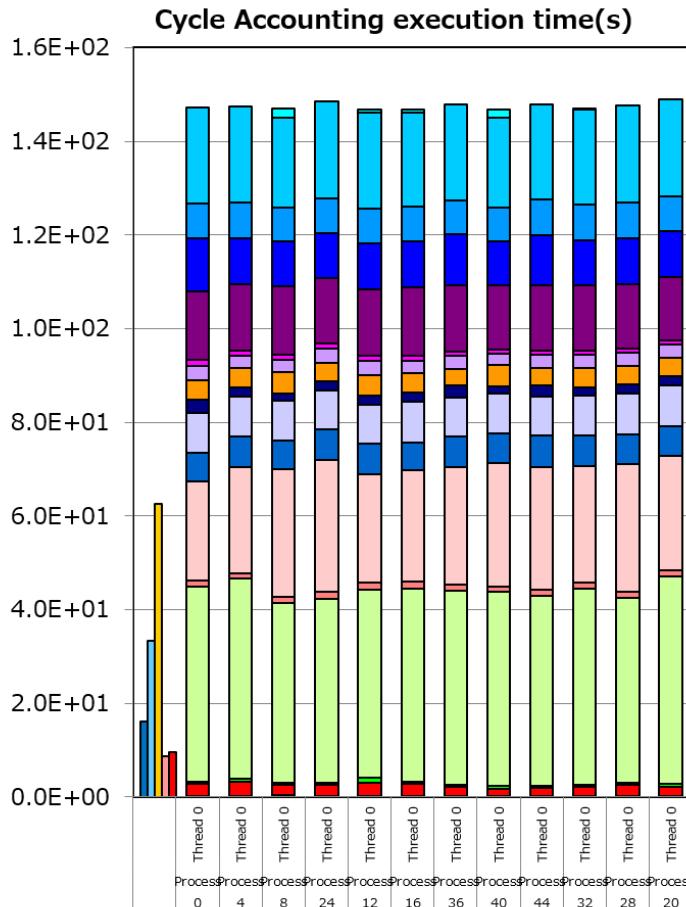
Spectral Elements: 96



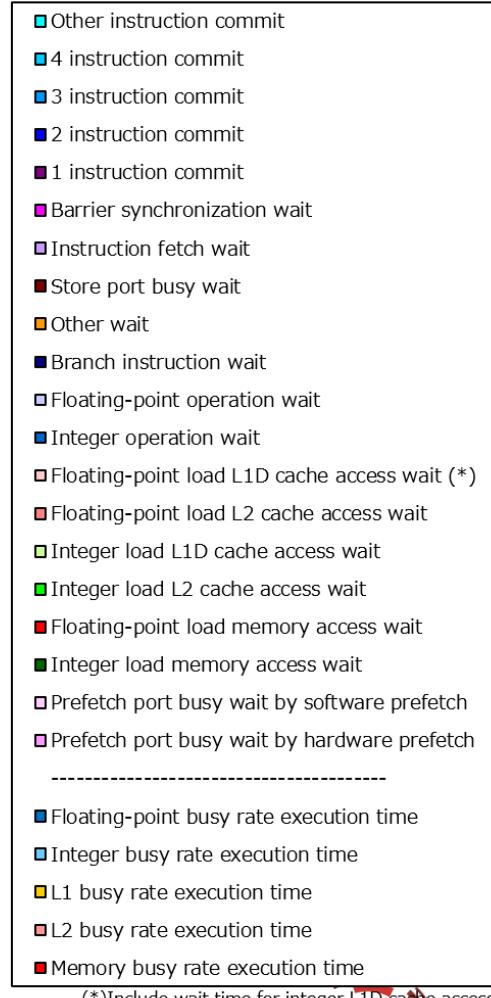
Significant fraction of runtime in the Integer Load L1D and Floating-point Load L1D cache access wait times

Left: pink section is Barrier synchronization wait

Spectral Elements: 2400

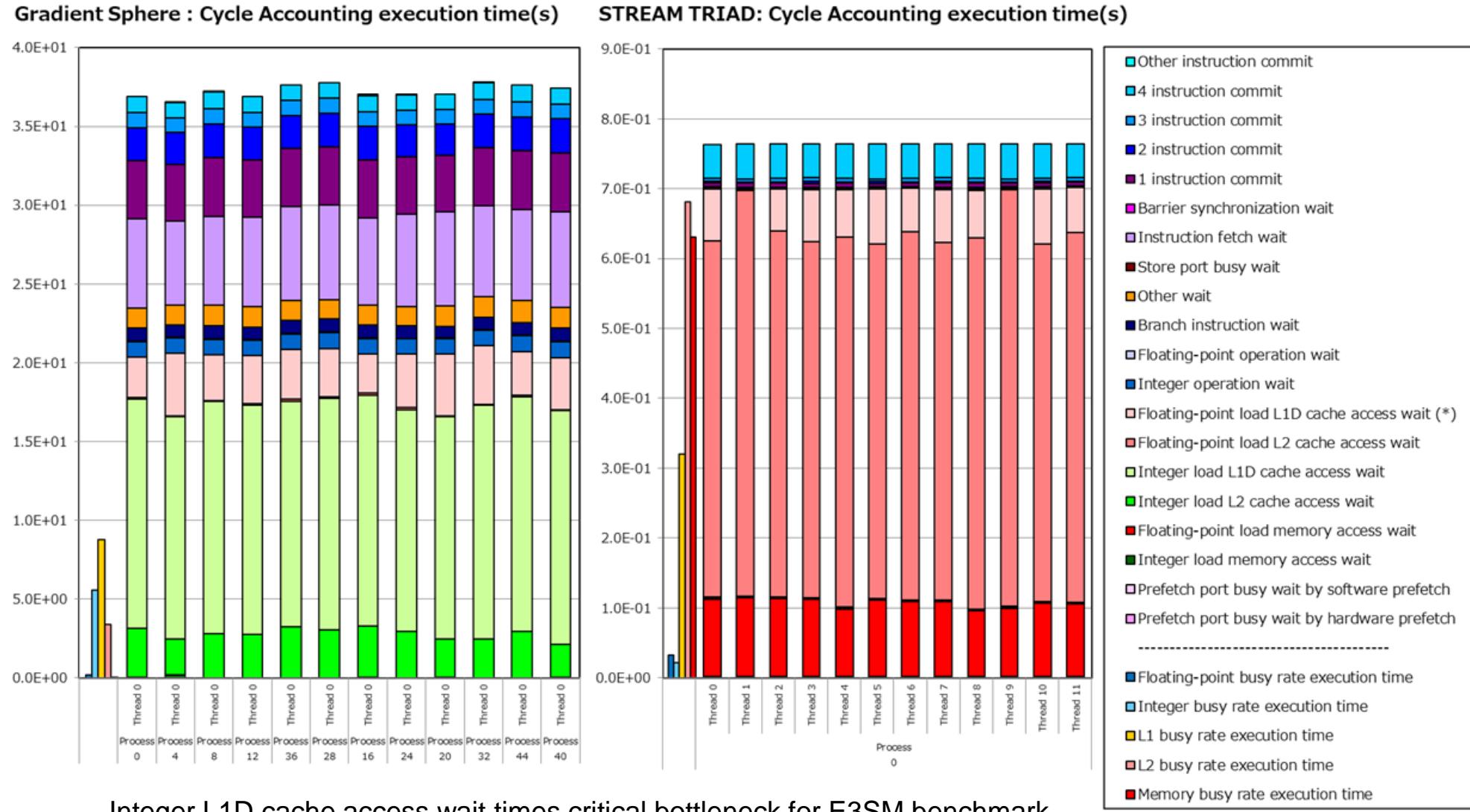


20 Categories



(\*)Include wait time for integer L1D cache access

# Instruction mix Comparison: Gradient sphere kernel vs. STREAM TRIAD



Integer L1D cache access wait times critical bottleneck for E3SM benchmark  
Mitigate high instruction latencies (INT: 5 cycles, FP: 8 cycles, SVE: 11 cycles)

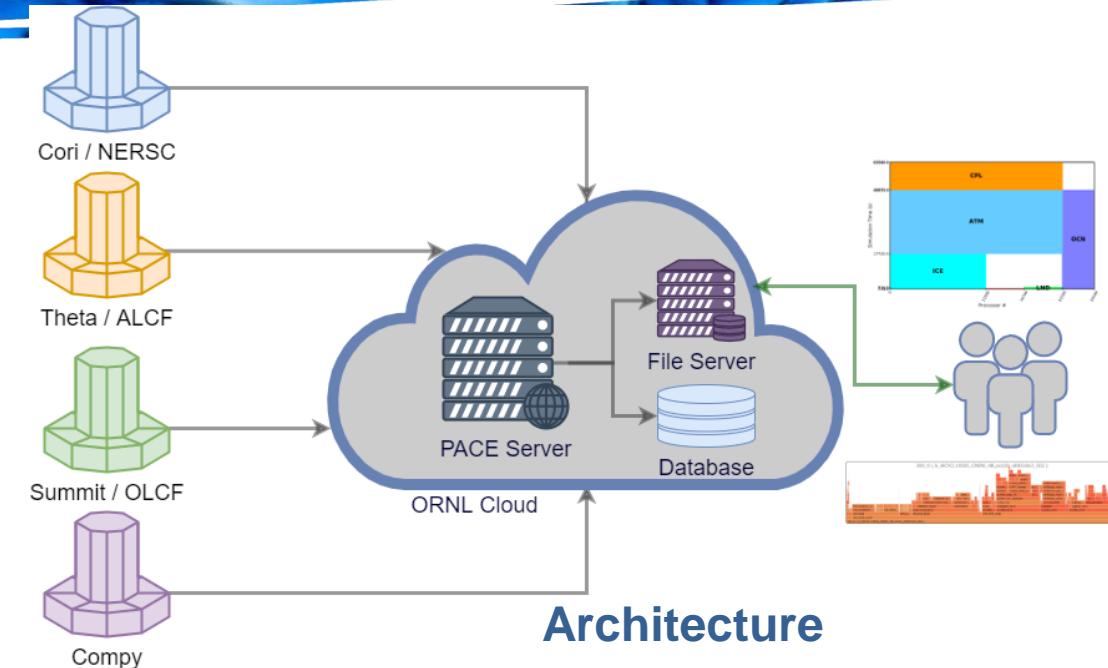
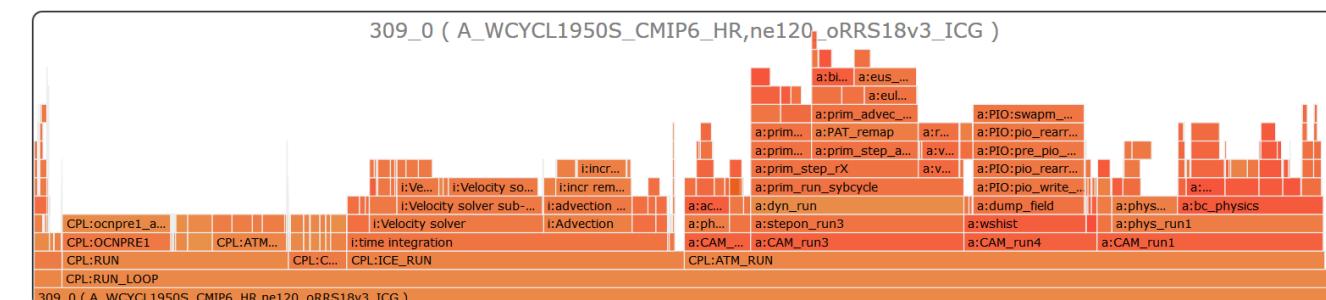


# Performance Analytics for Computational Experiments



## Summary

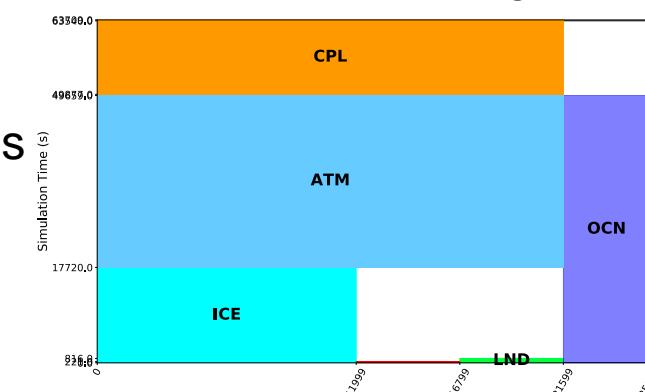
- Captures every E3SM experiment run on DOE supercomputers *automatically*
  - Performance Summary & Provenance
  - Facilitate performance research



## Stats

- 130k experiments**
- 3+ million input files**
- 200+ users**
- 14 platforms**

<https://pace.ornl.gov>

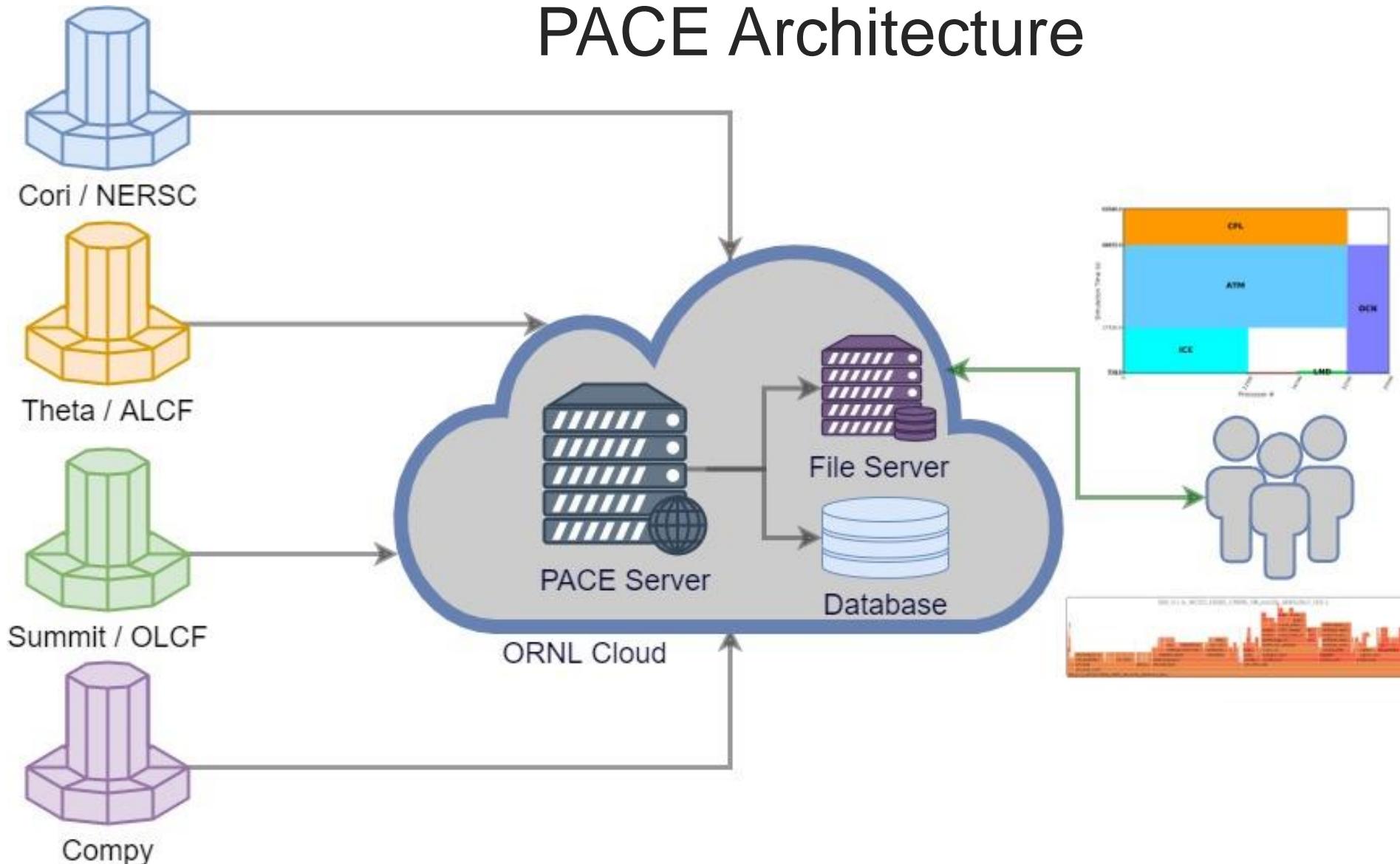


U.S. DEPARTMENT OF  
**ENERGY**

# E3SM Performance Data

- Lightweight performance profiling by default
  - Utilizes General Purpose Timing Library (GPTL) timers
  - Mark start/stop at defined application phases
  - Aggregate statistics for parallel processes
  - Collect computation, communication and I/O performance data
  - Support for PAPI hardware counters
- Performance Archiving
  - Enabled on supported platforms at OLCF, ALCF, NERSC etc.
  - Archive performance data in project wide locations
  - Provenance data for context and reproducibility
  - System state and various logs

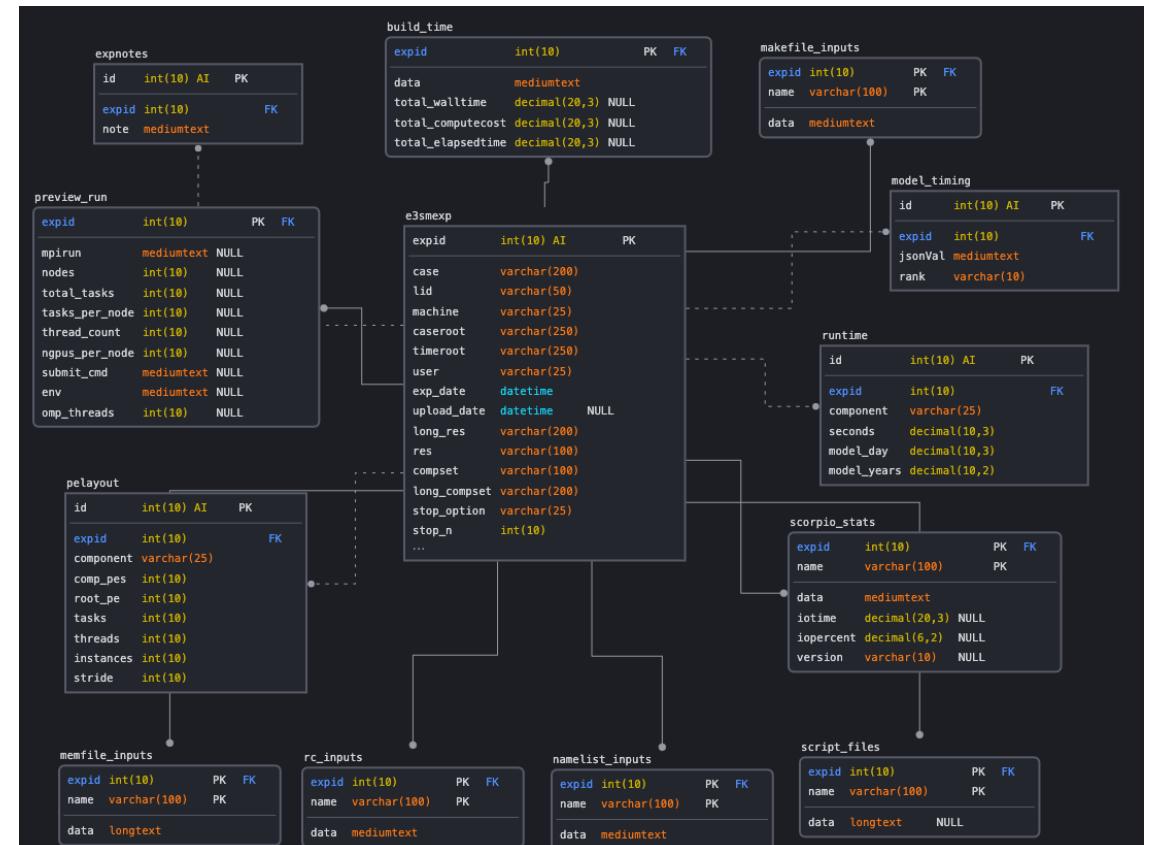
# PACE Architecture



# Technology Stack

- Infrastructure
  - ORNL Cloud (CADES)
  - OpenStack VM
- Nginx Web Server + Reverse Proxy
- Python-Flask middleware
  - Application Server
  - Process model inputs/timings
- Minio File Server
  - Object based storage for raw data
- MariaDB database
  - Structured and semi-structured data
  - Flexible Schema
- JavaScript
  - Frontend and visualization

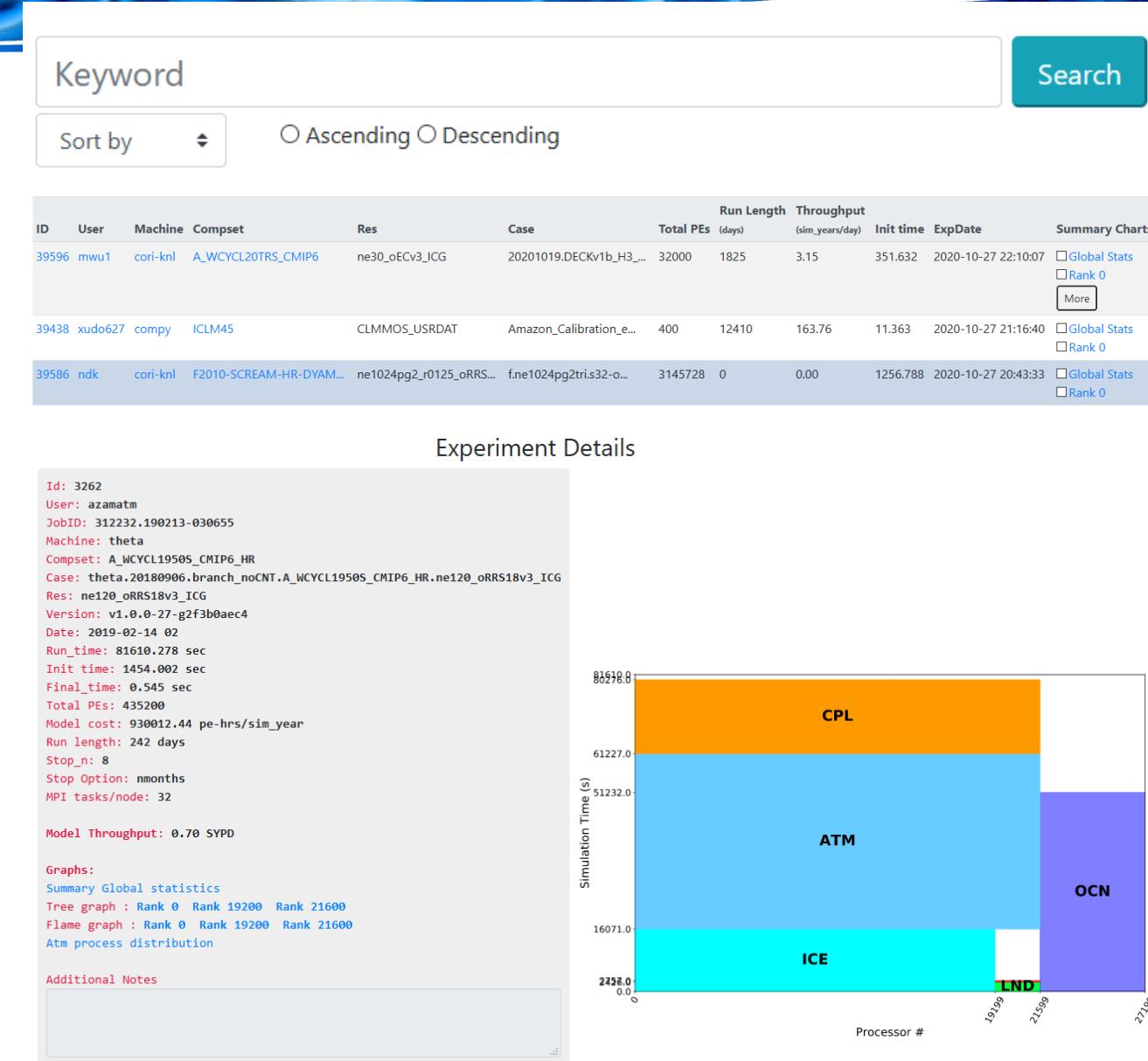
# Database Schema



Last but definitely not least:  
Cybersecurity compliance at a DOE lab

# Usage

- Search for existing experiment using case, compset, grid, user etc. (Autocomplete supported)
  - Sort by desired criterion
- Click on a row from search results to dive into specific experiment
- Experiment details page contains
  - Metadata: user, machine, date etc.
  - Provenance: Browse model inputs
  - Performance overview
    - Model, Component throughputs
  - Process layout diagram
  - Links to detailed performance graphs

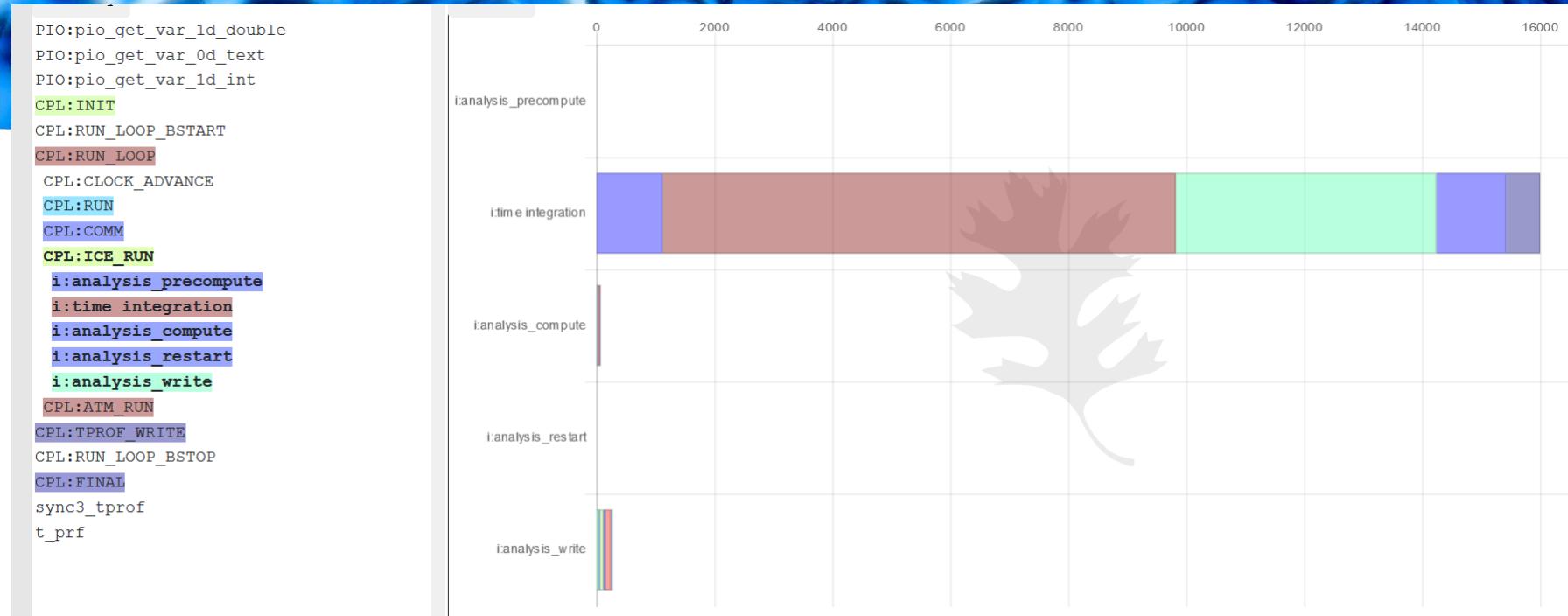




# Tree Graph

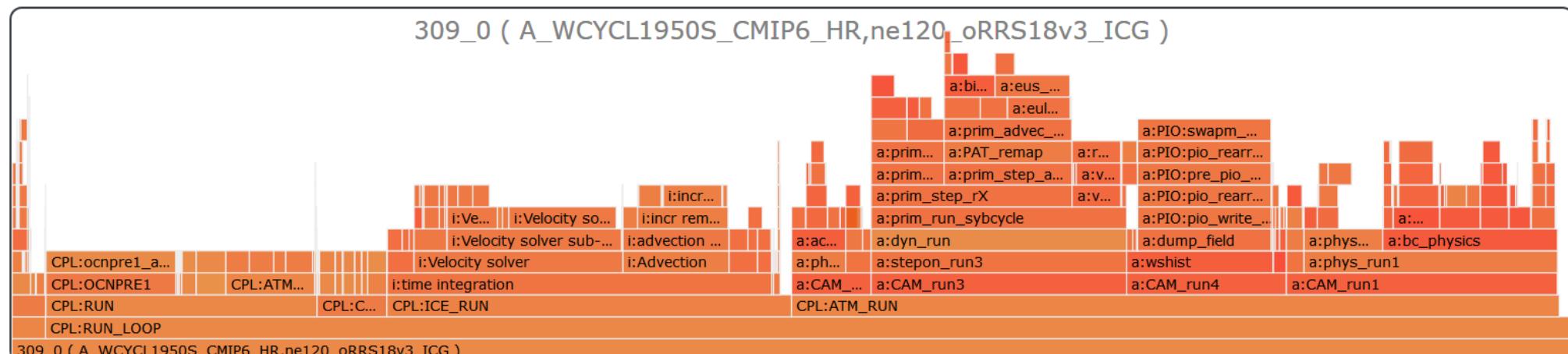
## Summarize time taken by model components

Recursively explore time taken by model sub-regions

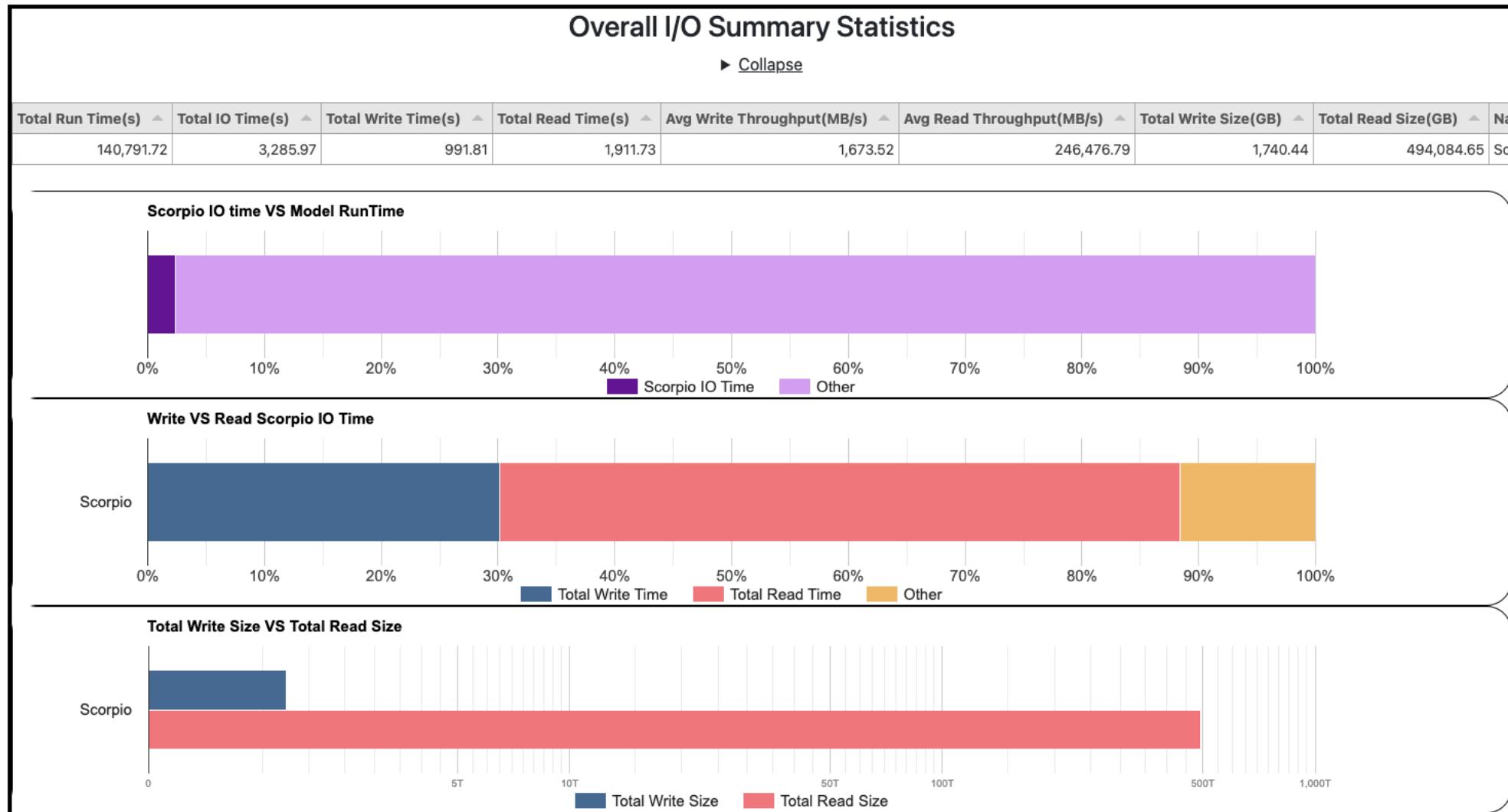


# Flame Graph

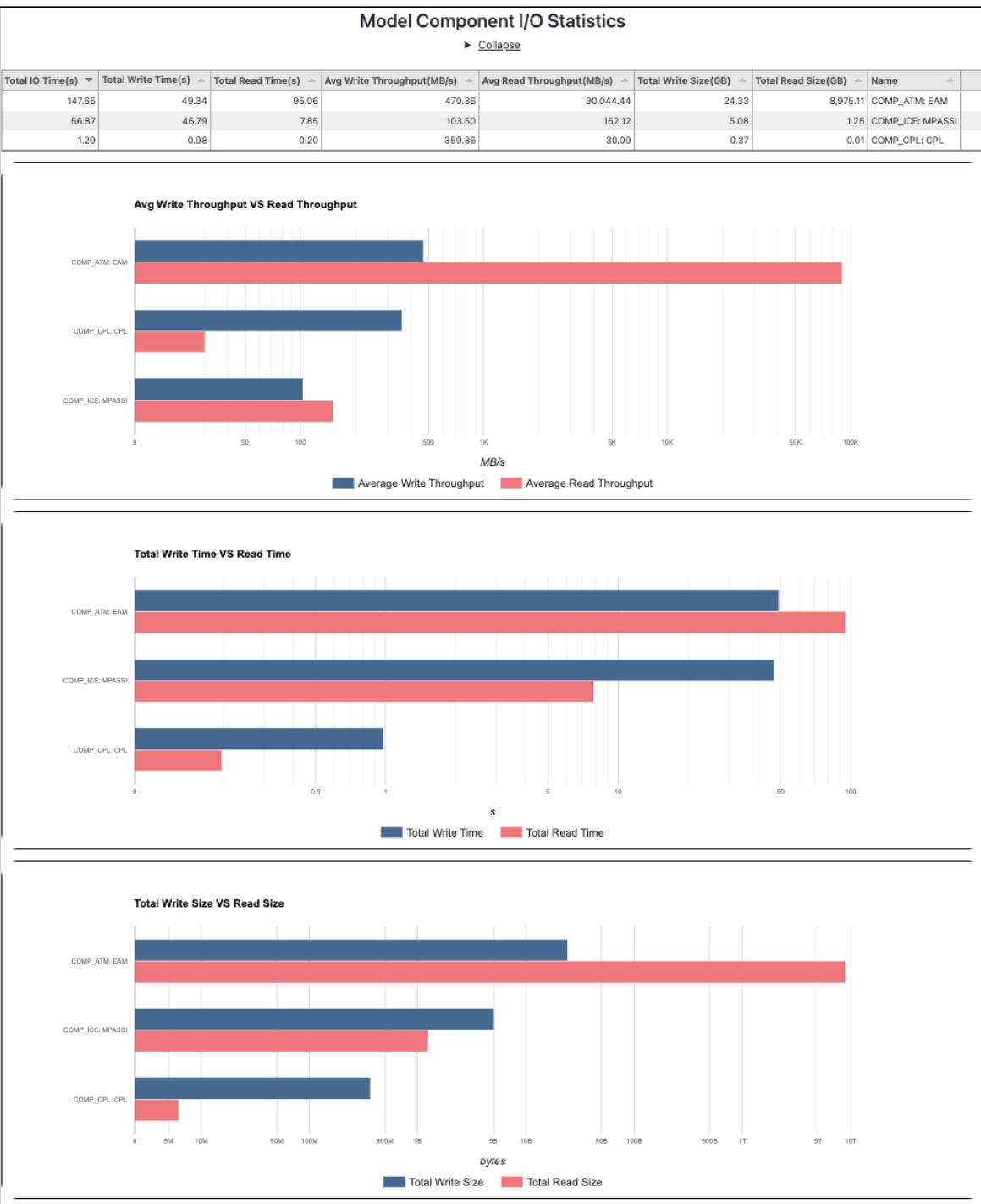
# High-level overview of a parallel process execution time



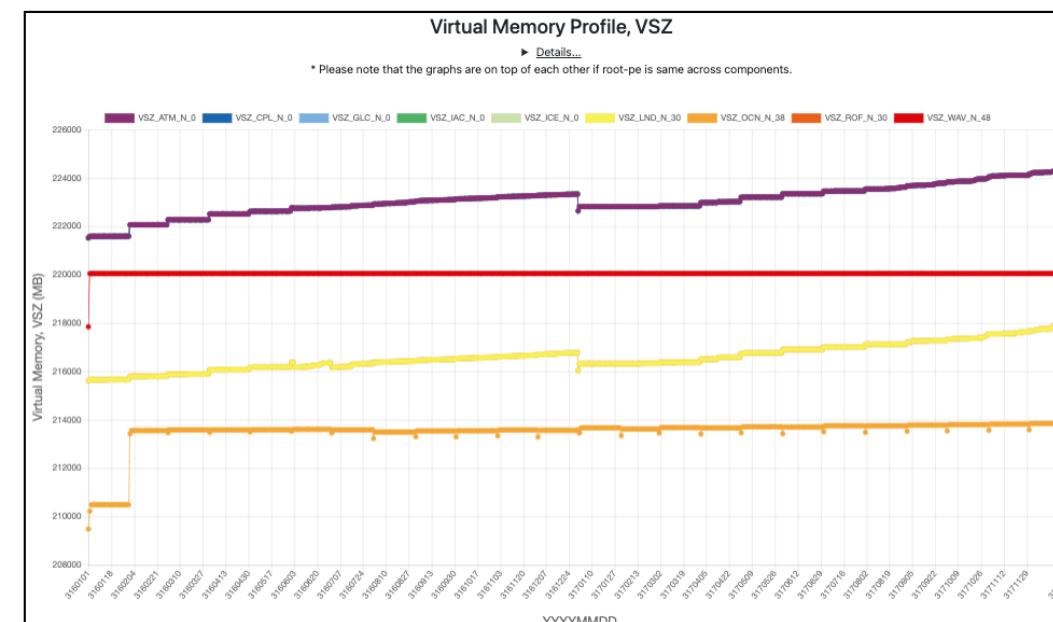
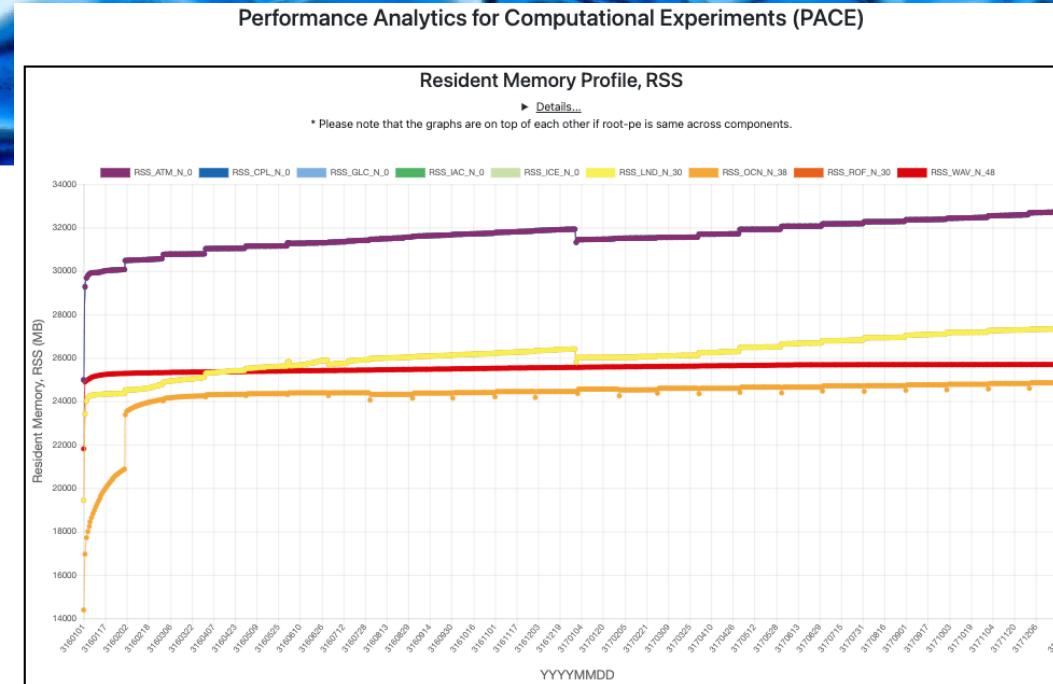
# I/O Performance



# I/O Performance Details



# Memory Profiles



# Build Profiles

## Build Times

**Wall Time for build:** 2039.520 sec.

**Total Compute Cost of build:** 2423.561 sec.

**Note:** This is the total cost associated with compilation across components.

Typically, the wall time is lower due to parallel builds.

► Details

Time (s) ▾	File Name
2,039.520000	Total_Build
139.131729	CMakeFiles/lnd.dir/_/_/elm/src/data_types/VegetationDataType.F90.o
63.014300	CMakeFiles/ocn.dir/_/_/core_ocean driver/mpas_ocn_core_interface.f90.o
60.025588	CMakeFiles/ice.dir/_/_/core_seaice/model_forward/mpas_seaice_core_interface.f90.o
52.475535	CMakeFiles/atm.dir/_/_/eam/src/physics/cosp2/local/cosp.F90.o
51.702540	CMakeFiles/lnd.dir/_/_/elm/src/data_types/ColumnDataType.F90.o
51.515716	CMakeFiles/lnd.dir/_/_/elm/src/biogeochem/CNCarbonFluxType.F90.o
35.808562	CMakeFiles/atm.dir/_/_/eam/src/physics/clubb/mt95.f90.o
27.578397	CMakeFiles/lnd.dir/_/_/elm/src/external_models/fates/main/FatesHistoryInterfaceMod.F90.o
25.040277	CMakeFiles/rof.dir/_/_/mosart/src/riverroute/RtmMod.F90.o

# Ongoing and Future

## Assistant

- Simulation planning
- Process layouts
- Data analytics
- Anomaly detection
- Allocation reports



Steve The Minion – from Pixabay  
<https://pixabay.com/photos/minions-banana-steve-the-minion-2552584/>

## Wizard

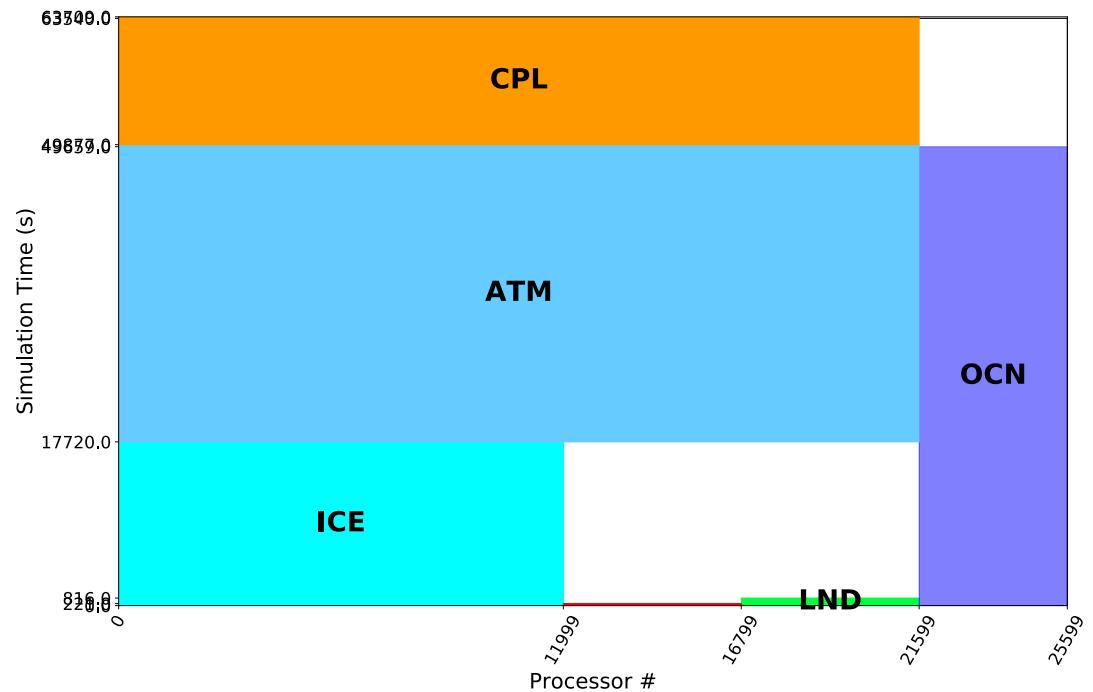
- Recommend optimizations
- Optimal resource allocations
- Machine Learning
- Communication optimization
- Active monitoring and reporting



Dennis Jarvis from Halifax, Canada / CC BY-SA  
(<https://creativecommons.org/licenses/by-sa/2.0>)

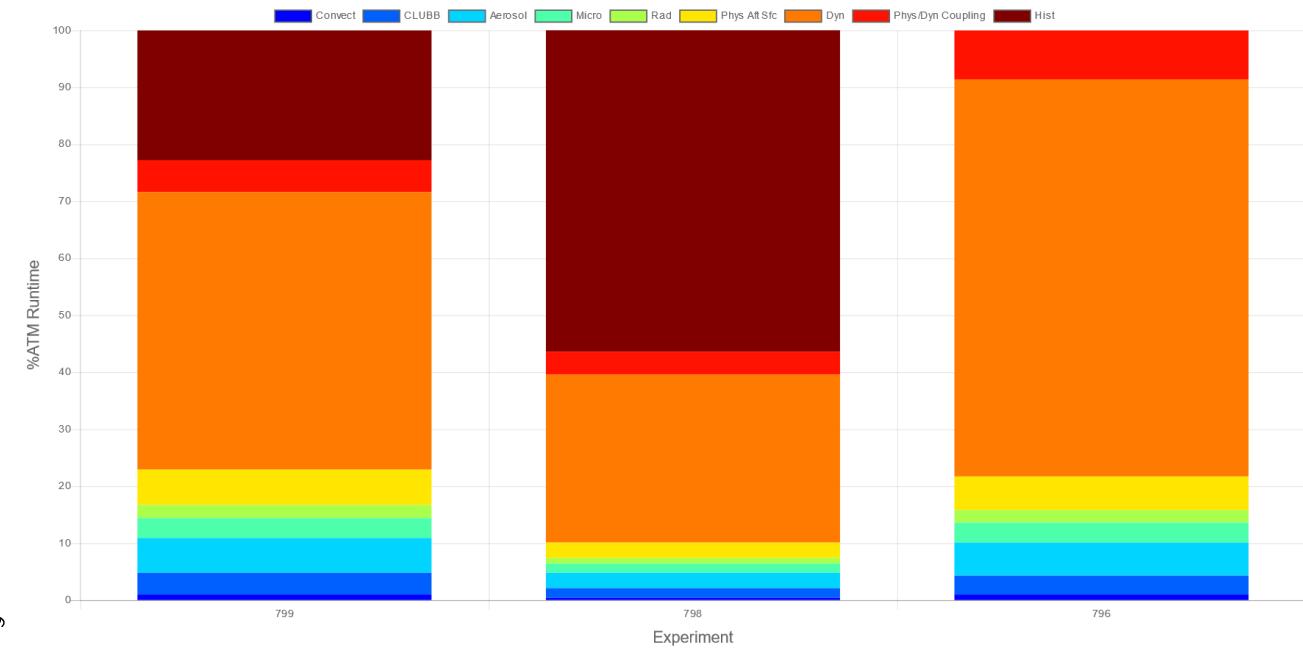
# Performance Research Directions

## Resource Allocation and Load Balancing



MPI Task Mapping

## Targeted Optimization



Atmosphere model time distribution

# EarthInsights: Parallel Clustering of Large Earth Science Datasets on the Summit Supercomputer

**Sarat Sreepathi**<sup>1</sup>, Jitendra Kumar<sup>1</sup>, Forrest M. Hoffman<sup>1</sup>,  
Richard T. Mills<sup>2</sup>, Vamsi Sripathi<sup>3</sup>, William W. Hargrove<sup>4</sup>

<sup>1</sup>Oak Ridge National Laboratory

<sup>2</sup>Argonne National Laboratory

<sup>3</sup>Intel Corporation

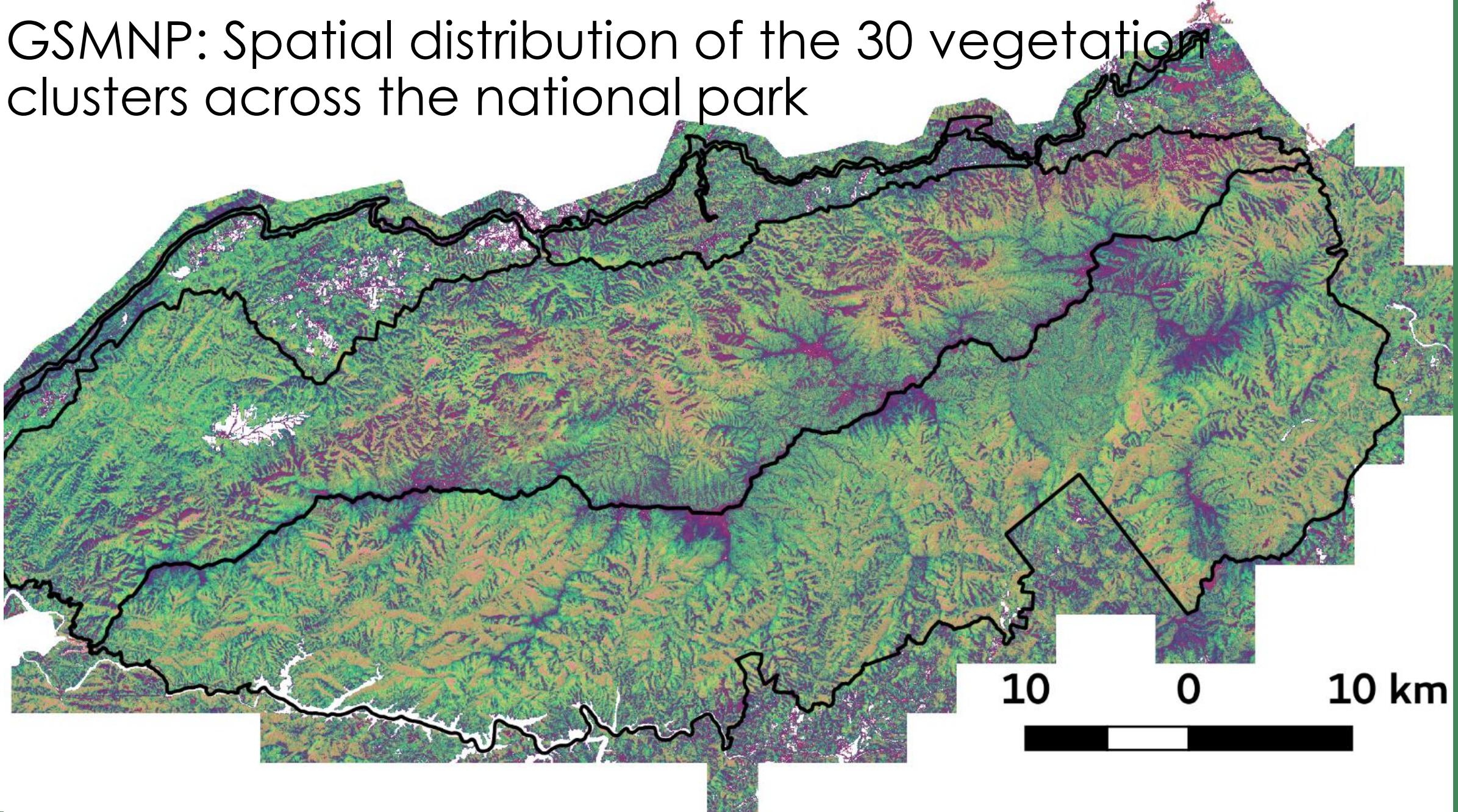
<sup>4</sup>USDA Forest Service

ORNL is managed by UT-Battelle, LLC for the US Department of Energy



Adapted from talk given at  
1<sup>st</sup> Workshop on Leveraging Artificial Intelligence (AI) in the  
Exploitation of Satellite Earth Observations & Numerical  
Weather Prediction(NWP)  
NOAA, College Station, MD  
April 23-25, 2019

# GSMPN: Spatial distribution of the 30 vegetation clusters across the national park

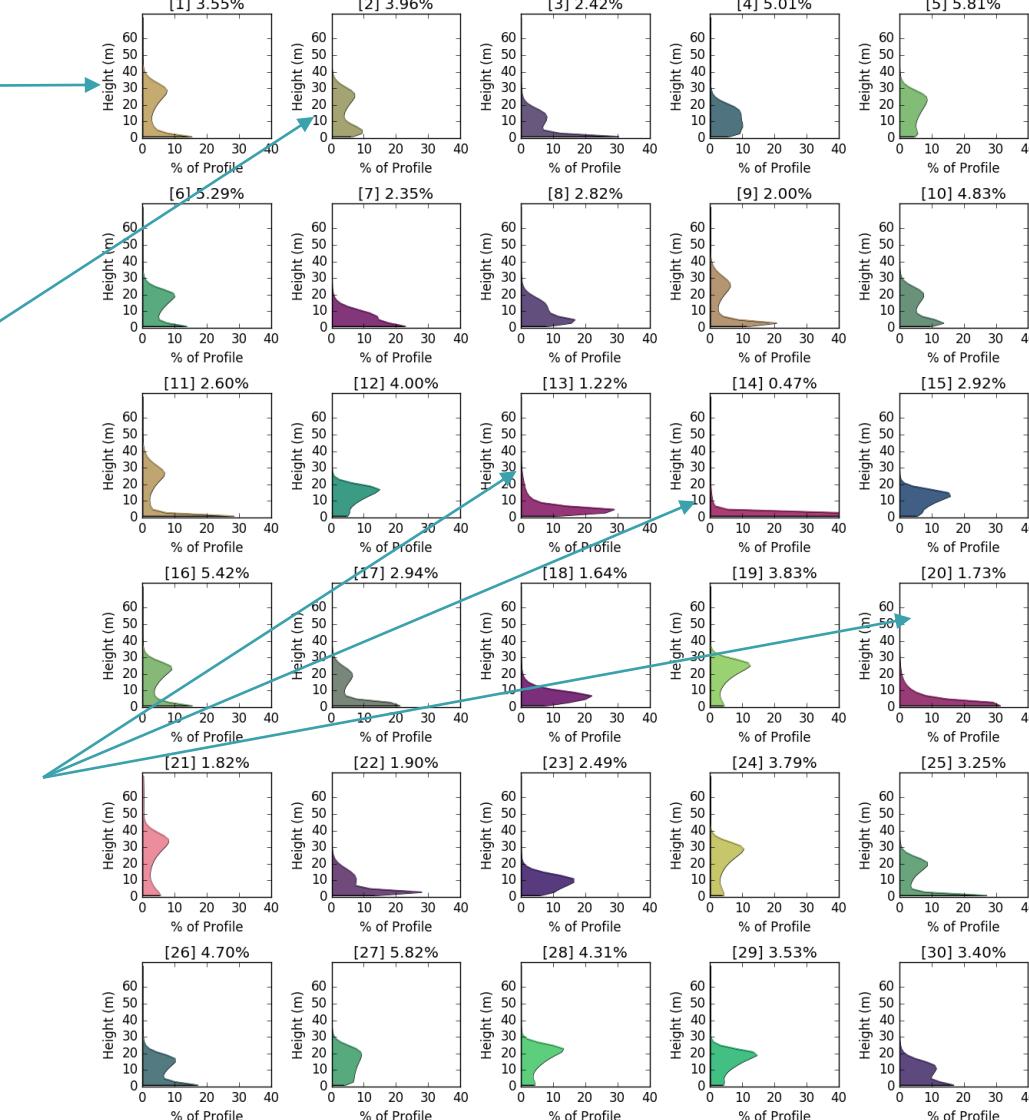


# GSMNP: 30 representative vertical structures (cluster centroids) identified

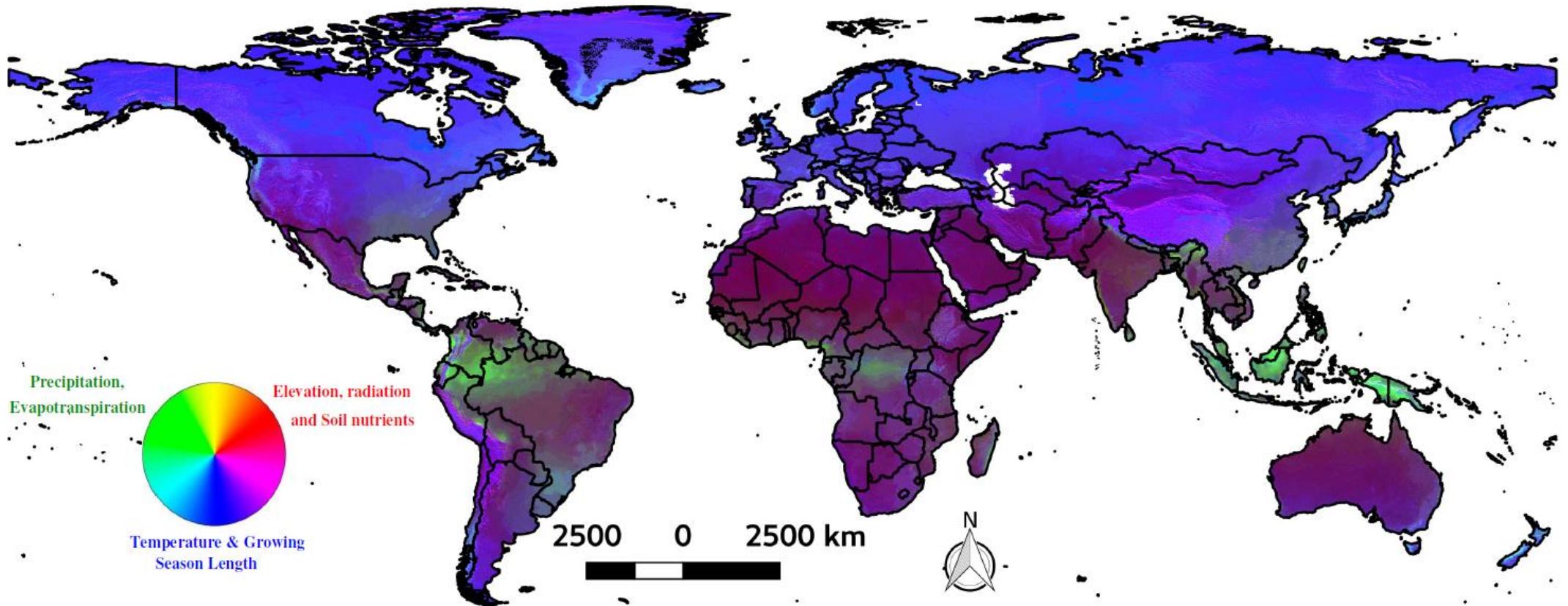
tall forests with low  
understory vegetation

forests with slightly lower  
mean height with dense  
understory vegetation

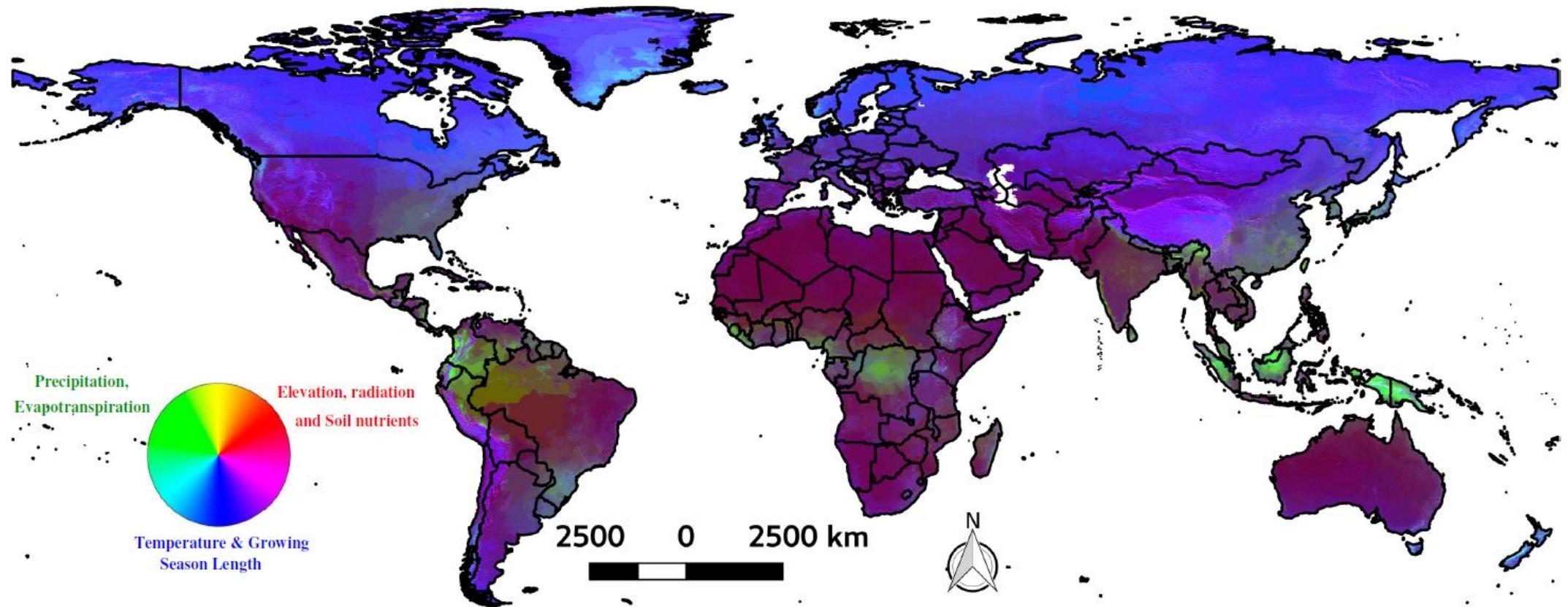
low height grasslands and  
heath balds that are small  
in area but distinct  
landscape type



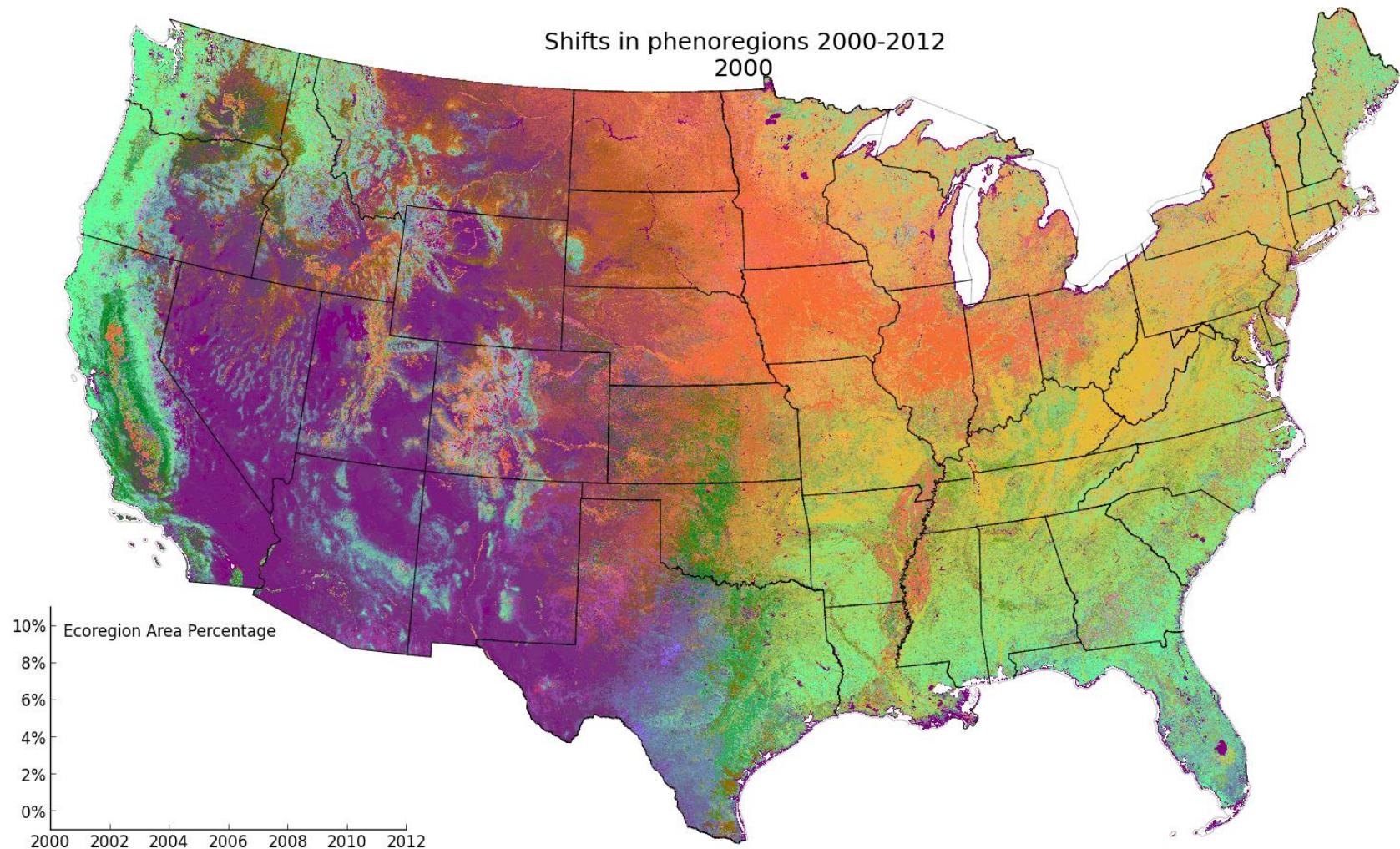
# Global Climate Regimes: 1000 clusters Contemporary using Similarity color scheme



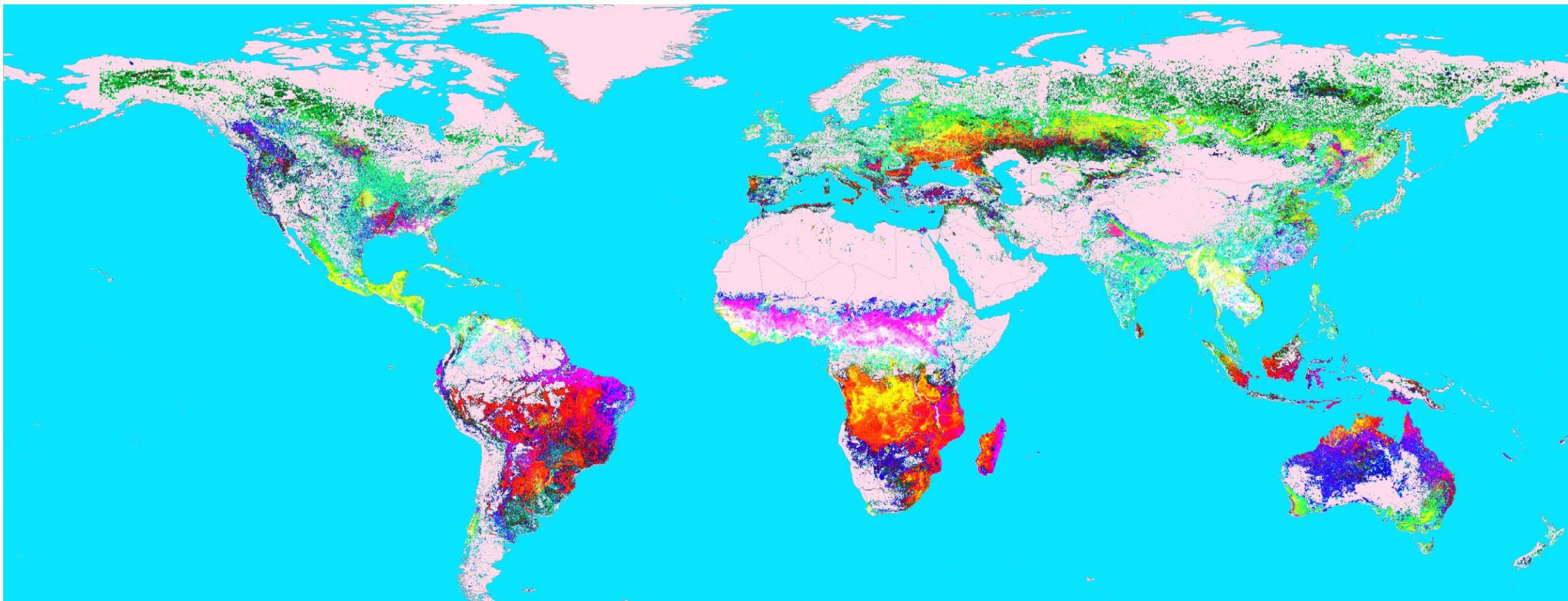
# Global Climate Regimes: 1000 clusters 2100 using Similarity color scheme



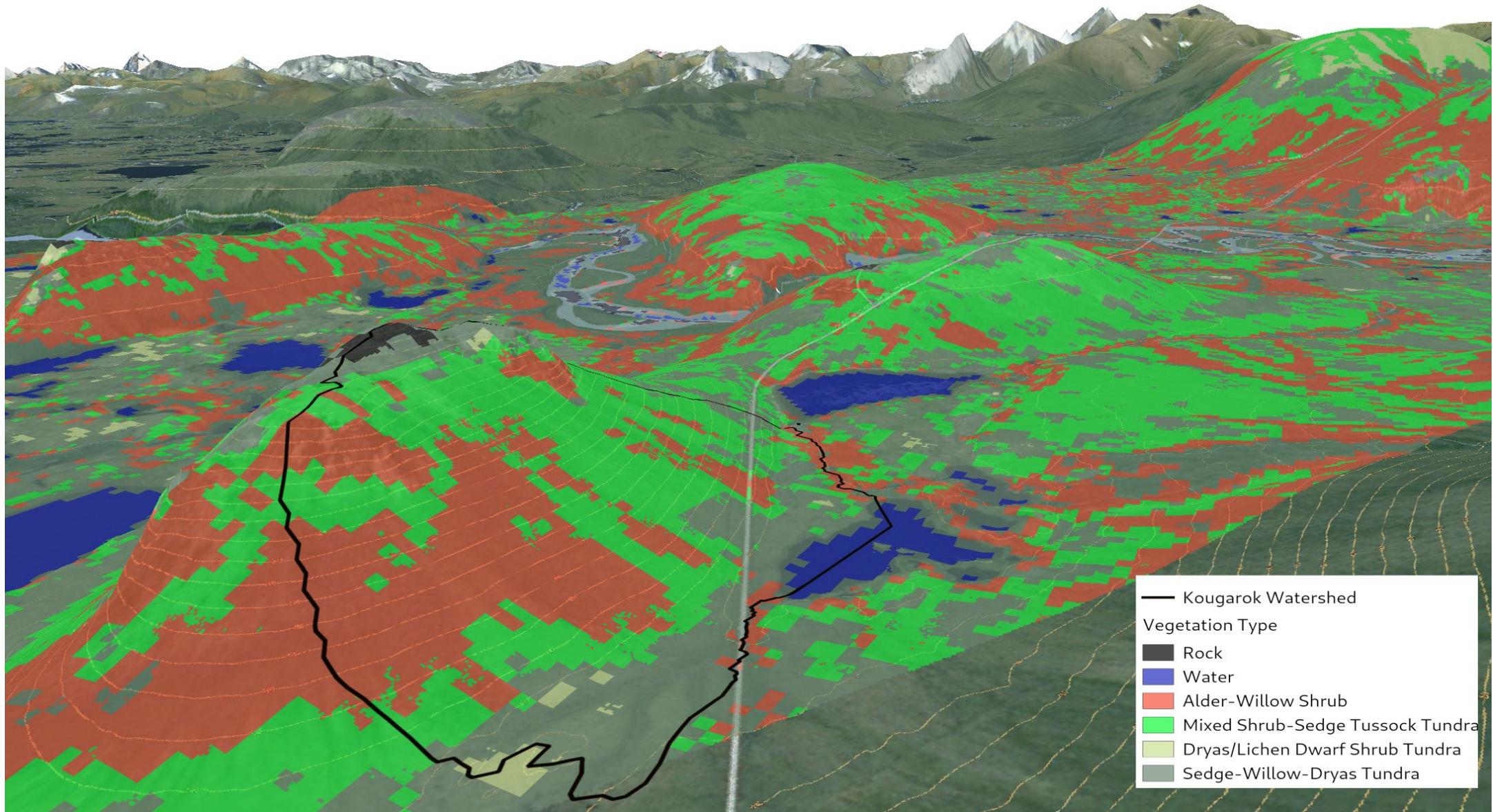
# CONUS dynamic phenoregions



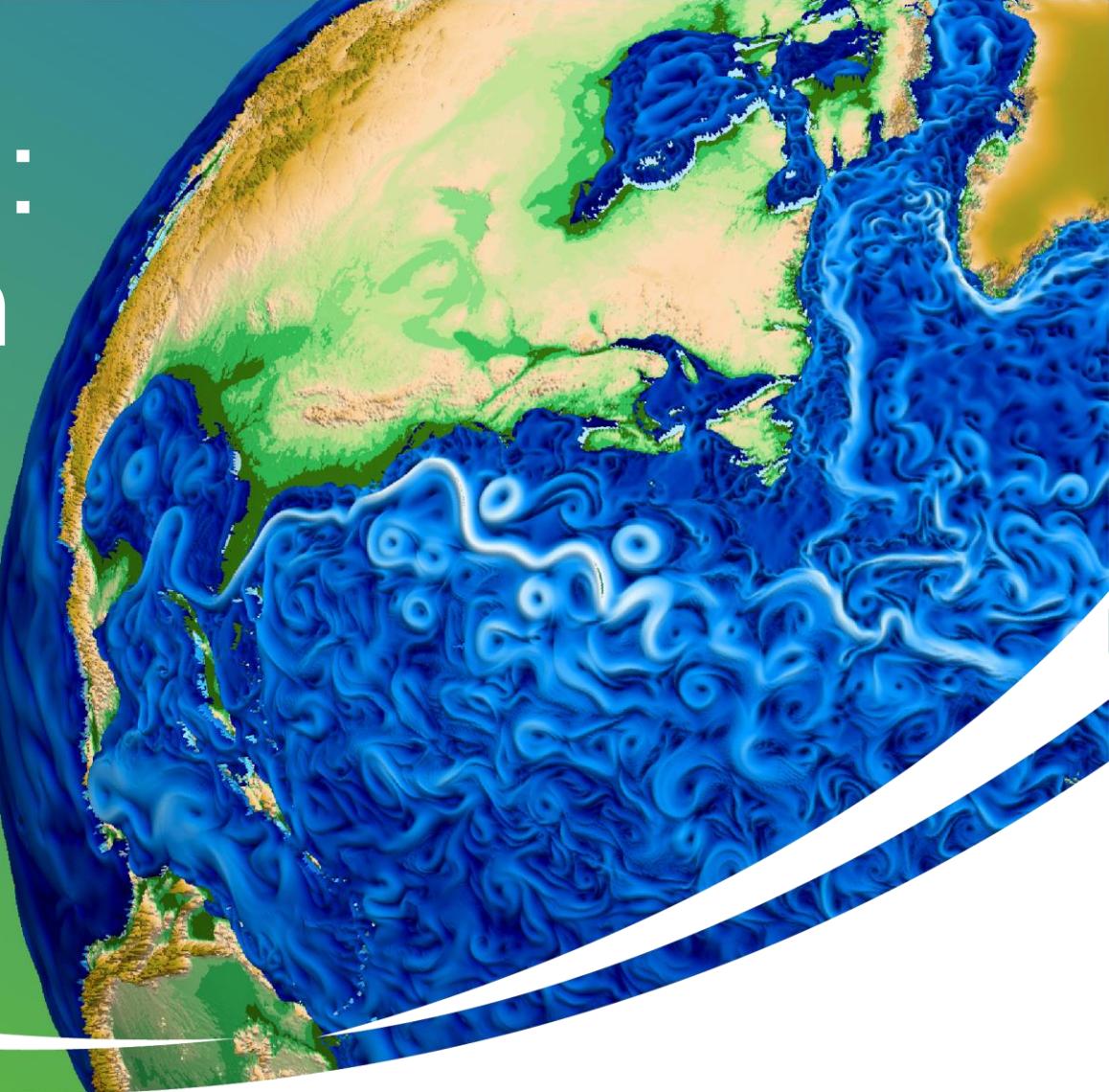
# Global Fire Regimes



# Arctic: High-resolution vegetation mapping

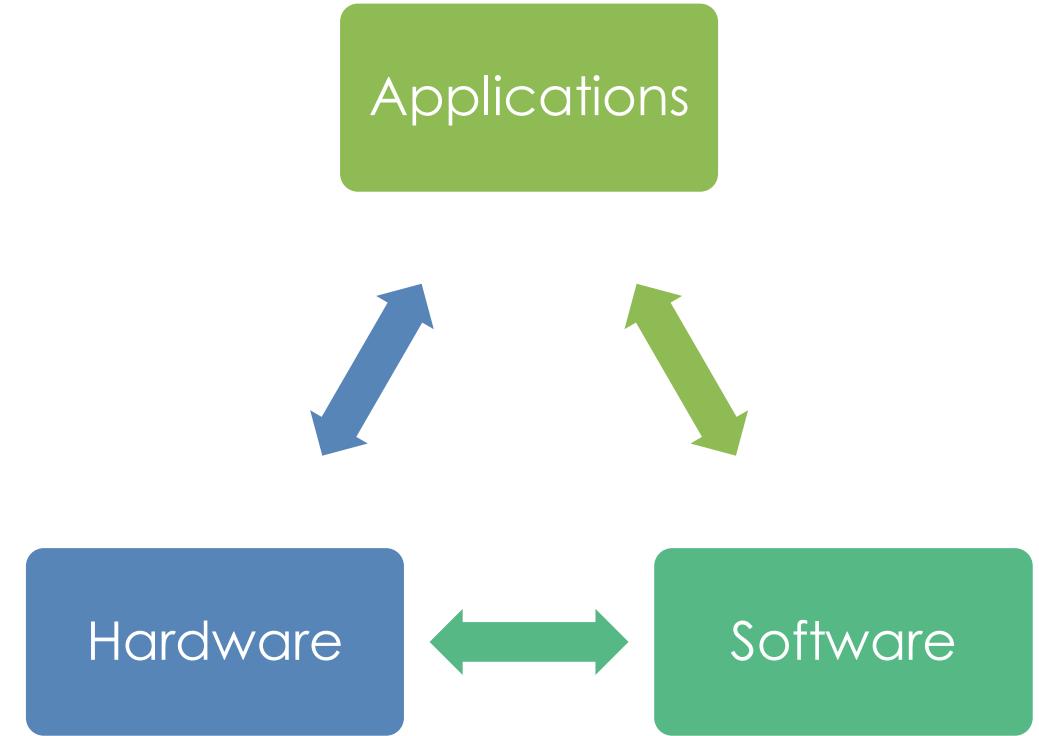


# Exascale and Beyond: Application Co-design



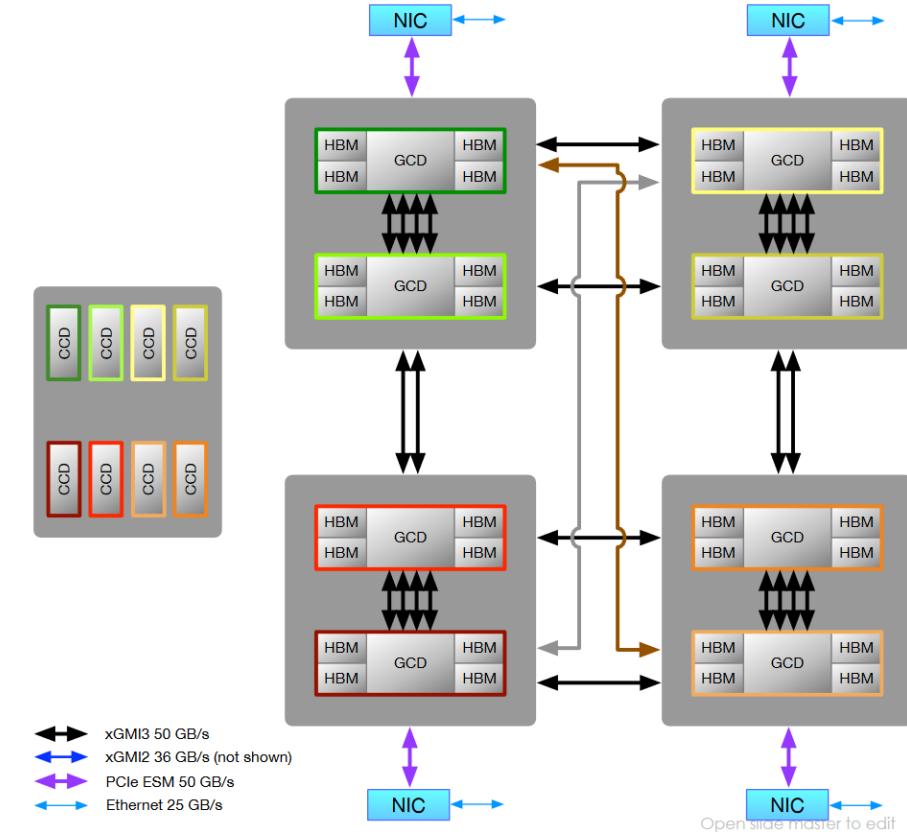
# Co-design

- Feedback loop between applications, system s/w and computer architecture
- Application requirements inform (influence?) hardware design
- Technology choices and constraints guide problem formulation and design of algorithms.



# Today's Exascale: Frontier

- 1.1 Exaflops (FP64 – HPL Benchmark)
- 29 MW
- 4,000 ft<sup>2</sup>
- 9,408 nodes
- Node
  - 4 AMD MI250X GPUs/node
  - Equiv. **8 logical GPU's/node**
  - 512 GiB DDR4 (CPU) + **512 GiB HBM2e (GPU)**
  - **GPU Mem B/W:** 8x 1,635 GB/s (**13,080 GB/s Total**)
  - 1 AMD Trento CPU (64 cores)
- GPUs directly connected to high-speed interconnect
- **Aurora: Still under NDA**



Frontier Compute Node Architecture  
1 CPU, 8 GPU's

One cabinet of Frontier (24 ft<sup>2</sup>) has higher HPL than all of Titan (4,500 ft<sup>2</sup>) while using lower power (309 kW vs. 7 MW)

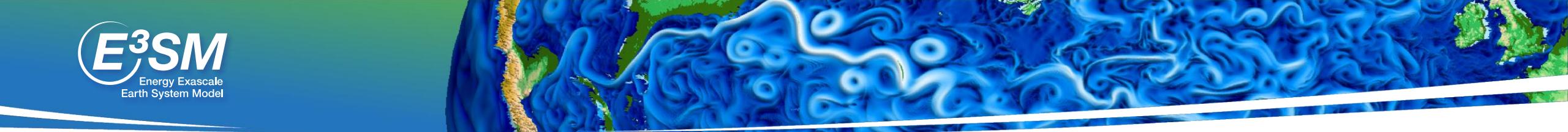
# DOE Thinking

- [DOE RFI](#) – Summer 2022
  - Computing vendors and system integrators
  - Next generation supercomputers for 2025-2030 timeframe
- 10-20 FP64 exaflops in 2025 (8x from 2022)
- 100+ FP64 exaflops in 2030 (64x from 2022)
- 20-60 MW
- 4000 ft<sup>2</sup> (+ 50% more option)
  - Optional
    - Upgradability: Every 1-2 years
    - Emerging accelerators (Quantum,...) if feasible
    - Hybrid: On-prem + Cloud



Contract Opportunity  
General Information

[Request for Information - Advanced Computing Ecosystems \(Dept. of Energy\)](#)



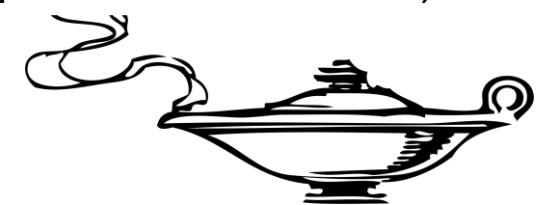
# New Golden Age for Computer Architecture

- Increasing heterogeneity
- Hybrid chips (APU/XPUs)
- Divergence of AI and HPC
  
- Chiplet-based System-on-Chips
- Widespread HBM
- 3D stacking
- Low-power ARM (A64FX, Grace,...)
- RISC-V
- Processing in memory
- Silicon Photonics, Optical Interconnects
  
- Open Source Hardware
  - [DARPA Electronic Resurgence Initiative \(ERI\)](#)
  - Numerous [semiconductor startups](#)
  
- Moore's Law, Dennard Scaling
- Quantum: Optimization problems
  - Noisy Intermediate-Scale Quantum (NISQ): Practically useful?
- Neuromorphic: No clear fit
  - Spiking Neural Networks: Perhaps wavefront computations

# Planning under uncertainty: A perspective

- Compute Architectures and Science: Friends or Frenemies?
  - Creativity for effective science
- High-end scientific computing: Leading vs. following
  - Cultivate and nurture vendor relationships
  - *Strategize ahead and influence vs. starting after general availability of an architecture*
- Co-design: Key application kernels and mini-apps
  - Impact on hardware: Skepticism warranted
  - Ray of hope (software/compilers)
- Changing Economics of Hardware Design
  - Fugaku (\$1B incl. R&D), Frontier \$600M procurement
- Wish: Imagine *relatively affordable* custom chips
  - Opinion: Better bet than fusion-powered quantum computers

(Domain-specific architectures)



# Acknowledgments



EXASCALE COMPUTING PROJECT



Energy Exascale  
Earth System Model



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