

2 February

Tuesday

33-332 / Week 6

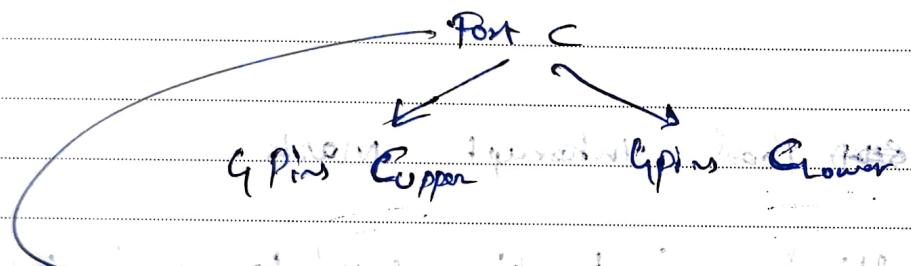
February 2021						
S	M	T	W	T	F	S
1	2	3	4	5	6	
7	8	9	10	11	12	13
14	15	16	17	18	19	20
21	22	23	24	25	26	27
28						

8255

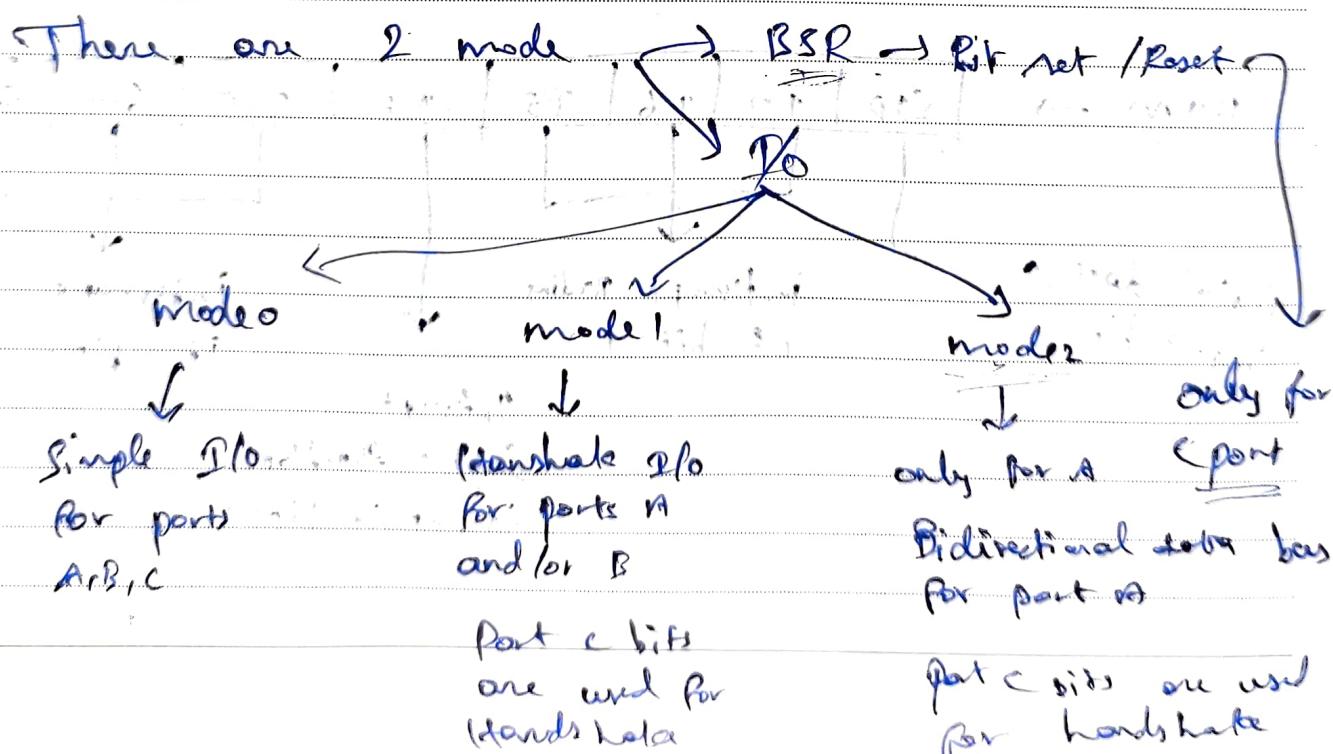
26 has 16 I/O pins

which are segregated as Port A - 8 pins

Port B - 8 pins



And also access individual pins in port C

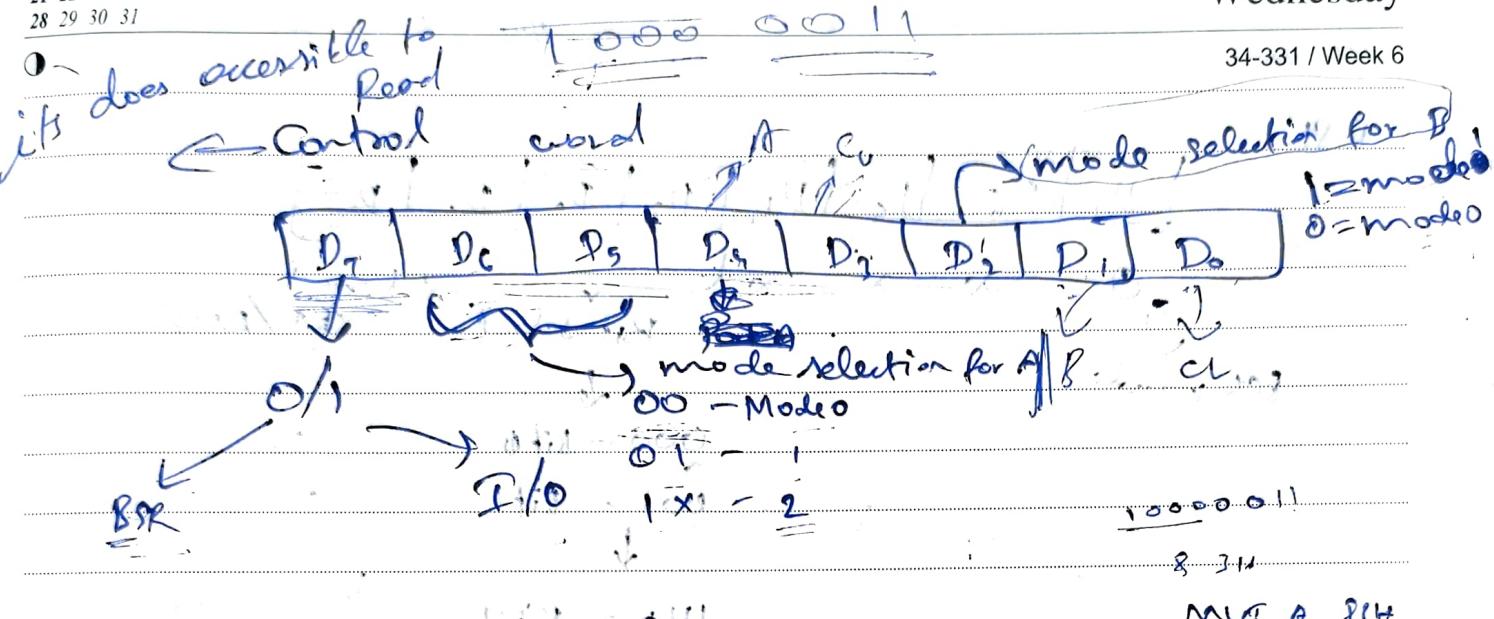


March 2021						
S	M	T	W	T	F	S
1	2	3	4	5	6	
7	8	9	10	11	12	13
14	15	16	17	18	19	20
21	22	23	24	25	26	27
28	29	30	31			

February 3

Wednesday

34-331 / Week 6



six lines for control 8255

MUX A, 8254

OUT 8254

\overline{WR} , \overline{RD} , \overline{CS} , RESET \rightarrow clears control register and sets all ports in the input mode

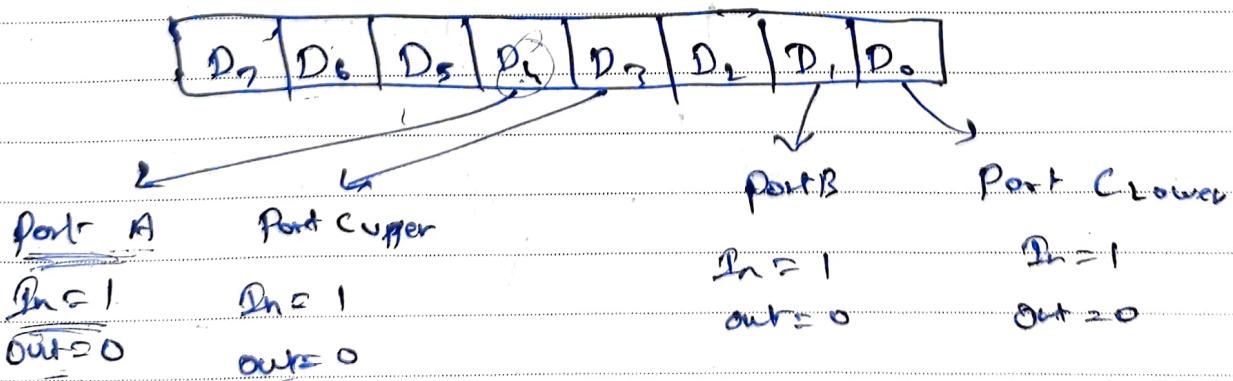
A_1, A_0 Selected

0 0 \rightarrow Port A

0 1 \rightarrow Port B

1 0 \rightarrow Port C

1 1 \rightarrow Control register



4 February

Thursday

35-330 / Week 6

BSR

