

2 February

Tuesday

February 2021						
S	M	T	W	T	F	S
	1	2	3	4	5	6
7	8	9	10	11	12	13
14	15	16	17	18	19	20
21	22	23	24	25	26	27
28						

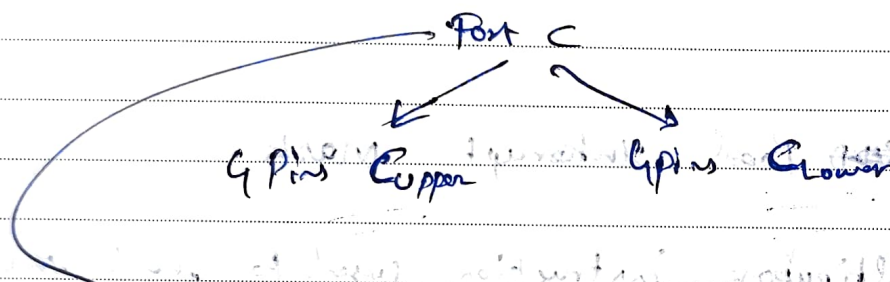
33-332 / Week 6

8255

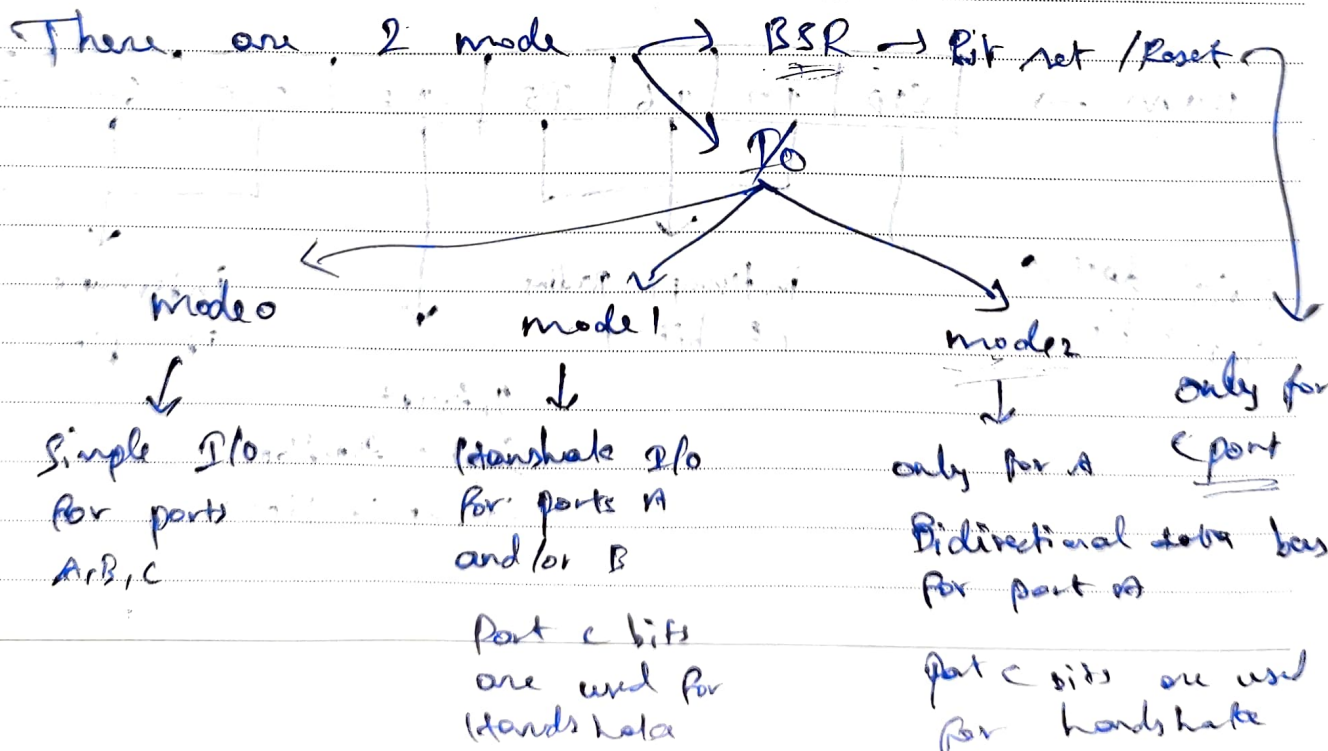
It has 24 I/O pins

which is segregated as Port A - 8 pins

Port B - 8 pins



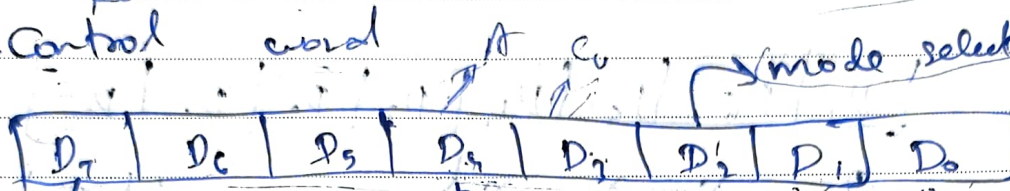
And also access individual pins in port C



its does accessible to Read
 ← Control

10011000
 9 82

1000 0011



0/1
 BSR
 I/O
 00 - Mode 0
 01 - 1
 1x - 2

10000011

8 3H

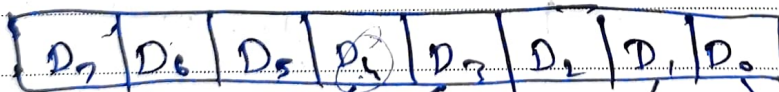
MUL A, 85H

OUT 83H

pin lines for control 8255

\overline{WR} , \overline{RD} , \overline{CS} , RESET → cleans Control register and sets all ports in the input mode

A_1, A_0 Selected
 0 0 → Port A
 0 1 → Port B
 1 0 → Port C
 1 1 → Control register



Port A

Port C upper

Port B

Port C Lower

In = 1

In = 1

In = 1

In = 1

Out = 0

Out = 0

Out = 0

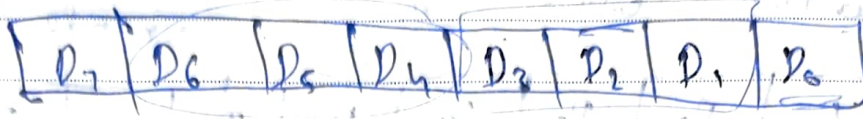
Out = 0

4 February

Thursday

35-330 / Week 6

BSR



BSR mode

000 - Bit 0

001 - Bit 1

111 - Bit 7