

Specification and Analysis of Systems with TLA+ An Brief Introduction

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TLA+ - Introduction

A system is specified as:

- 1 A set of initial states
- 2 A transition function $T(State, State')$ that represents possible transitions.

We also include a set of properties that we indicate that any behavior (valid execution sequence) of the system should satisfy, using invariants and other temporal logic assertions.



TLA+ - Introduction

Two ways to specify the system:

- 1 The mathematical language of TLA+ (based on mathematical expressions that use basically set theory to express states and the relation between states)
- 2 A more high-level language: PlusCal (but it does not allow to specify as much systems as the TLA+ language) The TLA+ tools make the automated translation of a PlusCal algorithm to an equivalent TLA+ version.

When low-level detail of the system is needed (e.g. for very concrete hardware systems), TLA+ language may be the only choice



TLA+ - Example 1 with PlusCal

MODULE *Add2FG*

```
***** --algorithm Add2FG{
```

```
  variable  $x = 0$ ;
```

```
  process (  $A = 1$  )
```

```
    variable  $ta = -1$ ; {
```

```
      a1:  $ta := x$ ;
```

```
      a2:  $x := ta + 1$ ; }
```

```
  process (  $B = 2$  )
```

```
    variable  $tb = -1$ ; {
```

```
      b1:  $tb := x$ ;
```

```
      b2:  $x := tb + 1$ ; }
```

```
*****
```



TLA+ - Example 1 with PlusCal

```

BEGIN TRANSLATION (chksum(pcal) = "1b626abd"  $\wedge$  chksum(tla) = "5ff5e3e0")
VARIABLES x, pc, ta, tb

vars  $\triangleq$   $\langle x, pc, ta, tb \rangle$ 

ProcSet  $\triangleq$   $\{1\} \cup \{2\}$ 

Init  $\triangleq$    Global variables
             $\wedge x = 0$ 
            Process A
             $\wedge ta = -1$ 
            Process B
             $\wedge tb = -1$ 
             $\wedge pc = [self \in ProcSet \mapsto \text{CASE } self = 1 \rightarrow \text{"a1"}
                    \square \quad self = 2 \rightarrow \text{"b1"}]$ 

```



TLA+ - Example 1 with PlusCal

$$\begin{aligned} a1 &\triangleq \wedge pc[1] = \text{"a1"} \\ &\quad \wedge ta' = x \\ &\quad \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"a2"}] \\ &\quad \wedge \text{UNCHANGED } \langle x, tb \rangle \end{aligned}$$

$$\begin{aligned} a2 &\triangleq \wedge pc[1] = \text{"a2"} \\ &\quad \wedge x' = ta + 1 \\ &\quad \wedge pc' = [pc \text{ EXCEPT } ![1] = \text{"Done"}] \\ &\quad \wedge \text{UNCHANGED } \langle ta, tb \rangle \end{aligned}$$

$$A \triangleq a1 \vee a2$$

Two only possible steps for process A: a1 and a2



TLA+ - Example 1 with PlusCal

$$\begin{aligned} b1 &\triangleq \wedge pc[2] = \text{"b1"} \\ &\quad \wedge tb' = x \\ &\quad \wedge pc' = [pc \text{ EXCEPT } ![2] = \text{"b2"}] \\ &\quad \wedge \text{UNCHANGED } \langle x, ta \rangle \end{aligned}$$

$$\begin{aligned} b2 &\triangleq \wedge pc[2] = \text{"b2"} \\ &\quad \wedge x' = tb + 1 \\ &\quad \wedge pc' = [pc \text{ EXCEPT } ![2] = \text{"Done"}] \\ &\quad \wedge \text{UNCHANGED } \langle ta, tb \rangle \end{aligned}$$

$$B \triangleq b1 \vee b2$$

Two only possible steps for process B: b1 and b2



TLA+ - Example 1 with PlusCal

Allow infinite stuttering to prevent deadlock on termination.

$$\textit{Terminating} \triangleq \bigwedge \forall \textit{self} \in \textit{ProcSet} : \textit{pc}[\textit{self}] = \text{“Done”} \\ \bigwedge \text{UNCHANGED } \textit{vars}$$

$$\textit{Next} \triangleq \textit{A} \vee \textit{B} \\ \vee \textit{Terminating}$$

$$\textit{Spec} \triangleq \textit{Init} \wedge \Box[\textit{Next}]_{\textit{vars}}$$

$$\textit{Termination} \triangleq \Diamond(\forall \textit{self} \in \textit{ProcSet} : \textit{pc}[\textit{self}] = \text{“Done”})$$

END TRANSLATION



TLA+ - Example 1 with PlusCal

The TLA+ equivalent translation of our algorithm describes the sets of possible behaviors (valid executions) with the specification:

$$Init \wedge \Box [Next]_{vars}$$

with two things:

- An initial condition that specifies the possible starting states (predicate *Init*).
- A next-state relation that specifies the possible steps (predicate *Next* that specifies the relation between pairs of successive states). The symbol \Box is a **temporal logic operator** that means for every state of the behavior



TLA+ - Example 1 with PlusCal

In the transition relation:

$$Next \triangleq A \vee B \vee Terminating$$

where:

$$Terminating \triangleq \bigwedge \forall self \in ProcSet : pc[self] = Done \\ \bigwedge UNCHANGED\ vars$$

we indicate that the concurrent system is in either a A (a1 or a2) or B (b1 or b2) transition or both processes are in the Done state so the variables at the next state have the same value that in the previous state



TLA+ - Example 1 with PlusCal

Then, if we want to check whether our system satisfies the following temporal property (invariant for any state of any valid execution):

$$Spec \Rightarrow \Box((pc[1] = "Done") \wedge (pc[2] = "Done") \Rightarrow (x = 2))$$

we can use the Model Checker TLC provided with the TLA+ tools.



The TLC Model Checker for TLA+ (Simplified)

Initially: $seen = toexpand = [s \mid s \text{ satisfies the Init predicate}]$

while there are new states in toexpand:

- ➊ Remove state s from the front of toexpand
- ➋ For any state t that satisfies $Next(s, t)$ and not on seen queue :
 - ➊ Add t to the queue seen with a pointer to s
 - ➋ If t satisfies all the invariant properties (if any) then add t to the end of the queue toexpand.
 - ➌ Else, report "found behaviour ending in t that violates some invariant"



TLA+ - Example 2 directly with TLA+

MODULE *concurrent_buffer*

***** Example from: <https://levelup.gitconnected.com/debugging-concurrent-systems-with-a-model-checker-c7eee210d86f>

EXTENDS *Naturals, Sequences*

CONSTANTS *Producers,*
Consumers,
BufCapacity,
Data

ASSUME \wedge *Producers* $\neq \{\}$
 \wedge *Consumers* $\neq \{\}$
 \wedge *Producers* \cap *Consumers* = $\{\}$
 \wedge *BufCapacity* > 0
 \wedge *Data* $\neq \{\}$



TLA+ - Example 2 directly with TLA+

VARIABLES *buffer*,
waitSet

Participants \triangleq *Producers* \cup *Consumers*

RunningThreads \triangleq *Participants* \setminus *waitSet*

TypeInv \triangleq \wedge *buffer* \in Seq(*Data*)
 \wedge Len(*buffer*) \in 0 .. *BufCapacity*
 \wedge *waitSet* \subseteq *Participants*

Notify \triangleq IF *waitSet* \neq {}
THEN $\exists x \in$ *waitSet* : *waitSet'* = *waitSet* \setminus {*x*}
ELSE UNCHANGED *waitSet*

NotifyAll \triangleq *waitSet'* = {}

Wait(*t*) \triangleq *waitSet'* = *waitSet* \cup {*t*}



TLA+ - Example 2 directly with TLA+

$$Init \triangleq buffer = \langle \rangle \wedge waitSet = \{ \}$$
$$Put(t, m) \triangleq \text{IF } Len(buffer) < BufCapacity \\ \text{THEN } \wedge buffer' = Append(buffer, m) \\ \wedge Notify \\ \text{ELSE } \wedge Wait(t) \\ \wedge UNCHANGED buffer$$
$$Get(t) \triangleq \text{IF } Len(buffer) > 0 \\ \text{THEN } \wedge buffer' = Tail(buffer) \\ \wedge Notify \\ \text{ELSE } \wedge Wait(t) \\ \wedge UNCHANGED buffer$$
$$Next \triangleq \exists t \in RunningThreads : \vee t \in Producers \\ \wedge \exists m \in Data : Put(t, m) \\ \vee t \in Consumers \wedge Get(t)$$


TLA+ - Example 2 directly with TLA+

$$Prog \triangleq Init \wedge \Box [Next]_{\langle buffer, waitSet \rangle}$$
$$NoDeadlock \triangleq \Box (RunningThreads \neq \{\})$$

THEOREM $Prog \Rightarrow \Box TypeInv \wedge NoDeadlock$



TLA+ - Example 2 directly with TLA+

If we run TLC with:

- Producers = $\{ "p1", "p2", "p3" \}$
- Consumers = $\{ "c1", "c2" \}$
- BufCapacity = 2
- Data = $\{ "m1" \}$

and specifying the predicate NoDeadlock as an invariant we obtain an error trace with **24 states** where the final state violates the invariant:

- buffer = $\langle "m1", "m1" \rangle$
- waitSet = $\{ "p1", "p2", "p3", "c1", "c2" \}$

This happens only due to a very specific ordering of the processes and too many producers !

