Fast Multistep Finite Control Set Model Predictive Control for Transient Operation of Power Converters

Roky Baidya*, Ricardo P. Aguilera[†], Pablo Acuna*, Ramon Delgado[‡], Tobias Geyer[§], Daniel Quevedo[¶], Toit Mouton^{||}

*Australian Energy Research Institute, University of New South Wales, Australia,

† The School of Electrical, Mechanical and Mechatronic Systems, The University of Technology Sydney (UTS), Australia,

‡School of Electrical Engineering, The University of Newcastle, Australia,

§ABB Corporate Research, Switzerland,

¶Department of Electrical Engineering (EIM-E), Paderborn University, Germany.

Department of Electrical and Electronic Engineering, University of Stellenbosch, South Africa.

Email: raguilera@ieee.org

Abstract-Recently, an efficient optimization strategy based on the sphere decoding algorithm (SDA) has been proposed to solve the optimal control problem underlying direct model predictive control (MPC) formulations with long horizons. However, as will be elucidated in this work, this optimization algorithm presents some limitations during transient operation of power converters, which increase the execution time required to obtain the optimal solution. To overcome this issue, the present work presents an improved version of the SDA for direct MPC that is not affected by transient operations of the power converter. The key novelty of the proposal is to reduce the execution time of the SDA when the system is in a transient by projecting the unconstrained optimal solution onto the envelope of the original finite control set. As evidenced by the simulation results, the proposed SDA is able to quickly compute the optimal solution for the long-horizon direct MPC during both steady-state and transient operation of the power converter.

Index Terms- Finite control set, predictive control, multilevel converters, optimization problem, sphere decoding, convex hull.

I. Introduction

Multilevel converters (MCs) are the preferred technology for medium/high-voltage and high power applications [1], [2]. Several MC topologies have been proposed in the literature, which offer different features mainly related with high-quality voltage and currents waveforms at medium/high-voltage using low voltage rated semiconductor switches. Traditional modulation techniques or special methods can be used to provide a multilevel output voltage with improved power quality (i.e., low harmonic profile, low dv/dt ratings, etc). Thus, MCs are of special importance in high power applications, such as high-power drives [3], active power filters [4], and renewable energy applications [5], [6], among others.

Model predictive control (MPC) for MCs has become an interesting option due to its attractive features e.g., fast dynamic response, easy inclusion of nonlinearities and constraints, etc [7]. Several works can be found in the literature utilizing the MPC successfully for MCs, e.g., [8]–[13]. Among the

predictive control families, the so-called Finite-Control-Set MPC (FCS-MPC) or direct MPC is the most popular one for power converters [14], [15]. FCS-MPC directly considers the switching states or voltage levels as control inputs into the optimization problem. In the standard approach, the optimal control input is found by evaluating a finite set of input combinations in a cost function which has minimum cost value. Therefore, a modulation stage is not required.

In recent works, the importance of using long horizon direct MPC schemes has been demonstrated [16], [17]. The advantage of moving to longer horizons is to obtain an improved steady-state performance in terms of harmonic distortion and switch commutations. However, the required computational effort is challenging since the number of input combinations increases exponentially with the horizon length. This issue becomes a particular concern when implementing the controllers standard control platform such as FPGAs, DSPs, etc [18], [19]. Recent research outcomes show that the computational burden can be reduced significantly by introducing an efficient optimization strategy named Sphere Decoding Algorithm (SDA) [20]-[22]. Nevertheless, as will be elucidated in this work, standard SDA presents some limitations during transient operations of power converters, which increase the execution time required to obtain the optimal solution. This compromises the implementation of SDA to solve the associated optimal problem of FCS-MPC governing power converters.

The work at hand presents an improved version of the SDA, which tackles the aforementioned issues. Here, the controller is directly formulated in the original *abc*-framework. The key novelty of the proposal is to quickening the SDA, when the system is in transient by projecting the unconstrained optimal solution on the envelope of the original finite control set. As evidenced by the simulation results, the proposed SDA is able to quickly compute the optimal solution for the long-horizon direct MPC during both steady-state and transient operations of the power converter.

Peer-reviewed author's copy of:

R. Baidya, R.P. Aguilera, P. Acuna, R.A. Delgado, T. Geyer, D. Quevedo, and T. Mouton. Fast multistep finite control set model predictive control for transient operation of power converters. In 42nd Annual Conference of IEEE Industrial Electronics Society (IECON 2016), Florence, Italy, 2016.

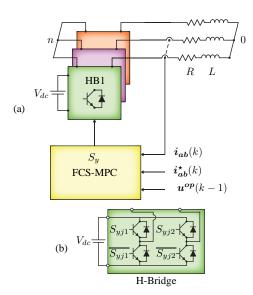


Fig. 1. Schematic of a three-phase three-level HB inverter: (a) block diagram of the FCS-MPC; (b) single H-Bridge converter

II. FCS-MPC FORMULATION

To employ the new proposal of interest, this paper considers the use of FCS-MPC to govern a three-level three-phase H-Bridge (HB) inverter shown in Fig. 1. Here, each HB cell is electrically fed with an isolated dc voltage source. The inverter supply power to a passive RL load. This section introduces the system model and the optimization problem.

A. Converter Model

From the basic circuit analysis, we can express the continuous-time dynamic model of the inverter as:

$$\frac{di_y(t)}{dt} = -\frac{R}{L}i_y(t) + \frac{1}{L}(v_{yn}(t) - v_{0n}(t)),\tag{1}$$

for all $y \in \{a,b,c\}$ and $v_{yn}(t)$ is the individual cell output voltage. The common mode voltage (CMV, v_{0n}) is given by:

$$v_{0n}(t) = \frac{1}{3}(v_a(t) + v_b(t) + v_c(t)). \tag{2}$$

Usually in power electronics, the power switches are considered as control input [23]. This may lead to a large number of input combinations, which may difficult unnecessarily the implementation of FCS-MPC. To account for this, this work considers the phase voltage levels, $v_{\ell y}$, as control input [22]. Thus, for a generic η -cells converter, the output voltage becomes:

$$v_{yn}(t) = \frac{V_{dc}}{n} v_{\ell y}(t), \tag{3}$$

where η is the number of cell per phase ($\eta=1$ for one-cell) in the converter and,

$$v_{\ell y}(t) \in \mathbb{V} \stackrel{\triangle}{=} \{-\eta, -\eta + 1, \dots, 0, \dots, \eta - 1, \eta\}. \tag{4}$$

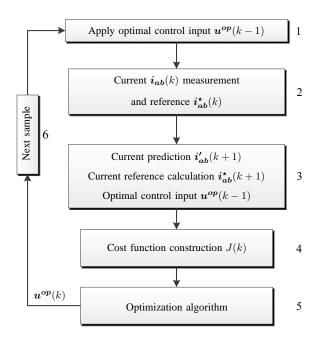


Fig. 2. Flow diagram of the proposed FCS-MPC strategy for horizon one

Here, we implement the FCS-MPC for a HB inverter in the original abc-framework. The system states and control inputs are chosen as:

$$\mathbf{i}_{ab}(k) = \begin{bmatrix} i_a(k) \\ i_b(k) \end{bmatrix} \in \mathbb{R}^2, \quad \mathbf{u}(k) = \begin{bmatrix} v_{\ell a}(k) \\ v_{\ell b}(k) \\ v_{\ell c}(k) \end{bmatrix} \in \mathbb{U}, \quad (5)$$

where $i_c(k) = -(i_a(k) + i_b(k))$ and the input, u(k), belongs to the finite control set $\mathbb{U} \triangleq \mathbb{V}^3$. Now, the discrete-time dynamic model can be obtained by applying the forward Euler discretization to (1) with a sampling period of T_s , i.e.,:

$$i_{ab}(k+1) = Ai_{ab}(k) + Bu(k), \tag{6}$$

where

$$\mathbf{A} = \begin{bmatrix} 1 - \frac{RT_s}{L} & 0\\ 0 & 1 - \frac{RT_s}{L} \end{bmatrix}, \mathbf{B} = \frac{V_{dc}T_s}{3L} \begin{bmatrix} 2 & -1 & -1\\ -1 & 2 & -1 \end{bmatrix}.$$
 (7)

B. Optimal Control Problem

In this work, our control target focuses on the reference current tracking and the switching effort at each time step-k. The first target is to track the current reference, i_{ab}^{\star} , for the HB output current, i.e.,:

$$i_{ab}^{\star}(t) = \begin{bmatrix} I^{\star} \sin(\omega t) \\ I^{\star} \sin(\omega t - 2\pi/3) \end{bmatrix},$$
 (8)

where, I^* is the amplitude of the current references.

The second target is the switching effort $\Delta u'(k) = u'(k) - u^{op}(k-1)$. Here, $u^{op}(k-1)$ is the optimal solution found at the last step (k-1). Thus, the cost function penalizes the current tracking error and the switching effort as shown below for the horizon length N=1,

$$J(k) = \|\mathbf{i}_{ab}'(k+1) - \mathbf{i}_{ab}^{\star}(k+1)\|_{2}^{2} + \lambda \|\Delta \mathbf{u}'(k)\|_{2}^{2}.$$
(9)

Here, $i'_{ab}(k+1)$ stands for the tentative HB current predictions that is generated by the tentative control input u'(k). In (9), $\|\cdot\|_2^2$ represents the quadratic eucledian norm, i.e., $\|a-a^\star\|_2^2=(a_1-a_1^\star)^2+\ldots+(a_p-a_p^\star)^2$, for a pair of vectors $a,a^\star\in\mathbb{R}^p$. Additionally, the weighting factor λ penalizes the impact on commutations to achieve the desired switching frequency. In recent research work [20], this cost function (9) has been proposed for three-level neutral point clamped voltage source inverter in the the $\alpha\beta$ -framework.

The optimal control input $\boldsymbol{u^{op}}(k)$, which is to be applied by the converter, is the one that minimizes the cost function. Thus, the optimization problem underlying in FCS-MPC strategy can be written as:

$$\boldsymbol{u^{op}}(k) = \arg\{\min_{\boldsymbol{u} \in \mathbb{U}} J(k)\}.$$
 (10)

Normally, in power electronics, one step voltage level changes are recommended to avoid high dv/dt rating in the system. Thus, the constraint on the optimal control input to be obtained $\boldsymbol{u^{op}}(k)$ can be defined as follows:

$$\|\Delta \boldsymbol{u'}(k)\|_{\infty} \le 1. \tag{11}$$

A flow diagram of the FCS-MPC strategy for N=1 is shown in Fig. 2. This strategy considers the aforementioned cost function (9) by performing in general curent prediction $i'_{ab}(k+1)$, current reference calculation $i^{\star}_{ab}(k+1)$ and the optimal control input $u^{op}(k-1)$ found at previous time step-(k-1). The optimal input $u^{op}(k)$ is obtained by solving (10).

Now, the cost function and the optimization problem for arbitrary horizon length N is:

$$J_N = \sum_{l=k}^{k+N-1} \| \mathbf{i}'_{ab}(l+1) - \mathbf{i}^{\star}_{ab}(l+1) \|_2^2 + \lambda \| \Delta \mathbf{u}'(l) \|_2^2, \quad (12)$$

$$\boldsymbol{U^{opt}}(k) = \arg\{\min_{\boldsymbol{U} \in \mathbb{U}} J_N\}. \tag{13}$$

where $\boldsymbol{U}(k) = [\boldsymbol{u}^T(k) \dots \boldsymbol{u}^T(k+N-1)]^T$ is the input sequences which belongs to the finite input sequence $\mathbb{U} \triangleq \mathbb{V}^{3N}$, and $\boldsymbol{U^{opt}}$ is the optimal solution. Interested readers are referred to [23] for a detailed analysis of long horizon FCS-MPC.

III. STANDARD SDA

This work uses the well known SDA to find the optimal control input, $\boldsymbol{u^{op}}(k)$, to be applied to the inverter. The importance of SDA for long prediction horizons in power electronics has been shown in the recent research works [20]–[22]. Here, we briefly discuss the working principle of SDA for the horizon-one case, which helps the reader to understand our proposal.

SDA solves the optimization problem of interest (10) in an equivalent triangular-least squares form. To do this, we rewrite (10) as the following integer quadratic programming problem:

$$\boldsymbol{u^{op}}(k) = \arg\min_{\boldsymbol{u} \in \mathbb{I}} \{ \boldsymbol{u^T}(k) \boldsymbol{W} \boldsymbol{u}(k) + 2\boldsymbol{u^T}(k) \boldsymbol{F}(k) \}.$$
 (14)

where

$$F(k) = B^{T} A i_{ab}(k) - B^{T} i_{ab}^{\star}(k) - \lambda u^{op}(k-1), \quad (15)$$

and $W = B^T B + \lambda I_{3\times 3}$ is a symmetric and positive definite matrix for $\lambda \geq 0$. Thus, we can apply the, so-called, Cholesky decomposition [20], [24] on W to find a unique invertible and triangular matrix $H \in \mathbb{R}^{3\times 3}$, which satisfies:

$$\boldsymbol{W} = \boldsymbol{H}^T \boldsymbol{H}. \tag{16}$$

When there are no constraints on the input, i.e., $u(k) \in \mathbb{R}^3$, the unconstrained optimal solution $u_{uc}^{op}(k)$ can be expressed in the following manner:

$$u_{uc}^{op}(k) = \arg\min_{u \in \mathbb{R}^3} \{ u^T(k) W u(k) + 2u^T(k) F(k) \}$$

$$= -W^{-1} F(k).$$
(17)

Now, using (16) and (17), we can rewrite the integer quadratic problem (14) as:

$$\boldsymbol{u^{op}}(k) = \arg\{\min_{\boldsymbol{u} \in \mathbb{U}} \|\boldsymbol{H}\boldsymbol{u}(k) - \boldsymbol{y_c}(k)\|_2^2\}, \quad (18)$$

where $y_c(k) = Hu_{uc}^{op}(k)$.

Notice that (18) is a least-squares quantization problem in triangular form, which is the requirement to implement SDA. It is important to highlight that obtaining the optimal solution $\boldsymbol{u^{op}}(k)$ by solving the minimization in (10) is equivalent to the quantization as per (18).

The SDA solves the above quantization problem (18) by searching the tentative solutions inside the sphere. This sphere is defined with the center y_c and an initial radius ρ , as proposed in [20]. To concise the paper we discard the initial radius selection method. Interested readers are referred to [20] for detail analysis.

A flow diagram of the SDA for three-phase three-level HB inverter is shown in Fig. 4. Taking three precalculated parameters $(\boldsymbol{H}, \boldsymbol{u_{uc}^{op}})$ and ρ^2 , the algorithm starts searching the prospective solution in a repetitive manner, which satisfy the following condition:

$$\|\boldsymbol{H}\boldsymbol{u}(k) - \boldsymbol{y}_{c}(k)\|_{2}^{2} \leqslant \rho(k).$$
 (19)

Here, H is the lower triangular matrix that leads the algorithm to a sequential element-wise computation. Now to see the computational advantage, (19) can be extended to:

$$(h_{11}u_1 - y_{c(1)})^2 + (h_{21}u_1 + h_{22}u_2 - y_{c(2)})^2 + (h_{31}u_1 + h_{32}u_2 + h_{33}u_3 - y_{c(3)})^2 \le \rho^2(k),$$
 (20)

where h_{ij} is the $(i,j)^{th}$ -element of \boldsymbol{H} , u_i stands for the i^{th} -element of \boldsymbol{u} and $y_{c(i)}$ refers to the i^{th} -element of $\boldsymbol{y_c}$. Based on (20), it begins computing the squared distance for the 1st element (u_1) up to the last element (u_3) of $\boldsymbol{u}(k)$. Calculations for each element of $\boldsymbol{u}(k)$ initiate from $-\eta$ and ends at η . If the computed distance d' for u_i is larger than ρ^2 , then the algorithm discards the rest of the computations for the remaining elements. However, if d' for any element is smaller than ρ^2 , then the SDA continues with the next elements. In this way, when d' for the final element (u_3) is equal or smaller than ρ^2 , then the corresponding $\boldsymbol{u}(k)$ is a prospective solution. Whenever we found a better prospective solution, the algorithm updates \boldsymbol{u}^{op} and ρ^2 with d' as well.

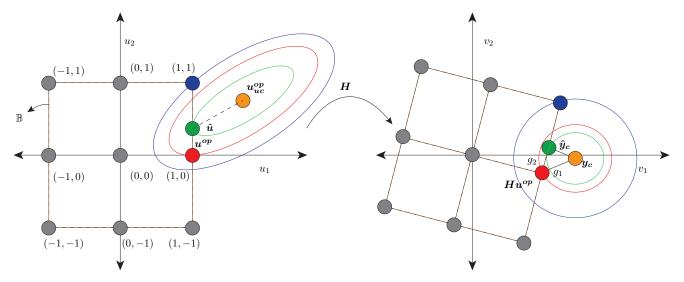


Fig. 3. The principle of the FCS-MPC with the modified sphere decoding algorithm in the original space- v_1v_2 and in the transformed space- v_1v_2 .

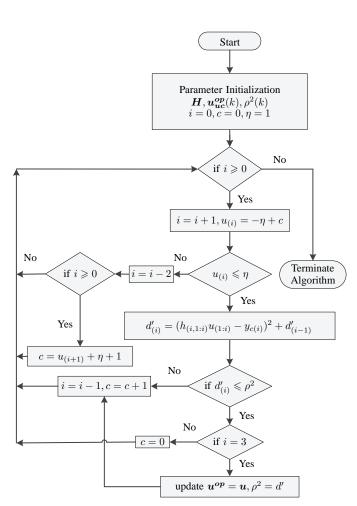


Fig. 4. Flow diagram of the sphere decoding algorithm for horizon one

Hence, the radius of the sphere is reduced if possible [20]; i.e., the search space is reduced. This approach can reduce the computational effort significantly.

With effective selection of the initial radius, the SDA can be extended to long horizons and the larger number of phase voltage level typically interested to multilevel converters.

IV. ENHANCING SDA FOR TRANSIENT OPERATIONS

In this section, we illustrate the proposed modification to the SDA to achieve a fast optimization during transients of the power converter. This adaptation is based on the standard quadratic programming imposed on the integer quadratic problem (14).

To ease the understanding, we present the idea graphically in a two-dimensional space for the 3-level HB converter (i.e., $u_1, u_2 \in \{-1, 0, 1\}$) and horizon length N = 1, see Fig. 3. Thus, the finite set \mathbb{U} has $3^2 = 9$ combinations for the control input u. At first, we define the convex hull (i.e., the envelope) of the lattice formed by the finite control set \mathbb{U} .

$$\mathbb{B} = ConvexHull(\mathbb{U}), \mathbb{U} \subset \mathbb{B}. \tag{21}$$

In general, during steady-state operation, the unconstrained optimal solution, u_{uc}^{op} lies inside the boundary \mathbb{B} (i.e., $u_{uc}^{op} \in \mathbb{B}$). However, during transients (reference changes), u_{uc}^{op} can be located far away from the lattice boundary. This results in a large initial sphere and, thereby, high computational time to find the optimal solution.

In dealing with this situation, whenever the unconstrained optimal solution, u_{uc}^{op} , lies outside of \mathbb{B} (i.e., $u_{uc}^{op} \notin \mathbb{B}$), we propose to project u_{uc}^{op} onto \mathbb{B} by solving the following quadratic problem:

$$\hat{\boldsymbol{u}}(k) = \arg\min_{\boldsymbol{u} \in \mathbb{B}} \{ u^T W u + 2u^T \boldsymbol{F}(k) \}.$$
 (22)

where

$$\hat{\boldsymbol{u}}(k) \in \mathbb{R}^{3N} \in \mathbb{B}. \tag{23}$$

We replicate this case in the first plot of Fig. 3, where the unconstrained optimal solution u_{uc}^{op} (yellow point), is outside the boundary $\mathbb B$ and the optimal solution u^{op} (red point) to be found is on $\mathbb B$. As per definition in [23], the cost function J(k) forms different ellipses (all points that lead to the same cost) with their centers at u_{uc}^{op} . The directions of an ellipse depend on the eigenvalues of W. It is clear that ellipses closer to the center produce smaller cost values. Therefore, for the example in Fig. 3, the red point produces a lower cost than the blue one. Now, according to the quadratic programming problem (22), we obtain \hat{u} (green point) by projecting u_{uc}^{op} onto the boundary. In this work, we propose to consider this projection as the new center in the original space u_1u_2 to run the SDA.

Therefore, instead of solving (18), during the transient we propose to solve the following optimization problem:

$$u^{op}(k) = \arg\{\min_{u \in \mathbb{U}} \|Hu(k) - \hat{y}_c(k)\|_2^2\}.$$
 (24)

where

$$\hat{\mathbf{y}}_{\mathbf{c}}(\mathbf{k}) = \mathbf{H}\hat{\mathbf{u}}(k). \tag{25}$$

It is important to emphasize that if the optimal solution u^{op} is one of the outer points of the original lattice formed by the finite control set, then solving (24) is, thus, equivalent to solve (18), whenever the unconstrained optimal solution is outside from the lattice boundary \mathbb{B} .

With the transformation matrix H, the original space u_1u_2 is transformed into the new space v_1v_2 . Here, the optimal solution is now the one, which is nearest neighbour to the center. This is because, the cost function forms now different circles (spheres for more than 2-dimension) with the defined center in the quantization problem. In essence, (22) brings the center of the sphere, $y_c(k)$ in (18), close to the lattice in an optimal manner by obtaining $\hat{y}_c(k)$. Here, y_c (yellow point) and \hat{y}_c (green point) are the centers for the standard and the modified SDA respectively. It is clear from the figure that, using both y_c and \hat{y}_c give us the same optimal solution u^{op} , since from both, Hu^{op} (red point) is the closest solution in the set. However, the target is closer to \hat{y}_c than y_c , as $y_c < y_1$. Here, y_1 and y_2 are the distance from the target (Hu^{op}) to the centers y_c and \hat{y}_c respectively.

Consequently, whenever $y_{\boldsymbol{c}}(k)$ is outside the ConvexHull, a new initial sphere with radius $\|\boldsymbol{H}\boldsymbol{u}(k-1)-\hat{\boldsymbol{y}}_{\boldsymbol{c}}(k)\|_2^2$ is setup, which is smaller than $\|\boldsymbol{H}\boldsymbol{u}(k-1)-\boldsymbol{y}_{\boldsymbol{c}}(k)\|_2^2$. This reduces the computational effort required by the SDA to obtain the optimal solution. Therefore, this modification helps to speedup the SDA during transients.

V. PERFORMANCE EVALUATION: SIMULATION RESULTS

The simulation results of the FCS-MPC governing a three-level converter using the improved SDA are carried out in this section. The system model uses the main parameters as shown in Table. I. The effectiveness of the proposal is verified by comparing it with the standard SDA used in the recent works [20], [22].

The results have been obtained in the, MATLAB-Simulink platform incorporated with PLECS. In Matlab, we use the

Table I System Parameters

Variable	Description	Values
V_{dc}	dc voltage supply per HB	180 V
I^{\star}	Reference current amplitude	4 A
f	Fundamental frequency of the load current	50 Hz
R	Load resistor	47 Ω
L	Load inductor	15 mH
f_s	Sampling frequency	10 kHz
λ	Weighting factor	0.1
N	Horizon length	5

following **quadprog** function, to find $\hat{\boldsymbol{u}}(k)$ in (22), with the constraints $lb = -\eta*\mathbf{ones}(p,1)$ and $ub = \eta*\mathbf{ones}(p,1)$. $\hat{\boldsymbol{u}}(k) = \mathbf{quadprog}(W, \boldsymbol{F}(k), [], [], [], [], lb, ub)$. Here, lb and ub are used to define the lower and upper boundary of the ConvexHull \mathbb{B} . Note that this is a simple quadratic programing problem since only lower and upper bounds are required as constraints.

Since, the steady-state performances are not the concern of this work, we have shown the results only for the transient-state. The simulations are performed for 30ms and a step change (-4A to 4A) in the current reference is enforced at 20ms.

Figure (5) represents the results when the FCS-MPC uses the standard SDA. One can clearly see from Fig. (5e) that, during transients the algorithm requires extensively high computational time ($T_c \approx 3.5s$) compared with the steady-state to obtain the optimal solution. The average computational time, T_c for steady-state is 1.2ms.

On the contrary, the system performances for the proposed SDA used in FCS-MPC are depicted in Figure. 6. Here, the computational time T_c required in during transients is similar to the one needed in the steady-state; see Fig. (6e). The required computational time, T_c , for the proposed SDA during the whole simulation period is 1.2ms.

Comparing the resulting inverter voltages and currents obtained by both approaches, it can be clearly observed that the same optimal solution is obtained by both approaches. Nevertheless, the proposed SDA is able to find the optimal solutions efficiently in both steady-state and transients operation condition of the power converter.

VI. CONCLUSIONS

This work proposed an improved SDA to solve the optimal control problem associated with FCS-MPC. The proposed approach is able to achieve the equivalent performance of standard SDA, in terms of the optimal solution. Consequently, the predictive controller ensures to obtain the optimal solution in transient-state, while maintaining the same computational effort like steady-state.

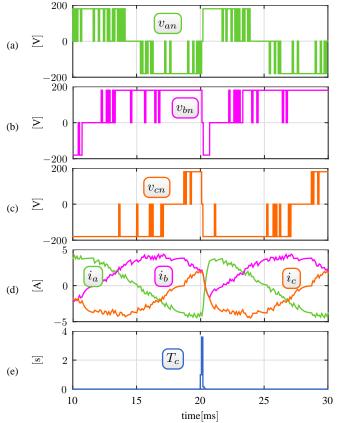


Fig. 5. The performance of the FCS-MPC strategy with the standard Sphere decoding algorithm (SDA) for a step change in the current references. Inverter voltages for, (a) Phase a, (b) Phase b, (c) Phase c. (d) Load currents, and (e) computational time.

The key novelty of the proposal lies in the way the center of the SDA is modified during transients. To do this, the unconstrained optimal solution is projected over the envelope of the original finite control set. Thus, a new center for the SDA is introduced. This speedup the optimization process since a smaller initial radius is obtained. As evidenced by the simulation results, the proposed SDA is able to quickly compute the optimal solution for the long-horizon direct MPC during both steady-state and transient operations of the power converter. Motivated by the present outcomes, future work will be focused on the experimental validation of this novelty and further contributions to make the SDA even faster.

REFERENCES

- [1] S. Kouro, M. Malinowski, K. Gopakumar, J. Pou, L. Franquelo, B. Wu, J. Rodríguez, M. A. Pérez, and J. Leon, "Recent Advances and Industrial Applications of Multilevel Converters," *IEEE Transactions on Industrial Electronics*, vol. 57, no. 8, pp. 2553–2580, 2010.
- [2] J. Rodriguez, L. Franquelo, S. Kouro, J. Leon, R. Portillo, M. Prats, and M. Perez, "Multilevel converters: An enabling technology for high-power applications," *Proceedings of the IEEE*, vol. 97, no. 11, pp. 1786–1817, Nov 2009.
- [3] E. Al-Nabi, B. Wu, N. R. Zargari, and V. Sood, "Input power factor compensation for high-power csc fed pmsm drive using d -axis stator current control," *IEEE Transactions on Industrial Electronics*, vol. 59, no. 2, pp. 752–761, Feb 2012.
- [4] P. Acua, L. Morn, M. Rivera, R. Aguilera, R. Burgos, and V. G. Agelidis, "A single-objective predictive control method for a multivariable single-phase three-level npc converter-based active power filter," *IEEE*

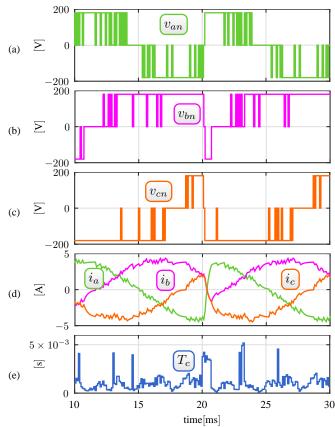


Fig. 6. The performance of the FCS-MPC strategy with the proposed Sphere decoding algorithm (SDA) for a step change in the current references. Inverter voltages for, (a) Phase a, (b) Phase b, (c) Phase c. (d) Load currents, and (e) computational time.

- Transactions on Industrial Electronics, vol. 62, no. 7, pp. 4598–4607, July 2015.
- [5] M. R. Islam, Y. Guo, and J. Zhu, "A high-frequency link multilevel cascaded medium-voltage converter for direct grid integration of renewable energy systems," *IEEE Transactions on Power Electronics*, vol. 29, no. 8, pp. 4167–4182, Aug 2014.
- [6] L. Popova, J. Pyrhonen, K. L. Ma, and F. Blaabjerg, "Device loading of modular multilevel converter mmc in wind power application," in Power Electronics Conference (IPEC-Hiroshima 2014 - ECCE-ASIA), 2014 International, May 2014, pp. 548–554.
- [7] S. Vazquez, J. Leon, L. Franquelo, J. Rodriguez, H. Young, A. Marquez, and P. Zanchetta, "Model predictive control: A review of its applications in power electronics," *IEEE Ind. Electron. Mag.*, vol. 8, no. 1, pp. 16–31, March 2014.
- [8] P. Lezana, R. Aguilera, and D. Quevedo, "Model predictive control of an asymmetric flying capacitor converter," *Industrial Electronics, IEEE Transactions on*, vol. 56, no. 6, pp. 1839–1846, June 2009.
- [9] T. Geyer, G. Papafotiou, and M. Morari, "Model Predictive Direct Torque Control—Part I: Concept, Algorithm, and Analysis," *IEEE Transactions on Industrial Electronics*, vol. 56, no. 6, pp. 1894–1905, 2009
- [10] G. Papafotiou, J. Kley, K. G. Papadopoulos, P. Bohren, and M. Morari, "Model predictive direct torque control—Part II: Implementation and experimental evaluation," *IEEE Transactions on Industrial Electronics*, vol. 56, no. 6, pp. 1906–1915, Jun. 2009.
- [11] N. Oikonomou, C. Gutscher, P. Karamanakos, F. D. Kieferndorf, and T. Geyer, "Model Predictive Pulse Pattern Control for the Five-Level Active Neutral-Point-Clamped Inverter," *IEEE Transactions on Industry Applications*, vol. 49, no. 6, pp. 2583–2592, 2013.
- [12] R. P. Aguilera, P. Lezana, and D. E. Quevedo, "Switched Model Predictive Control for Improved Transient and Steady-State Performance," *IEEE Transactions on Industrial Informatics*, vol. 11, no. 4, pp. 968– 977, Aug. 2015.

- [13] R. P. Aguilera, P. Lezana, G. Konstantinou, P. Acuna, B. Wu, S. Bernet, and V. G. Agelidis, "Closed-loop SHE-PWM technique for power converters through Model Predictive Control," in *Industrial Electronics Society, IECON 2015 41st Annual Conference of the IEEE*, Yokohama, Nov. 2015, pp. 005 261–005 266.
- [14] J. Rodríguez, M. P. Kazmierkowski, J. Espinoza, P. Zanchetta, H. Abu-Rub, H. A. Young, and C. A. Rojas, "State of the Art of Finite Control Set Model Predictive Control in Power Electronics," *IEEE Transactions* on *Industrial Informatics*, vol. 9, no. 2, pp. 1003–1016, May 2013.
- [15] S. Vazquez, A. Marquez, R. Aguilera, D. Quevedo, J. I. Leon, and L. G. Franquelo, "Predictive optimal switching sequence direct power control for grid-connected power converters," *IEEE Transactions on Industrial Electronics*, vol. 62, no. 4, pp. 2010–2020, April 2015.
 [16] T. Geyer, "A Comparison of Control and Modulation Schemes for
- [16] T. Geyer, "A Comparison of Control and Modulation Schemes for Medium-Voltage Drives: Emerging Predictive Control Concepts Versus PWM-Based Schemes," *IEEE Transactions on Industry Applications*, vol. 47, no. 3, pp. 1380–1389, 2011.
- [17] P. Karamanakos, T. Geyer, N. Oikonomou, F. D. Kieferndorf, and S. Manias, "Direct Model Predictive Control: A Review of Strategies That Achieve Long Prediction Intervals for Power Electronics," *IEEE Industrial Electronics Magazine*, vol. 8, no. 1, pp. 32–43, Mar. 2014.
- [18] C. Buccella, C. Cecati, and H. Latafat, "Digital control of power converters – a survey," *Industrial Informatics, IEEE Transactions on*,

- vol. 8, no. 3, pp. 437-447, Aug 2012.
- [19] T. Vyncke, S. Thielemans, and J. Melkebeek, "Finite-set model-based predictive control for flying-capacitor converters: Cost function design and efficient fpga implementation," *Industrial Informatics, IEEE Trans*actions on, vol. 9, no. 2, pp. 1113–1121, May 2013.
- [20] T. Geyer and D. E. Quevedo, "Multistep Finite Control Set Model Predictive Control for Power Electronics," *IEEE Transactions on Power Electronics*, vol. 29, no. 12, pp. 6836–6846, 2014.
- [21] —, "Performance of Multistep Finite Control Set Model Predictive Control for Power Electronics," *IEEE Transactions on Power Electronics*, vol. 30, no. 3, pp. 1633–1644, Mar. 2015.
- [22] R. P. Aguilera, R. Baidya, P. Acuna, S. Vazquez, T. Mouton, and V. G. Agelidis, "Model predictive control of cascaded h-bridge inverters based on a fast-optimization algorithm," in *Industrial Electronics Society*, *IECON 2015 41st Annual Conference of the IEEE*, Nov 2015, pp. 004 003–004 008.
- [23] D. E. Quevedo, R. P. Aguilera, and T. Geyer, "Predictive Control in Power Electronics and Drives: Basic Concepts, Theory, and Methods," in *Studies in Computational Intelligence*. Cham: Springer International Publishing, Jan. 2014, pp. 181–226.
- [24] R. A. Horn and C. R. Johnson., "Matrix Analysis," Cambridge University Press, Cambridge, UK., 1985.