Rongala Ram Prasad

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Objective

Motivated Electronics and Communication graduate with strong fundamentals in VLSI design, CMOS circuits, and digital electronics. Proficient in Verilog programming and eager to contribute to semiconductor industry advancements. Passionate about low-power design, verification methodologies, and digital circuit optimization.

Education

Bachelor of Technology (ECE), Pragati Engineering College   *Nov 2021 – May 2025*
CGPA: 8.03

Secondary Education (MPC), Narayana Junior College   *July 2019 – May 2021*
CGPA: 9.04

Primary Education, Madhuri Vidyala   *July 2018 – March 2019*
CGPA: 9.8

Skills

Domain Knowledge: Digital Electronics, Electronic Devices, CMOS Design
Languages: Python, SQL
Tools: Power BI, ModelSim
Soft Skills: Team Work, Time Management, Critical Thinking

Internships

Tata Data Visualization: Empowering Business with Effective Insights *– August 2024*
Job Simulation on Forage

- Created data visualizations for Tata Consultancy Services.

- Prepared questions for client senior leadership meetings.

- Designed visuals to support executive decision-making.

Projects

Hybrid Approximate Multiplier for Efficient Energy Image Processing

- Designed an optimized **low-power multiplier architecture** for **VLSI-based** image processing.

- **Implemented using Verilog and simulated on ModelSim** to verify performance.

- Evaluated improvements in energy efficiency and area reduction in **digital circuits**.

- Applied CMOS design principles to enhance power efficiency and circuit reliability.

Bike Company Sales Analysis

- Analyzed sales data using **SQL** and **MS Excel**.

- Developed an interactive **Power BI** dashboard for sales insights.

Certifications

- **CMOS Certification** from Class Central

- **IoT Certification** from edX

- **Python Certification** from NPTEL

Achievements

- Organized technical and cultural college events, including webinars.

- Awarded first place in Kho Kho and second place in cricket at college-level sports events.

- Participated in a national-level coding competition, securing a position in the top 10%.