



# JNTU COLLEGE OF ENGINEERING HYDERABAD

(AUTONOMOUS)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD - 500 085, TELANGANA, INDIA.

## CONSOLIDATED MEMORANDUM OF MARKS



CMM. No.: **c 0104591**

BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No.: 213000440

Month & Year of Final Exam : April, 2018

Name: RAM SAI KARTHIK UNNAM

Class Awarded: First Class with Distinction

Hall Ticket No.: 14011A0434

Year of Admission: 2014 - 2015

SNO	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	SNO	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	30	70	100			Maximum Marks in Lab	25	50	75	
	Seminar	50	†	50			Industrial Training/Mini Project	#	50	50	
	Project	60	140	200							
I SEMESTER					I YEAR	II SEMESTER					
1	MATHEMATICS-I	27	54	81	4	1	MATHEMATICS-II	20	50	70	4
2	COMP.PROGRAMMING & DATA STRUCTURES	26	25	51*	4	2	BASIC ELECTRICAL & ELECTRONICS ENGG.	23	49	72	4
3	ENGLISH	27	47	74	4	3	ENVIRONMENTAL SCIENCE	23	56	79	4
4	ENGINEERING GRAPHICS	20	66	86	4	4	COMPUTATIONAL MATHEMATICS	15	34	49	2
5	ENGINEERING MECHANICS	24	56	80	4	5	ENGINEERING CHEMISTRY	22	47	69	4
6	ENGINEERING WORKSHOP	20	34	54	2	6	APPLIED PHYSICS	26	57	83	4
7	COMP.PROGRAMMING & DATA STRUCT.-(LAB)	23	30	53	2	7	COMPUTATIONAL MATHEMATICS - (LAB)	25	46	71	2
8	ENGLISH LANG. & COMMUNIC.SKILLS -(LAB)	23	42	65	2	8	BASIC ELE.& ELECTRONICS ENGG.-(LAB)	24	43	67	2
						9	APPLIED PHYSICS -(LAB)	22	43	65	2
I SEMESTER					II YEAR	II SEMESTER					
1	MATHEMATICS-III	26	63	89	4	1	SWITCHING THEORY & LOGIC DESIGN	28	44	72	4
2	SIGNALS & SYSTEMS	29	66	95	4	2	PULSE & DIGITAL CIRCUITS	29	58	87	4
3	ELECTRICAL TECHNOLOGY	23	62	85	4	3	ELECTRO MAGNETIC THEORY & TRANSM.LII	28	38	66	4
4	ANALOG ELECTRONICS	29	63	92	4	4	CONTROL SYSTEMS	28	37	65	4
5	NETWORK ANALYSIS	25	53	78	4	5	ANALOG COMMUNICATIONS	23	58	81	4
6	ANALOG ELECTRONICS -(LAB)-I	23	46	69	2	6	HUMAN VALUES & PROFESSIONAL ETHICS	12	27	39	2
7	ELECTRICAL TECHNOLOGY -(LAB)	24	36	60	2	7	ANALOG COMMUNICATIONS -(LAB)	21	39	60	2
8	BASIC SIMULATION (LAB)	24	38	62	2	8	PULSE & DIGITAL CIRCUITS -(LAB)	23	42	65	2
						9	ANALOG ELECTRONICS -II (LAB)	18	37	55	2
I SEMESTER					III YEAR	II SEMESTER					
1	MANAGERIAL ECONOMICS & FINANCIAL ANALY	25	52	77	4	1	MICROPROCESSORS & MICROCONTROLLER	22	33	55	4
2	LINEAR & DIGITAL I C APPLICATIONS	26	48	74	4	2	DIGITAL SIGNAL PROCESSING	25	44	69	4
3	OOPS THROUGH JAVA	27	51	78	4	3	ELECTRONIC MEASURE & INSTRUMENTATIC	22	32	54	4
4	ANTENNAS & WAVE PROPAGATION	21	57	78	4	4	DIGITAL SYSTEMS DESIGN	25	50	75	4
5	DIGITAL COMMUNICATIONS	27	52	79	4	5	ENTREPRENEURSHIP	25	49	74	4
6	LINEAR I C APPLICATIONS -(LAB)	24	43	67	2	6	DISASTER MANAGEMENT	13	25	38	2
7	DIGITAL ELECTRONICS -(LAB)	22	43	65	2	7	DIGITAL SIGNAL PROCESSING -(LAB)	23	44	67	2
8	DIGITAL COMMUNICATIONS -(LAB)	20	40	60	2	8	MICROPROCESSOR & MICROCONTROLLER-	22	35	57	2
						9	ADV.ENGLISH LANG.COMMUNIC.SKILLS-(LAE	23	45	68	2
I SEMESTER					IV YEAR	II SEMESTER					
1	MICROWAVE ENGINEERING	26	54	80	4	1	MANAGEMENT SCIENCE	25	49	74	4
2	COMPUTER NETWORKS	26	37	63	4	2	RADAR SYSTEMS	29	67	96	4
3	VLSI DESIGN	18	36	54*	4	3	COMPREHENSIVE VIVA-VOCE	-	25	25	2
4	CELLULAR & MOBILE COMMUNICATIONS	22	43	65	4	4	MAJOR PROJECT#	55	125	180	12
5	NETWORK SECURITY & CRYPTOGRAPHY	25	38	63	4						
6	VLSI & ECAD-(LAB)	20	36	56	2	**	GENDER SENSITIZATION -(LAB)	22	40	62	2
7	MICROWAVE ENGINEERING-(LAB)	23	38	61	2						
8	INDUSTRY ORIENTED MINI PROJECT-(S.P)	-	42	42	2						
9	SEMINAR	45	-	45	2						

Number of Credits registered for : 214 Aggregate Marks Secured for credits: 204

Aggregate Marks Secured : 4323 OUT OF 5500 (78.6%)

Date of Issue : May 18 2018

(see overleaf for Rules concerned to award of class)



(\*Courses registered but not counted for calculation of aggregate)

*E. Sridhar Reddy*

PRINCIPAL