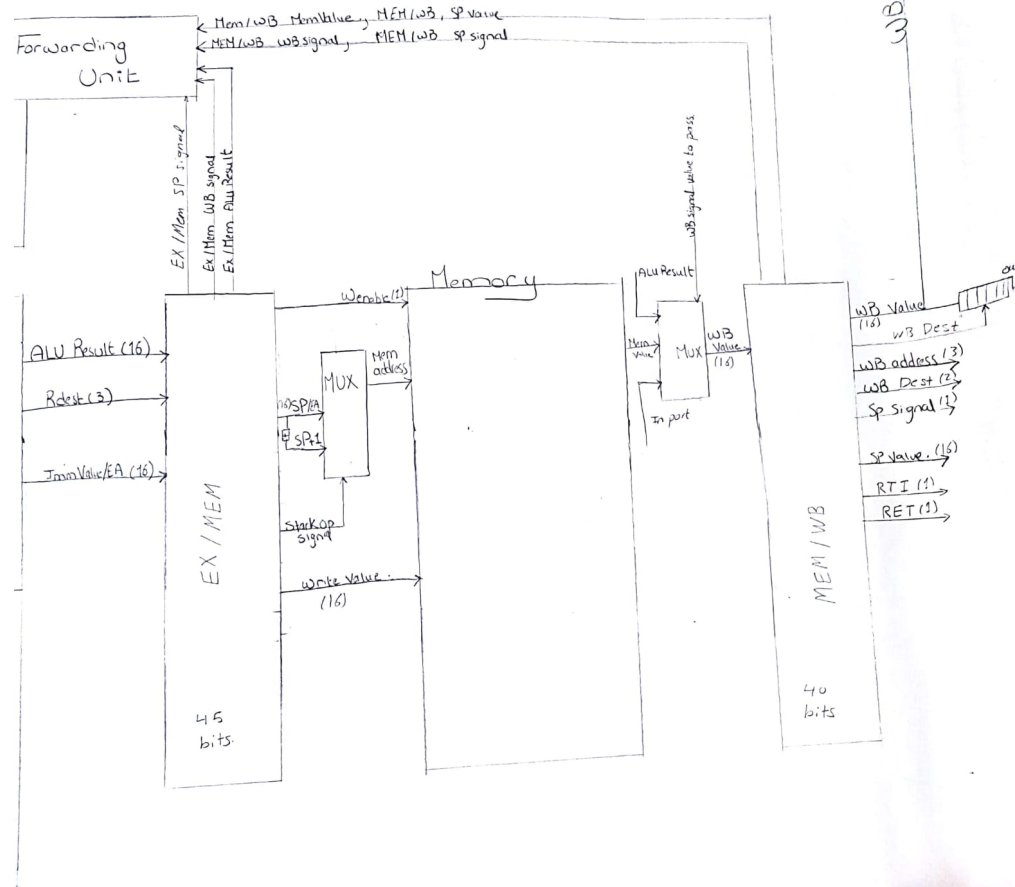
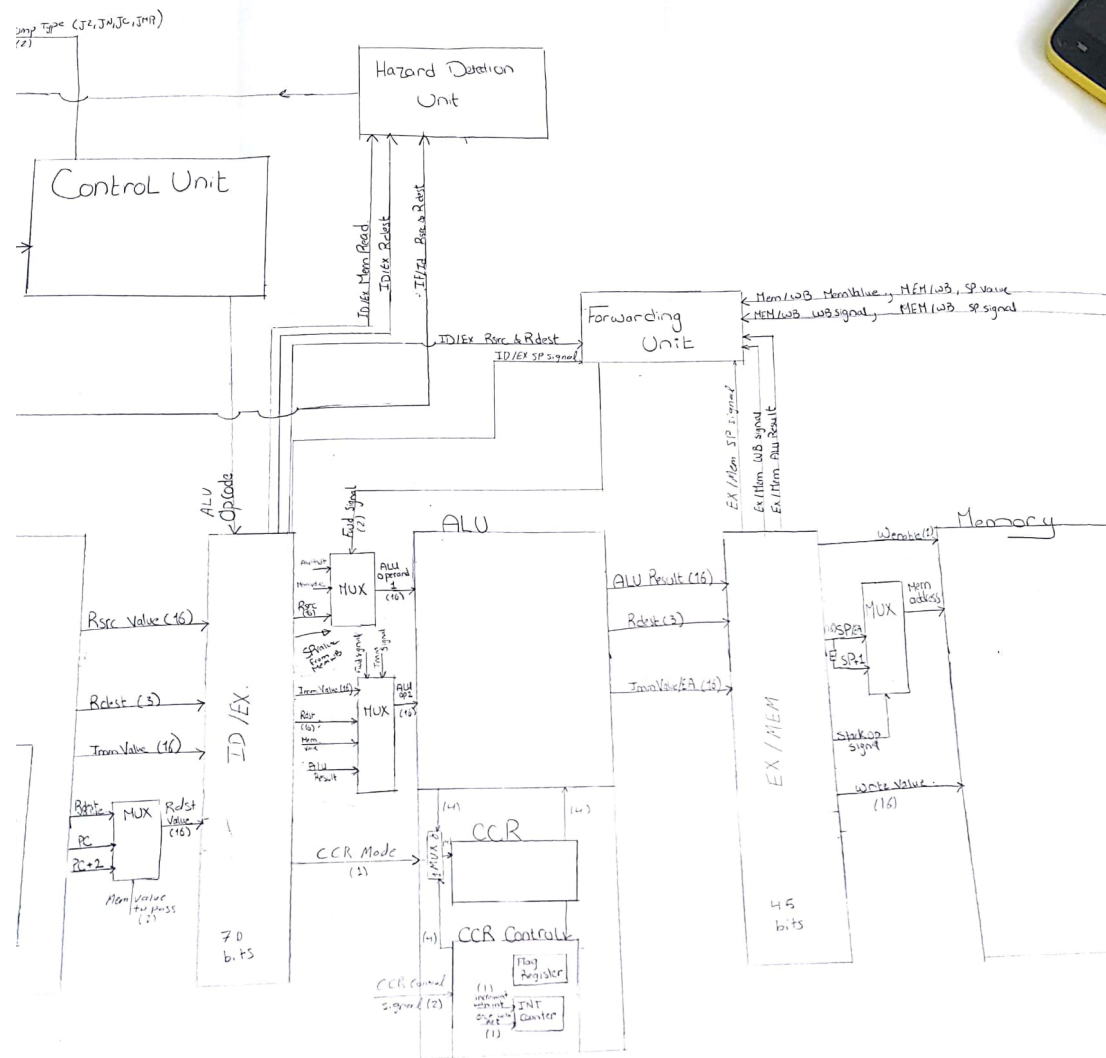


| | | | | |
|-----------------|-------|---------------|---------------|---------------|
| OpCode (5 bits) | 00000 | Rsrc (3 bits) | Rdst (3 bits) | Imm (16 bits) |
|-----------------|-------|---------------|---------------|---------------|



| | | | | |
|-----------------|-------|---------------|---------------|------------------|
| OPCode (5 bits) | 00000 | Rsrc (3 bits) | Rdst (3 bits) | Imm/EA (16 bits) |
|-----------------|-------|---------------|---------------|------------------|



OpCode (5 bits) 00000 Rsrc (3 bits)

