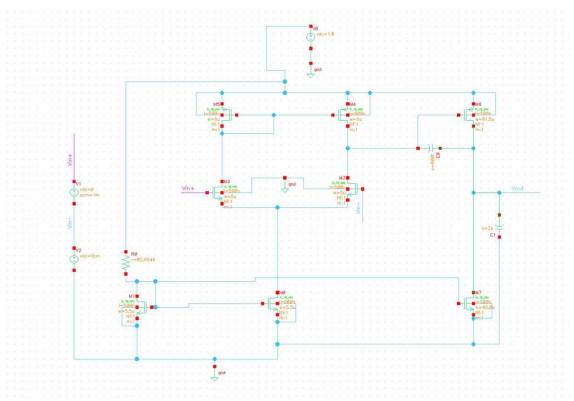
Design of a two stage operational amplifier

Schematic:



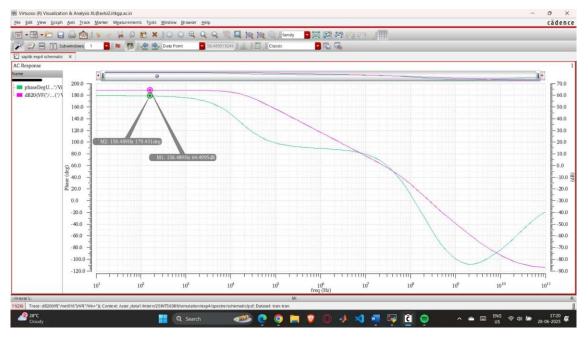
Inputs used:

Vin+ : Ac magnitude: 1m dc voltage of 0V and common mode voltage=1.2V

Vin-: common mode voltage=1.2V

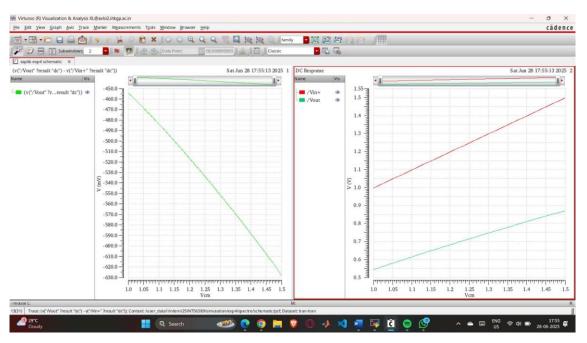
Plots:

1. Simulated at 27oC, VDD = 1.8 V, and TT corner



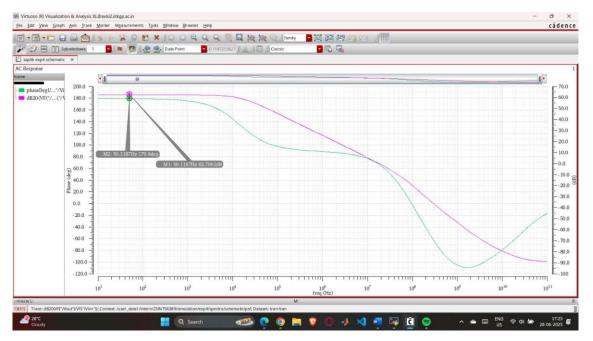
Gain and Phase plot:

Specs met as per Avo(gain)>=60db

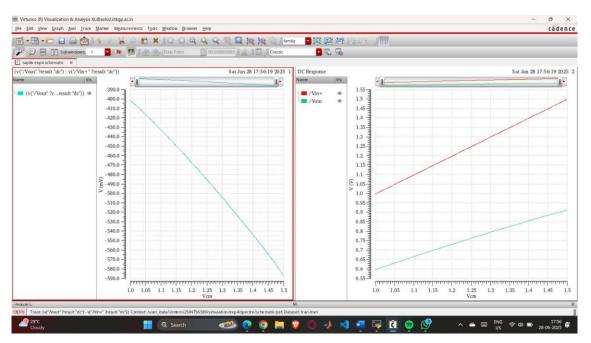


Input Offset plot(left)

2. Simulated at 125oC, VDD = 1.62 V, SS corner

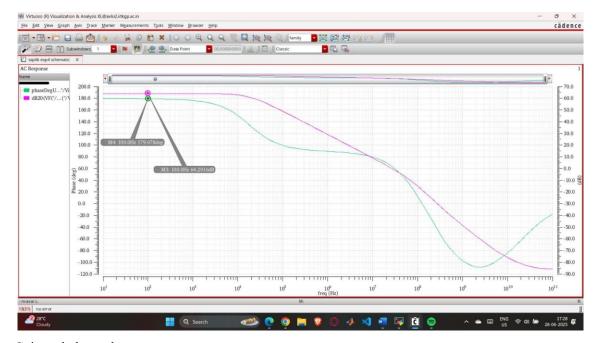


Gain and phase plot

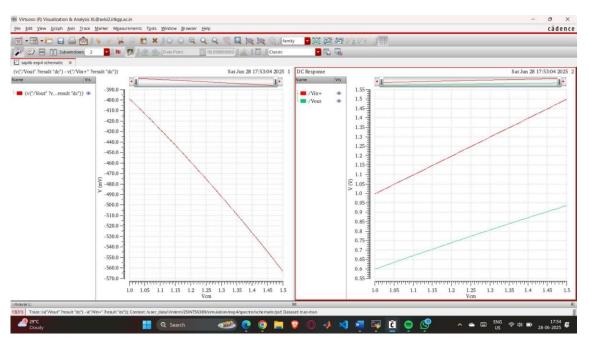


Input offset plot(left)

3. Simulated at -40oC, VDD = 1.98 V, FF corner



Gain and phase plot



Input offset plot(left)