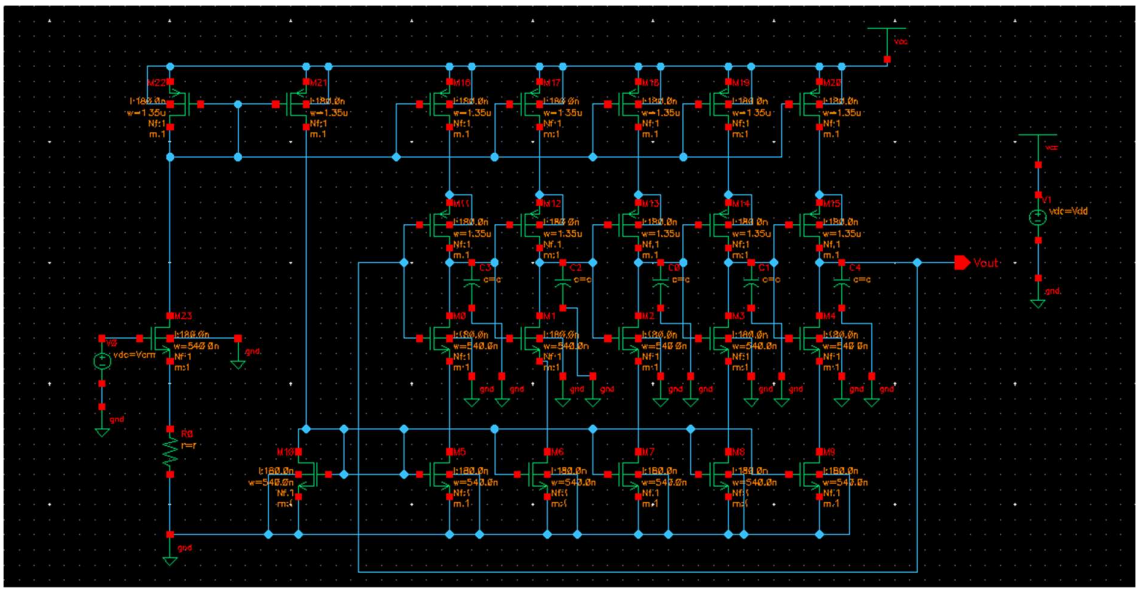
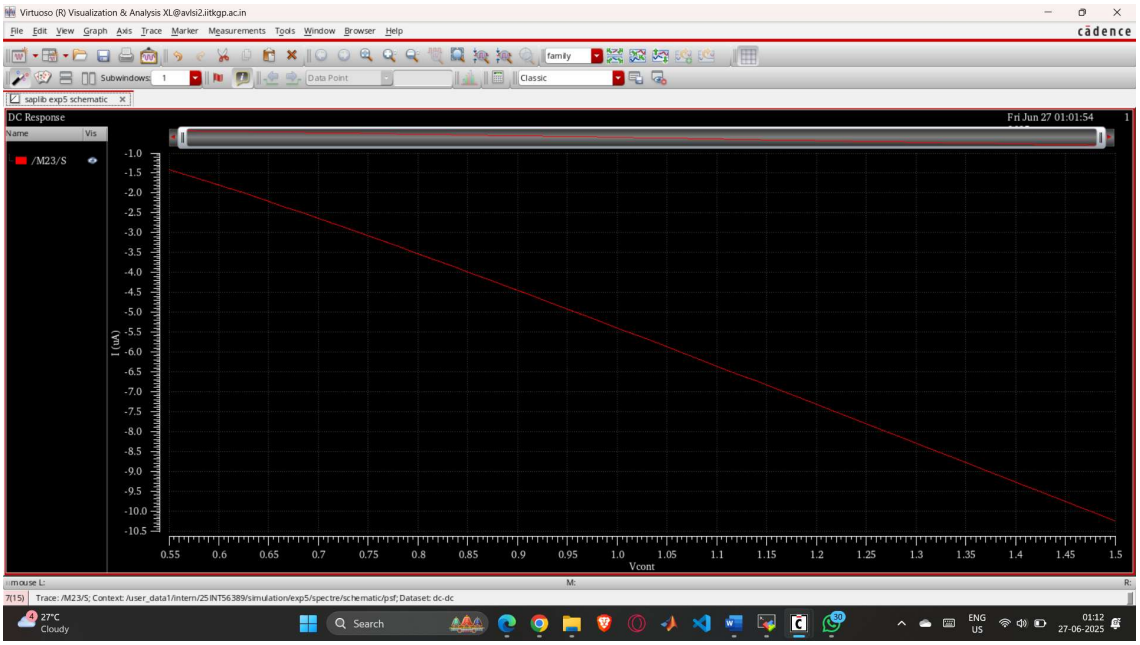
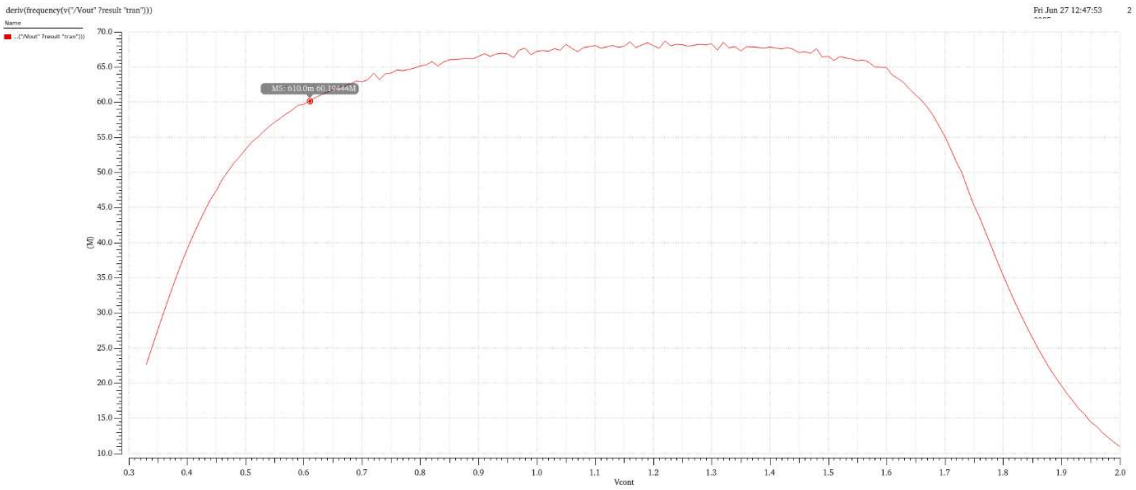


Plots:

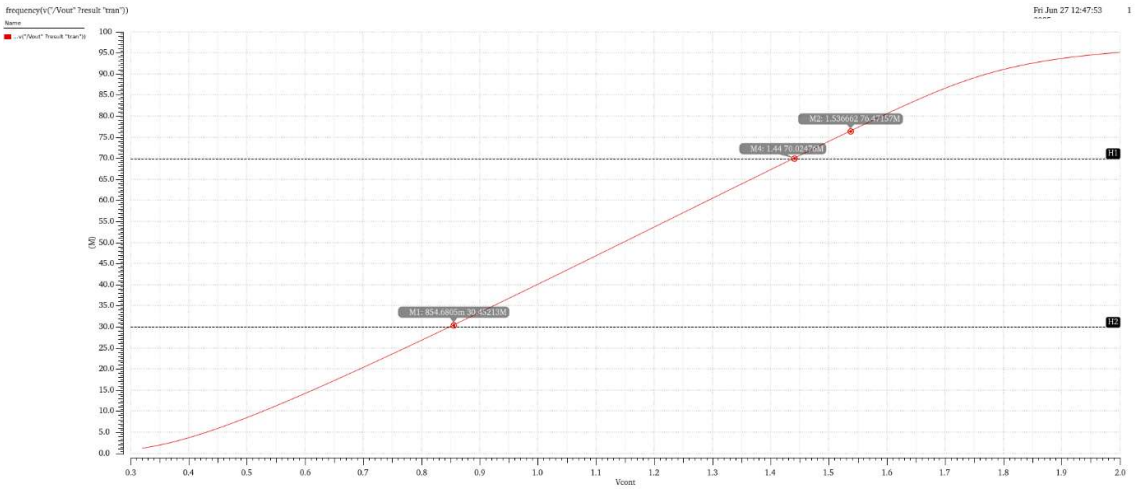
1. All the plots and results are simulated at 27 degree celcius at TT corner and Vdd=1.8V



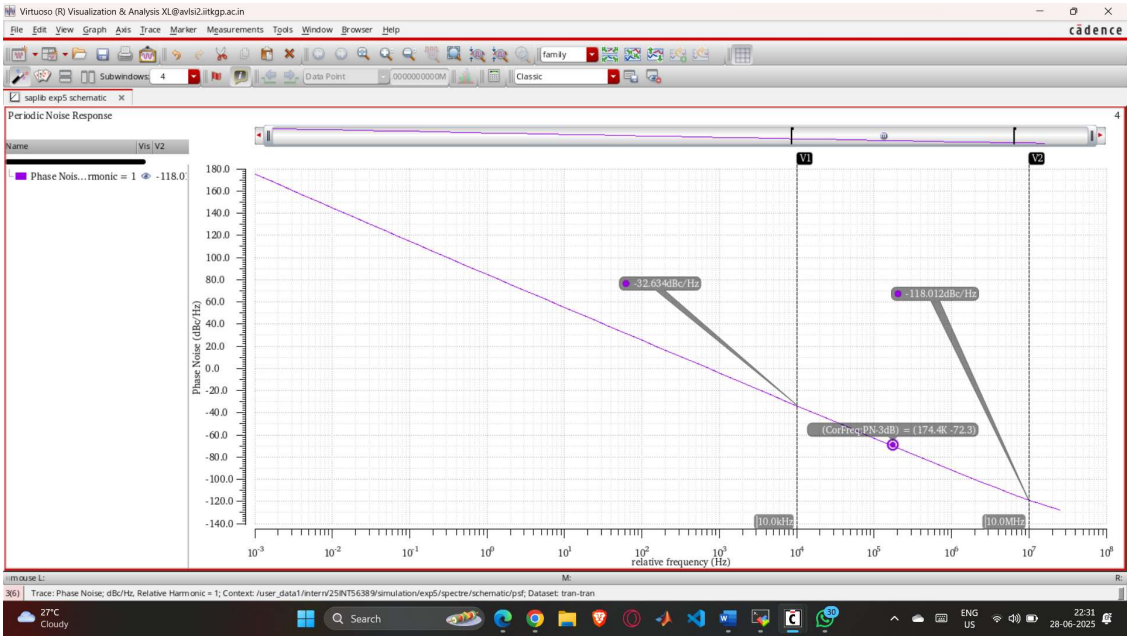
Control voltage vs Control current



Kvco vs Vcontrol

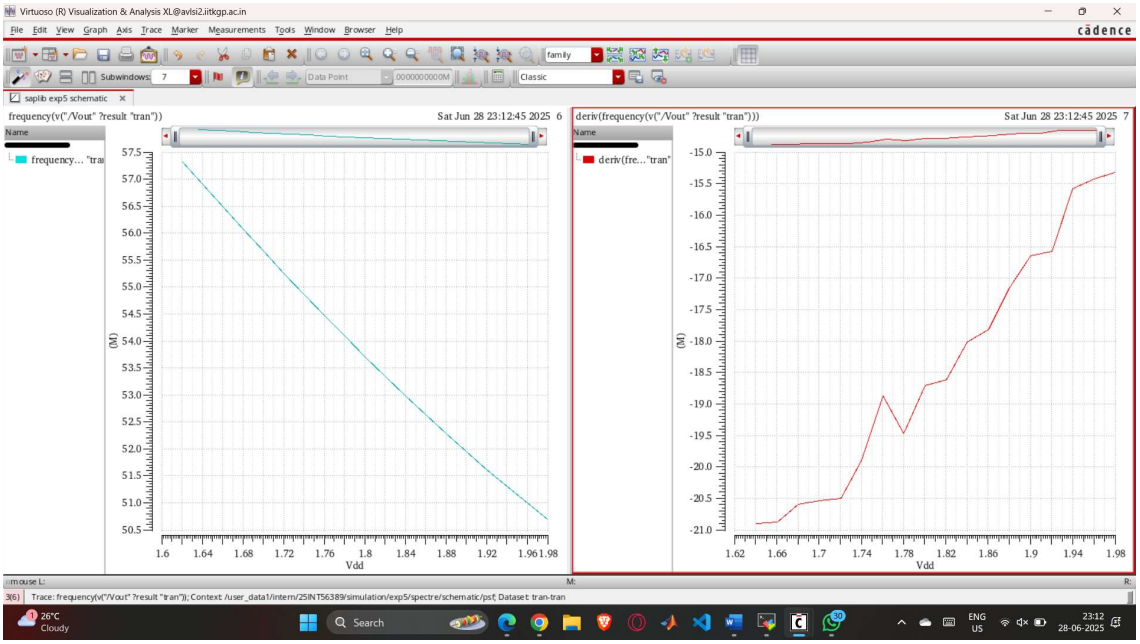


Vcontrol vs Output frequency



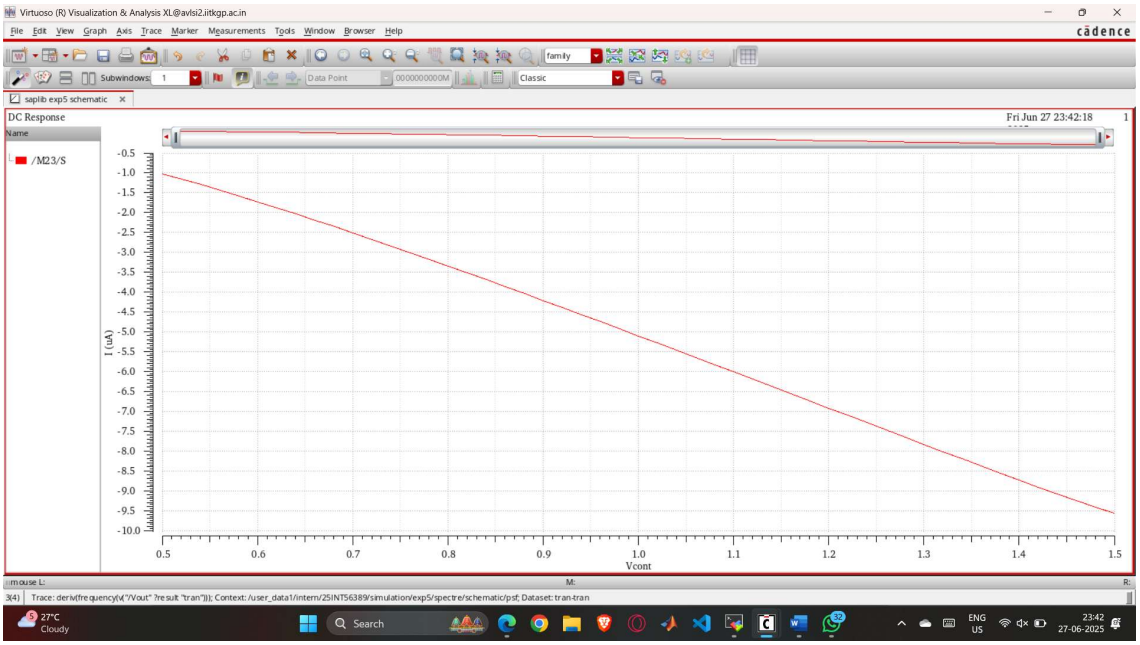
Phase noise analysis for beat frequency=53.632Mhz at Vcontrol=1.2V and Vdd=1.8

Beat frequency found by Vcontrol v/s frequency plot

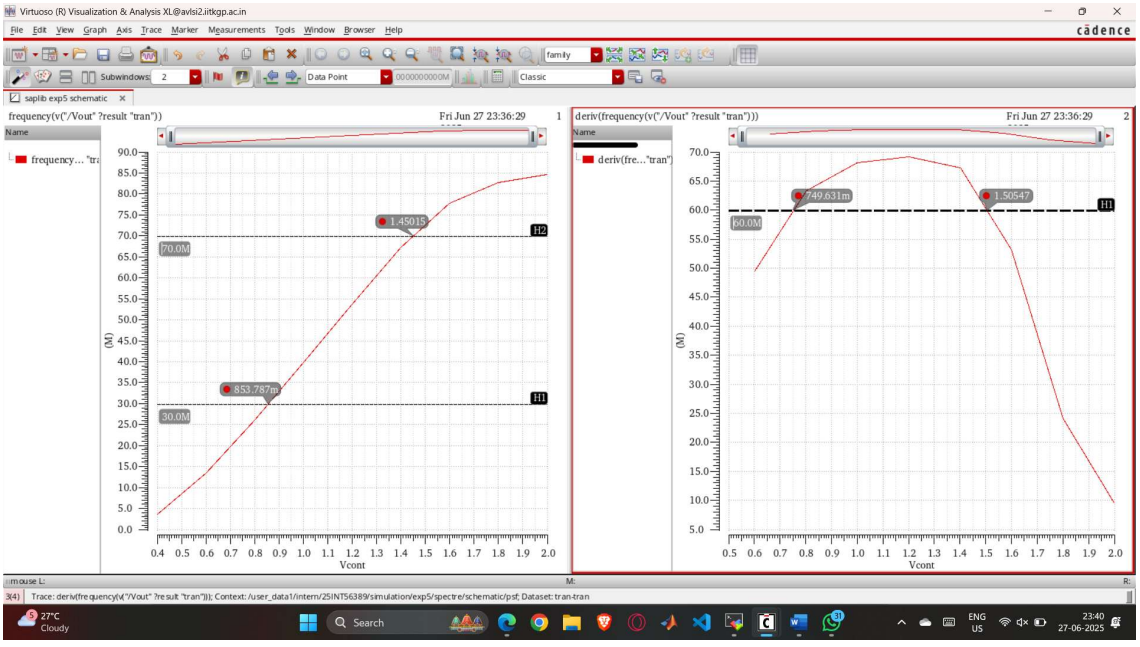


Vdd v/s frequency(left) and Vdd v/s Kvdd(right)

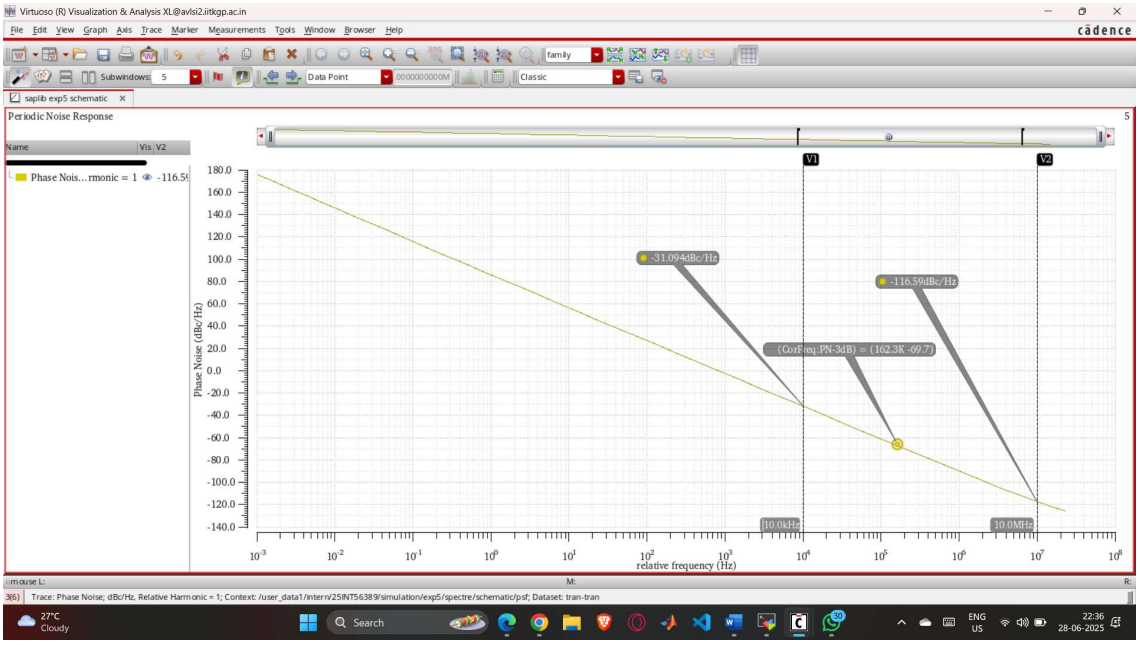
2. Simulating at 125 degree Celcius in SS corner with Vdd=1.62 V



Vcontrol vs Icontrol

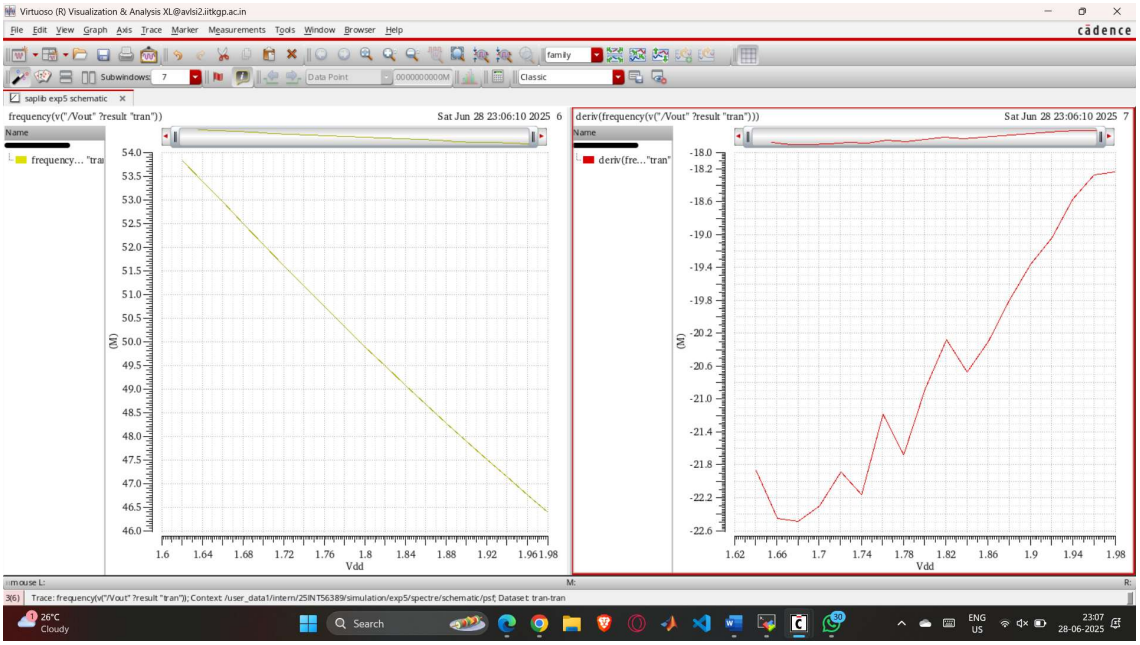


Vcontrol vs Frequency(left) and Vcontrol vs Kvco(right)



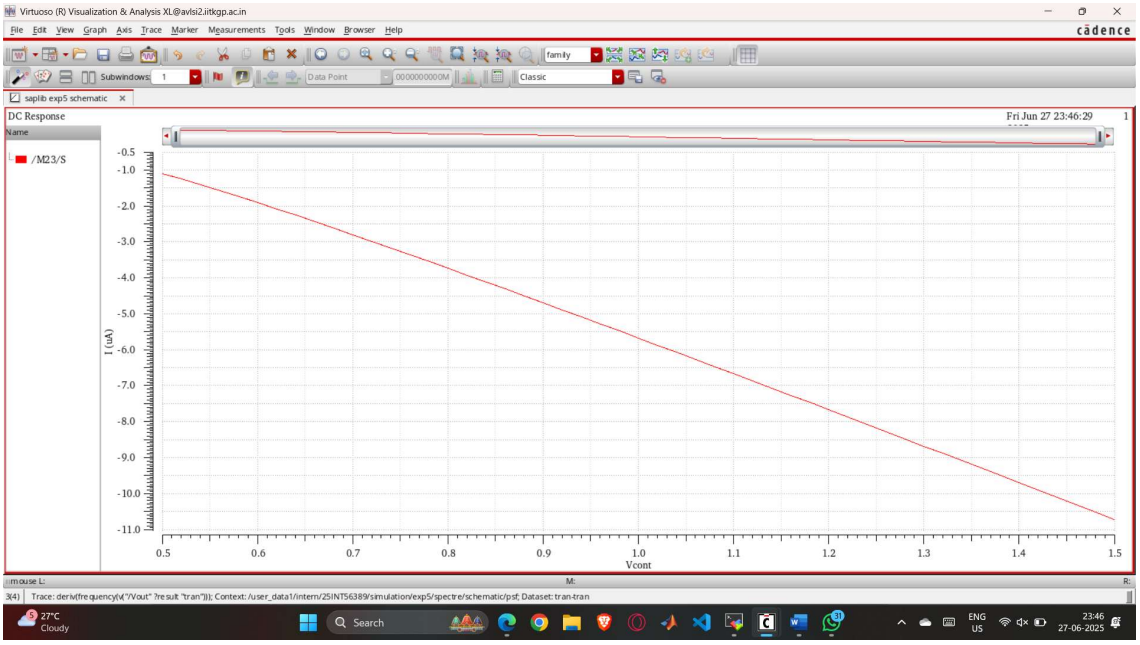
Phase noise analysis for beat frequency=49.905Mhz at Vcontrol=1.2V and Vdd=1.8

Beat frequency found by Vcontrol v/s frequency plot

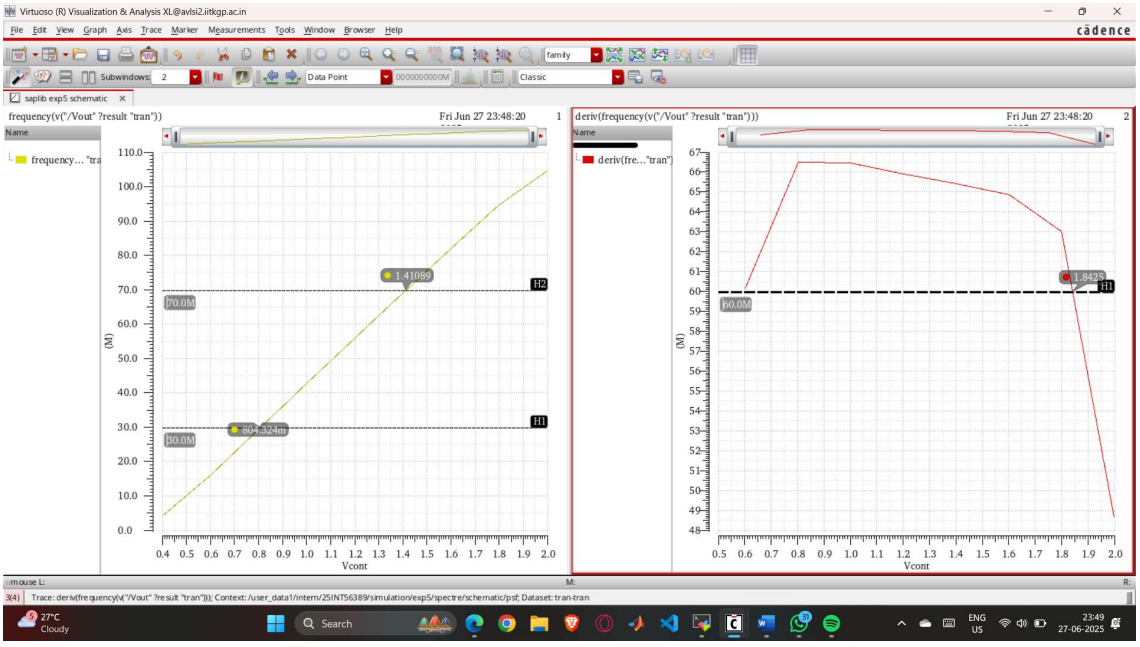


Vdd v/s frequency(left) and Vdd v/s Kvdd(right)

3. Simulating for -40 degree Celcius, Vdd=1.98V, FF corner



Vcontrol vs Icontrol



Vcontrol vs Frequency(left) and Vcontrol vs Kvco(right)