

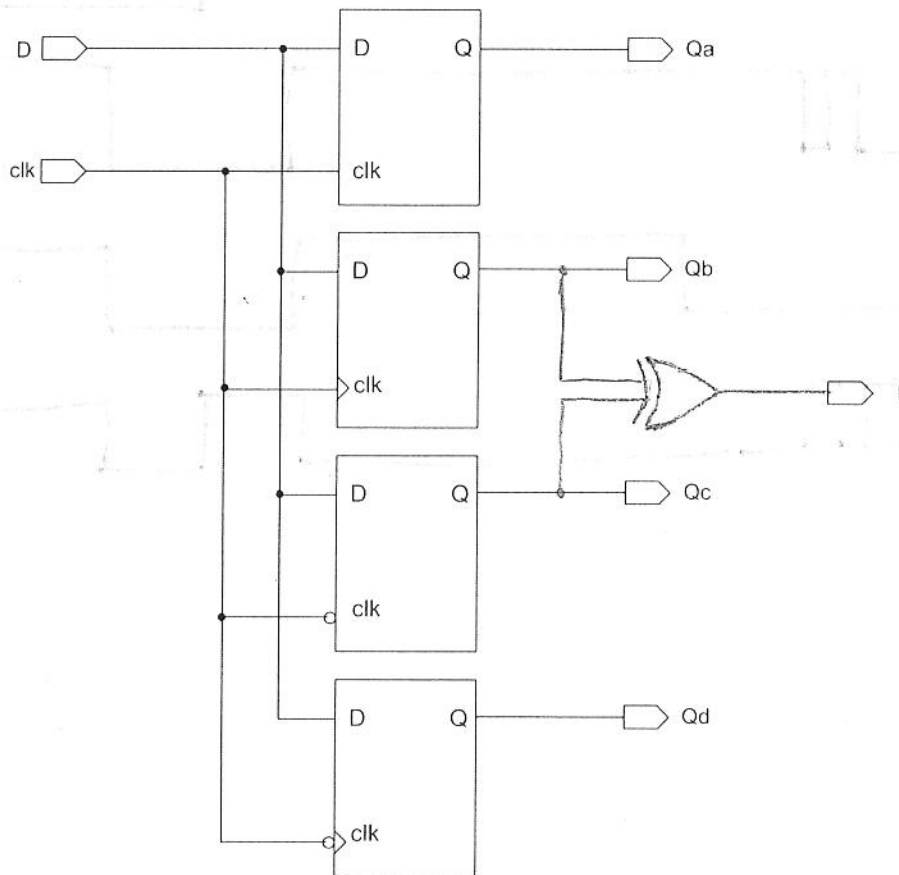
Boise State University
Department of Electrical and Computer Engineering
ECE 230 Digital Systems
Quiz 7 – October 14 2010

Name: _____

2 1. What are the differences between D latch and D flip-flop?

level sensitive → edge sensitive

8 2. Complete the following timing diagram for the given logic circuitry.



clk

D

Qa

Qb

Qc

Qd

f

