

src/ppu.rs

```
enum Mode {  
    HBlank = 0x00,  
    VBlank = 0x01,  
    RdOam   = 0x02,  
    RdVram  = 0x03,  
}
```

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```
pub fn read_byte(&self, address: u16) → u8 {  
    match a {  
        0x8000 ..= 0x9FFF ⇒ {  
            self.vram[(self.vrambank * 0x2000) |  
                (address as usize & 0x1FFF)]  
        }  
        0xFE00 ..= 0xFE9F ⇒  
            self.voam[address as usize - 0xFE00],  
        // ...  
    }  
}
```