

Exascale Computing at the LHC : Narrative

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1 Funding Strategy

This research proposal outlines the necessity to extend the computing capacity of the experiments at the Large Hadron Collider (LHC) in Geneva, Switzerland, to the exascale of high-throughput processing. This is an enormously challenging task, but a necessary one to ensure the long-term success of the LHC experiments.

Exascale computing (in this case, in high throughput) is an enormously growth-oriented area. Even during lean economic times, the US Federal Government is pledging to support this area of research, for instance in the Department of Energy Exascale Computing Initiative [1]. Indeed, the DOE Office of Science quotes :

The Exascale initiative will be significant and transformative for Department of Energy missions.

The current level of funding for the DOE EIC and related activities is \$21M. [2]. This “transformative” strategy is one that is envisioned to continue well into the future. In addition to DOE programs, the National Science Foundation (NSF) has several programs to address the problems of high-throughput exascale computing [3, 4].

In addition to governmental programs such as above, private sector funding sources are also available, such as the Google Faculty Research Awards [5], which has an interest in exascale computing projects also.

2 Project Organization

The principle investigators (PIs) of this proposal have a widely-varied and applicable skill set to accomplish the goals of extending LHC computing to the exascale.

- Salvatore Rappoccio has 15 years of experience programming in a high-energy physics environment, as well as other numerical software design for the private sector. He is an expert in critical areas of event reconstruction at CMS which can be optimized for multicore usage.
- Lukasz Ziarek has 9 years of experience in language, compiler, and runtime design targeted at improving multicore performance. He has worked on 5 compilers and 3 Java VMs. He is an expert at speculative and transactional computation focusing on the extraction of parallelism and lightweight concurrency.
- Steven Ko has 10 years of experience in distributed systems. His recent focus has been large-scale data processing in the cloud using MapReduce and other technologies built on top of it. He also has 5 years of experience in large-scale storage and data management in data centers.
- Matthew Jones has more than 12 years experience in scientific software development, with a particular emphasis on parallel programming. As the Associate Director of the Center for Computational Research at the University at Buffalo, he has also been responsible for designing and administering high-performance computing facilities for large-scale scientific computing.
- Reneta Barneva has 29 years of experience as a computer scientist, and is the author of over 50 publications in the subjects of combinatorial image analysis, pattern recognition, and computational geometry.

There are also two senior mentors to the project to offer guidance, insight and technical information.

- Valentin Brinkov has over 20 years of experience as a mathematician, and has extensive expertise in design and analysis of algorithms, combinatorial optimization, and discrete geometry.

- Peter Elmer is the deputy offline software coordinator of the CMS experiment at the LHC. He was responsible for the design and implementation of the data and workflow management system used by CMS, as well as its core event processing software and software development environment. He has made important contributions to the software and computing of several high-energy physics experiments over the past 20 years and is currently focused on planning the computing R&D needed for the next decade in CMS.

In addition, the proposal includes funding for one graduate student from the CSE department at SUNY Buffalo at 100% FTE, and one graduate student from the Physics Department at SUNY Buffalo at 50% FTE. They will be performing the study of this problem under the direction of the PIs, and will also be responsible for deployment and scalability of the solution.

3 Narrative

With the discovery of the Higgs boson by the Large Hadron Collider (LHC) experiments ATLAS and CMS [6, 7], the standard model (SM) of particle physics is now complete. This model unifies the electromagnetic force (carried by the *photon*) with the weak force, responsible for radioactive decay (carried by the *W and Z bosons*). At long last, physicists now understand that via interactions with the Higgs field, the *W and Z bosons* acquire a mass, but the photon does not. This is referred to as “electroweak symmetry breaking”.

A new phase of particle physics has therefore begun. The questions have shifted from the cause of electroweak symmetry breaking, to the study of the Higgs boson and its interactions in detail. To understand the larger picture of the fundamental forces in nature, the past excellence of the LHC experiments must therefore continue unabated in the face of new technical challenges.

One of the major technical challenges that lies ahead is the continuation of the scaling of computational power year by year, known colloquially as “Moore’s Law”. To set the scale, at the CMS experiment with LHC collisions rates reaching $7 \times 10^{33} \text{cm}^{-2} \text{s}^{-1}$, the processing time to reconstruct each event collected by CMS was approximately 20 seconds per event. However, as the collision rate is increased, the computational time currently scales quadratically. As the upgraded LHC is expected to deliver $> 12 \times 10^{33} \text{cm}^{-2} \text{s}^{-1}$ in the upcoming run, the processing time per event is expected to reach several minutes per event as shown in Figure 1. Furthermore, in future runs of the LHC in the next 15 years, the luminosity is expected to reach as high as $> 1 \times 10^{34} \text{cm}^{-2} \text{s}^{-1}$, which would correspond (naively) to several hours of computational time per event! Clearly, it is necessary for the computing power to scale in order to compensate for this dramatic increase in CPU time with instantaneous luminosity.

However, with the expected end of the historic scaling of single-core processing capability [8], it is imperative to utilize a parallel processing strategy in order to maintain the levels of computational speed of LHC data in the immediate future in experiments such as CMS. As a starting point we first note that during 2012, the CPU usage for CMS was very roughly divided up as:

- $\sim 40\%$ event simulation
- $\sim 20\%$ prompt event reconstruction (within 48 hours of data-taking)

- $\sim 40\%$ mixed user analysis applications

Oftentimes, the codes used by CMS (and experimental HEP in general) tend to lack clear numerical “kernels” where optimization efforts can be focused. Given these characteristics they are generally more properly classified as “high throughput computing” (HTC) rather than “high performance computing” (HPC). In terms of their detailed behavior on the CPU many of these codes resemble more general enterprise or “cloud” applications [9, 10].

However, there are several numerical algorithms where parallelization could be exploited more directly. One of these is the so-called “jet clustering” algorithms used at CMS. At CMS, this computation is done very often by individuals accounted in the 40% of CPU usage from “mixed user analysis applications” as described above. It is likely, therefore, that improvements observed in jet clustering will primarily benefit this portion of the CPU usage. We now discuss the prospects for utilizing parallelization in jet clustering in detail.

3.1 Jet Clustering

The energetic deposits of particles need to be clustered to obtain the complete response in our detector. This is because the process inherently involves a “shower” of particles called a “jet”. This “jet clustering” is a well-established technique employed at many different particle physics experiments worldwide, and is implemented in a common software framework called `fastjet` [11]. The mathematical problem is analogous to the “K-nearest neighbors algorithm” [13] (kNN). The single-core optimization of the mathematical implementation of this algorithm is outlined in Ref. [12], and the computational time scales as $O(N^2)$ or $O(N \ln N)$, where N is the number of inputs to the algorithm.

Even though the single-core computational strategy is sufficient for many applications, there are two key components of jet clustering that are still inefficient for future LHC data processing, which can be solved with appropriate development of a parallelization strategy. The first is the parallelization of the kNN algorithm itself to make optimum use of the more advanced vectorization capabilities of modern multicore CPUs. The second is to use a “divide and conquer” strategy to reconstruct a single collision event in several disjoint sections of the CMS detector in parallel.

There is existing work and literature on the topic of the parallelization of the kNN algorithm, for instance, in Refs. [14, 15, 16], where improvements $O(100)$ in CPU performance are observed over standard algorithms. Since the proposed use case is very similar to the kNN algorithm, similar improvements to the processing time by parallelization strategies are expected.

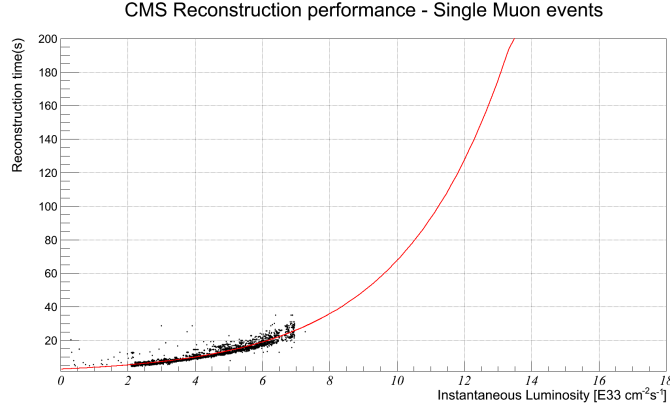


Figure 1: Event processing time versus instantaneous luminosity.

3.2 Full Stack Parallelization

To achieve the necessary improvements in performance required for scalability of jet clustering, we propose to examine parallelization opportunities across the entire software stack, including the use of lightweight concurrency extraction to mask high latency computations or I/O actions, extraction of parallelization from the computation itself in the form of optimistic speculation and specialized transform, and new methods for distributing the computation to maximize parallelization on each node.

3.2.1 Lightweight Concurrency for Latency Masking

Many mathematical kernels contain opportunities for extracting “micro parallelism,” usually on the order of tens of instructions, from their computational components. Unfortunately, it is very difficult to parallelize this computation profitably as the overhead of thread creation, scheduling, synchronization, and migration outweigh the gains in parallelism. Instead of extracting explicit parallelism from such computations, we proposed to explore

methods of lightweight asynchrony to allow for computation to proceed while waiting on high latency I/O operations to complete or the results of other computations. Since the creation of threads and associated schedule and synchronization costs are typically prohibitive, we will explore new threading models that allow for logically distinct computations to execute within a given construct. The PIs previous research has indicated that such schemes can profitably boost overall performance in the context of ML code [17, 18]. The salient research challenges in applying this strategy are as follows: 1) identifying what computation can be executed safely during high latency operations at compile time, 2) providing a lightweight threading runtime and programming model in the context of an imperative language, 3) specializing the approach to numeric kernels, and 4) building support for computation in a distributed setting.

3.2.2 Speculative Computation

In addition to exploring explicit parallelization of the numeric kernels in jet clustering, we propose to explore extraction of parallelism via speculative computation. At its core, speculative computation breaks apart sequential or parallel tasks into smaller tasks to be run in parallel. Once the speculation has completed, the runtime system validates the computation. If the computation is incorrect (*i.e.* a “data race” is detected, the computation cannot be serialized, *etc.*), the incorrect computation is re-executed in a non-speculative manner. If the rate of mis-speculation is low, such techniques can be leveraged to extract additional parallelism. There have been many different proposals, including large efforts on transactional memory [], lock elision [], thread level speculation and speculative multithreading [], for integrating speculative computation into programming languages and their associated runtimes []. The PIs have extensive experience with transactional memory [19], lightweight rollback methods [20], leveraging memoization to reduce re-computation costs [21, 22], and deterministic speculation [23]. We propose to explore a specialized speculation framework leveraging different speculation strategies, including speculation extracted by the programmer via programming language primitives, library level speculation, and compiler extracted speculation. The salient research challenges in applying this strategy are as follows: 1) identification the appropriate speculation model and discovering speculation points at compile time, 2) providing a speculative runtime specialized for jet clustering and capable of realizing user,

library, and compiler injected speculation, and 3) exploring new and specialized lightweight validation and re-execution mechanisms, including validation across multiple speculation strategies.

3.2.3 Smart Distribution

3.3 Summary

In summary, the problem of expanding LHC computing to the exascale is a difficult, but tractable one. This proposal investigates the possibility of applying cutting-edge computer science research to the real-world application of LHC data, specifically by reducing the computational time for k -nearest-neighbor-like numerical kernels. The investigators of this proposal have extensive experience in the various aspects of the problem, and the synergistic application of this experience is expected to attain considerable improvements in this avenue.

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