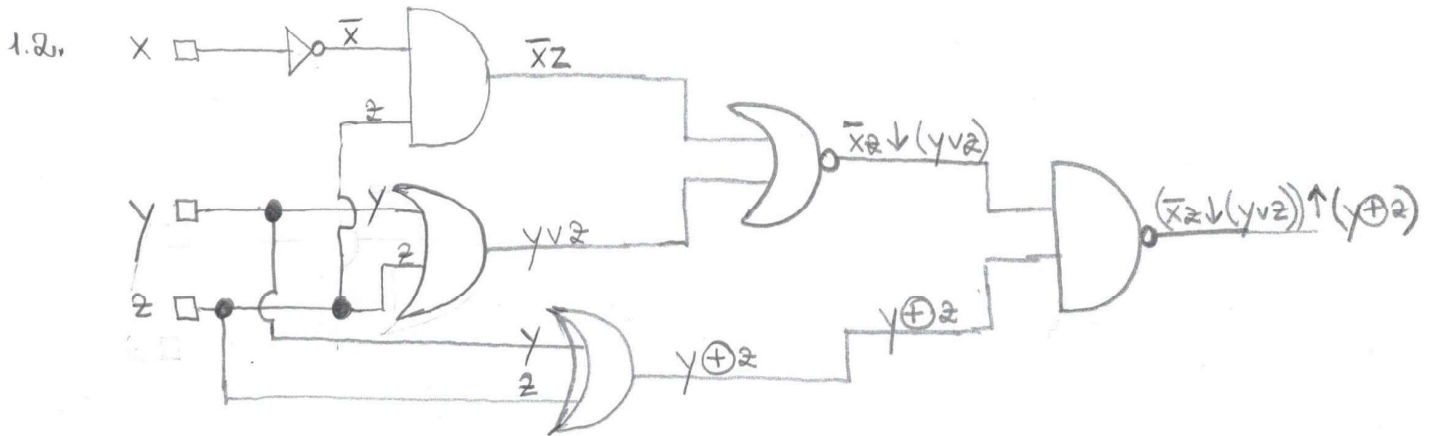


EX 1 LOGIC CIRCUITS

Write the corresponding Boolean function associated to the following logic circuit, then simplify it and ~~write~~ draw a simplified equivalent circuit using only basic gates.

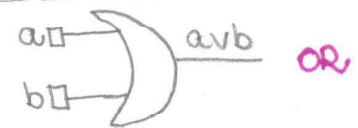
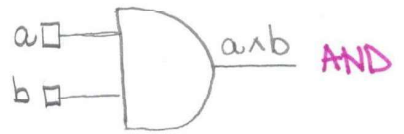


THEORETICAL RESULT

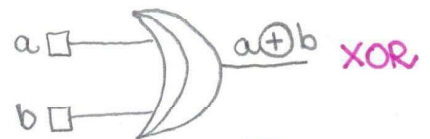
→ to derive the Boolean expression for a given logic circuit, we begin with the leftmost inputs and work towards the final output, writing the expression for each gate

$$\begin{aligned}
 f(x, y, z) &= (\bar{x}z \downarrow (y \downarrow z)) \uparrow (y \oplus z) \\
 &\stackrel{\text{replace } \uparrow}{=} ((\bar{x}z \downarrow (y \downarrow z)) \wedge (y \oplus z)) \\
 &\stackrel{\text{replace } \downarrow}{=} (\overline{\bar{x}z \vee (y \vee z)}) \wedge (y \oplus z) \\
 &\stackrel{\text{replace } \oplus}{=} \overline{\bar{x}z \vee y \vee z} \wedge (\bar{y}z \vee y\bar{z}) \\
 &\stackrel{\text{absorption: } uv(u \vee v) \text{ with } u=z, v=\bar{x}}{=} \overline{y \vee z} \wedge (\bar{y}z \vee y\bar{z}) \\
 &\stackrel{\text{deMorgan}}{=} \overline{y \vee z} \vee \overline{\bar{y}z \vee y\bar{z}} \\
 &\stackrel{\text{deMorgan}}{=} \overline{y \vee z} \vee (\overline{\bar{y}} \wedge \bar{z} \wedge \overline{y \wedge \bar{z}}) \\
 &= \overline{y \vee z} \vee ((\bar{\bar{y}} \vee \bar{z}) \wedge (\bar{y} \vee \bar{\bar{z}})) \\
 &= \overline{y \vee z} \vee ((y \vee \bar{z}) \wedge (\bar{y} \vee z)) \\
 &= \overline{y \vee z} \vee ((y \wedge \bar{y}) \vee (y \wedge z) \vee (\bar{z} \wedge \bar{y}) \vee (\bar{z} \wedge z)) \\
 &= \overline{y \vee z} \vee (y \wedge z) \vee (\bar{z} \wedge \bar{y}) \\
 &\stackrel{\text{absorption}}{=} \overline{y \vee z} \vee (y \wedge z) \vee (\bar{z} \wedge \bar{y})
 \end{aligned}$$

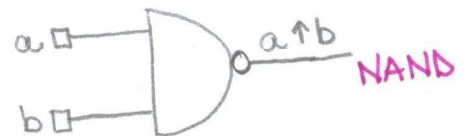
BASIC GATES



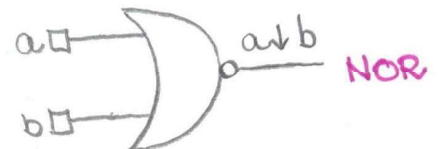
DERIVED GATES



$$a \oplus b = \bar{a}b \vee a\bar{b}$$



$$a \uparrow b = \overline{ab} = \overline{a} \vee \overline{b}$$



$$a \downarrow b = \overline{a \vee b} = \overline{a} \overline{b}$$