# Incremental Data Flow analysis using PRISM

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### Outline of the talk

- Incremental Data Flow analysis
  - Bit-vector frameworks
  - General frameworks
  - Method to reduce the size of affected region
- Overview of PRISM
- Incremental driver
  - Testing
- Limitations of old Solver
- Future work

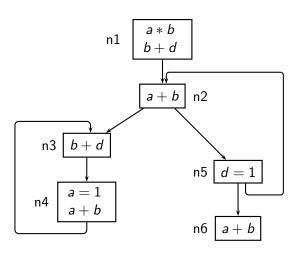
## Part I

Incremental Data Flow Analysis

# Why Incremental Analysis?

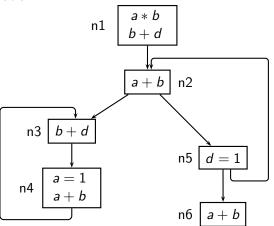
#### When program undergoes changes:

- Some or all computed data flow information become invalid
- Re-computation is required

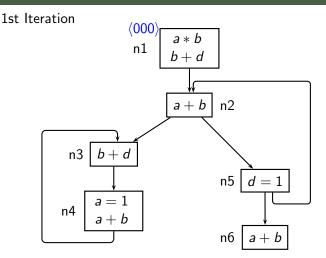


Bit Vector 
$$a * b \mid b + d \mid a + b$$

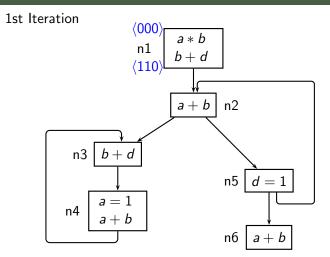
#### 1st Iteration



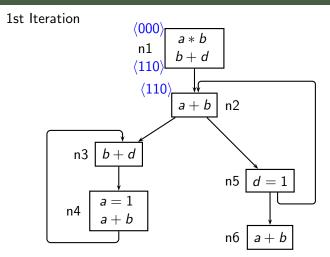
Bit Vector 
$$a * b \mid b + d \mid a + b$$



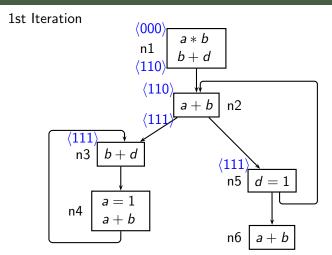
Bit Vector 
$$a * b \mid b + d \mid a + b$$



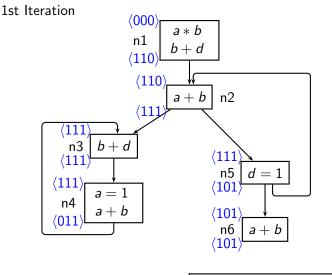
Bit Vector 
$$a * b \mid b + d \mid a + b$$



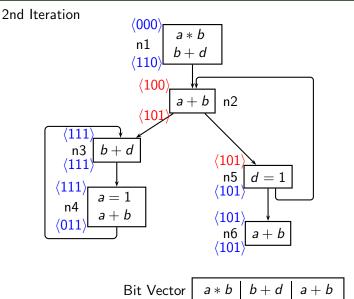
Bit Vector 
$$a * b \mid b + d \mid a + b$$

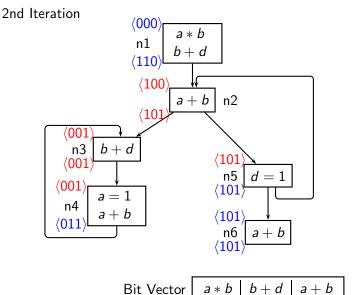


Bit Vector 
$$a * b \mid b + d \mid a + b$$

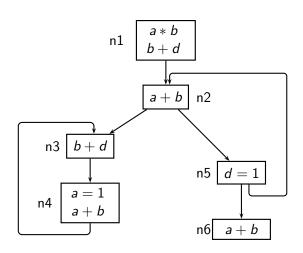


Bit Vector  $a*b \mid b+d \mid a+b$ 

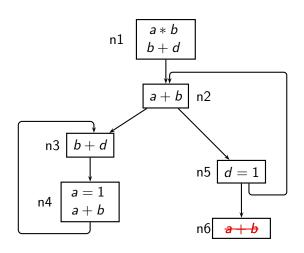




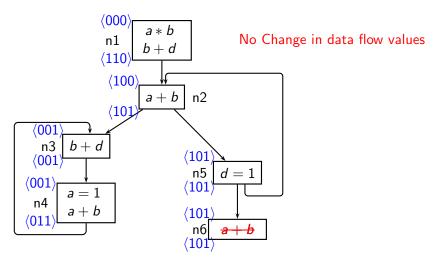
• It requires 3 iterations to converge



Bit Vector 
$$a*b b+d a+b$$



Bit Vector 
$$a*b b+d a+b$$



Bit Vector a\*b b+d a+b

- Recomputing the values from the scratch is very inefficient
- Need an incremental analysis:
  - modifies only affected data flow information
  - more cost effective then exhaustive analysis

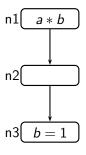
## Part II

Incremental Analysis in Bit-vector Framework

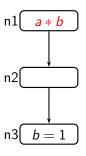
#### Flow functions in bit-vector frameworks

- Possible flow functions:
  - Raise : Result is always top
  - Lower: Result is always bottom
  - Propagate : Propagates the value from one program point to another

## **Available Expression Analysis**



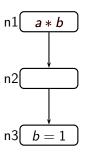
#### **Available Expression Analysis**



#### **Raise Function**

$$\begin{aligned} &\mathsf{Gen}_1 = 1 \\ &\mathsf{Kill}_1 = 0 \\ &\mathsf{IN}_1 = 0 \\ &\mathsf{OUT}_1 = &\mathsf{Gen}_1 \cup \left(\mathsf{IN}_1\text{-}\mathsf{Kill}_1\right) = 1 \end{aligned}$$

#### **Available Expression Analysis**

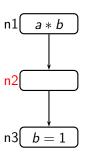


#### **Raise Function**

$$\begin{aligned} &\mathsf{Gen}_1 = 1 \\ &\mathsf{Kill}_1 = 0 \\ &\mathsf{IN}_1 = 0 \\ &\mathsf{OUT}_1 = &\mathsf{Gen}_1 \cup \left(\mathsf{IN}_1\text{-}\mathsf{Kill}_1\right) = 1 \end{aligned}$$

Result is always top

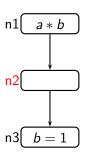
## **Available Expression Analysis**



## **Propagate Function**

$$\begin{aligned} &\mathsf{Gen}_2 = 0 \\ &\mathsf{Kill}_2 = 0 \\ &\mathsf{IN}_2 = 1 \\ &\mathsf{OUT}_2 = &\mathsf{Gen}_2 \cup \left(\mathsf{IN}_2 \text{-}\mathsf{Kill}_2\right) = \mathsf{IN}_2 = 1 \end{aligned}$$

#### **Available Expression Analysis**

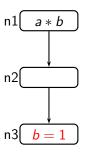


## **Propagate Function**

$$\begin{aligned} &\mathsf{Gen}_2 = 0 \\ &\mathsf{Kill}_2 = 0 \\ &\mathsf{IN}_2 = 1 \\ &\mathsf{OUT}_2 = &\mathsf{Gen}_2 \cup \left(\mathsf{IN}_2 \text{-}\mathsf{Kill}_2\right) = \mathsf{IN}_2 = 1 \end{aligned}$$

Propagates the value at IN to OUT

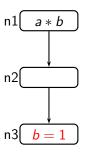
#### **Available Expression Analysis**



#### **Lower Function**

$$\begin{aligned} &\mathsf{Gen}_3 = 0 \\ &\mathsf{Kill}_3 = 1 \\ &\mathsf{IN}_3 = 1 \\ &\mathsf{OUT}_3 = &\mathsf{Gen}_3 \cup \left(\mathsf{IN}_3\text{-}\mathsf{Kill}_3\right) = 0 \end{aligned}$$

## **Available Expression Analysis**



#### **Lower Function**

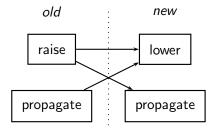
$$\begin{aligned} &\mathsf{Gen}_3 = 0 \\ &\mathsf{Kill}_3 = 1 \\ &\mathsf{IN}_3 = 1 \\ &\mathsf{OUT}_3 = &\mathsf{Gen}_3 \cup \left(\mathsf{IN}_3\text{-}\mathsf{Kill}_3\right) = 0 \end{aligned}$$

Result is always bottom

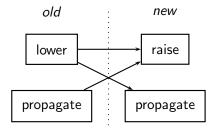
# Changes in Bit-vector Frameworks

- As a consequence of some change in a node, some data flow values may:
  - change from top to bottom
  - change from bottom to top
  - remain same

# Possible Changes in Flow Functions for Top to Bottom Change

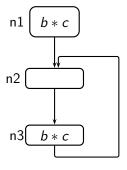


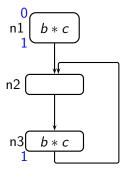
# Possible Changes in Flow Functions for Top to Bottom Change

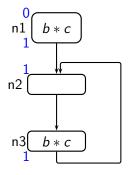


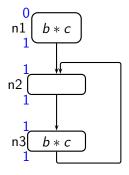
# Handling Top to Bottom Change

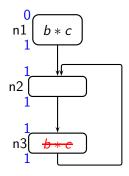
- Top value is an intermediate value until data flow analysis is completed
- Whenever there is top to bottom change, the changes can be propagated directly to its neighbouring nodes



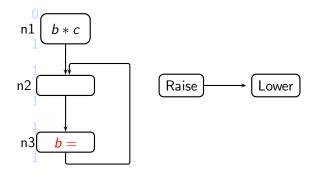




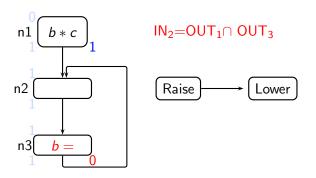




### Top to Bottom change

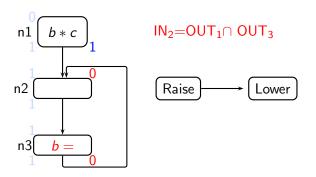


#### Top to Bottom change



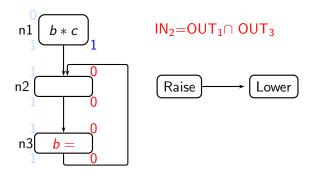
Directly Propagate the change to its neighbour

#### Top to Bottom change



Directly Propagate the change to its neighbour

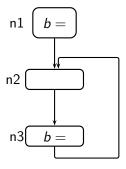
#### Top to Bottom change

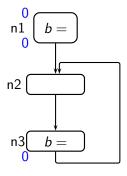


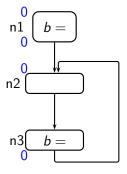
Directly Propagate the change to its neighbour

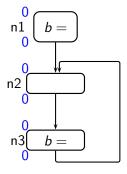
## Handling Bottom to Top Change

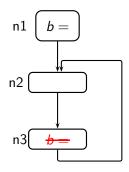
- Bottom value is a final value even during analysis
- Whenever there is bottom to top change, we cannot directly propagate the changes to its neighbouring nodes
- Need some more processing



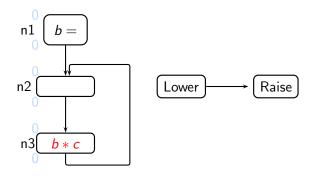




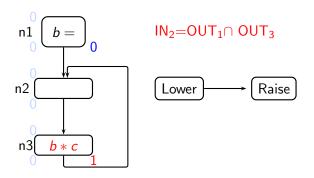




### Bottom to Top change

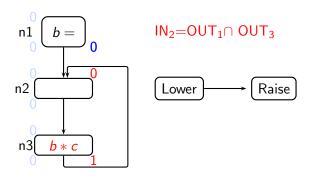


#### Bottom to Top change



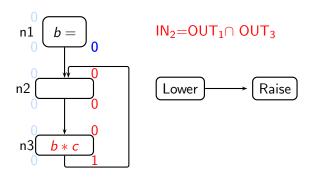
Cannot propagate the change to its neighbouring nodes

#### Bottom to Top change



Cannot propagate the change to its neighbouring nodes

#### Bottom to Top change



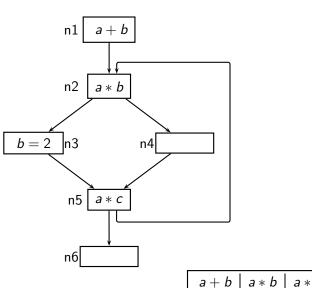
Cannot propagate the change to its neighbouring nodes

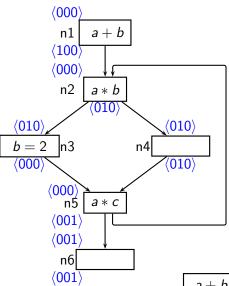
Need some more processing

• Steps to incorporate bottom to top change:

- Steps to incorporate bottom to top change:
  - Identify the data flow values which may become top

- Steps to incorporate bottom to top change:
  - Identify the data flow values which may become top
  - Find out the data flow values which must remain bottom due to the effect of some other property

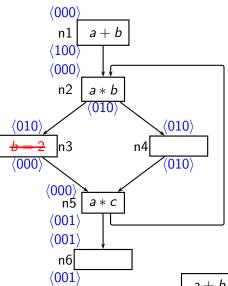




### Initial Available Expression Analysis

	a + b		a*b		a * c	
Node	In	Out	In	Out	In	Out
1.	0	1	0	0	0	0
2.	0	0	0	1	0	0
3.	0	0	1	0	0	0
4.	0	0	1	1	0	0
5.	0	0	0	0	0	1
6.	0	0	0	0	1	1

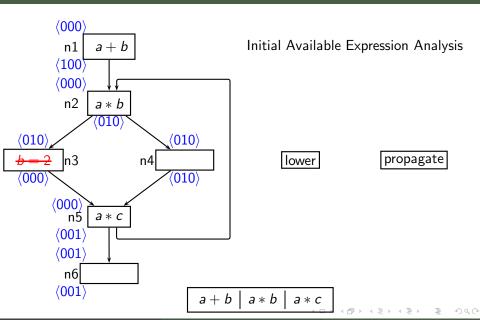
 $a+b \mid a*b \mid a*c$ 

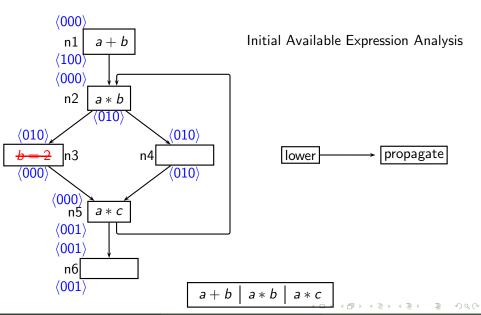


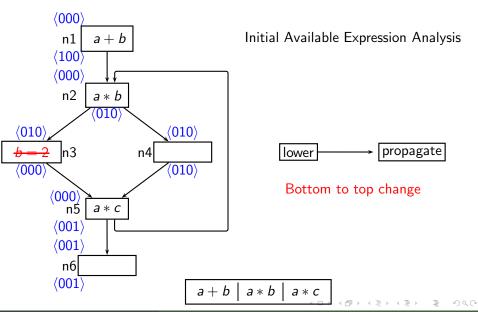
### Initial Available Expression Analysis

	a+b		a * b		a * c	
Node	In	Out	In	Out	In	Out
1.	0	1	0	0	0	0
2.	0	0	0	1	0	0
3.	0	0	1	0	0	0
4.	0	0	1	1	0	0
5.	0	0	0	0	0	1
6.	0	0	0	0	1	1

 $a+b \mid a*b \mid a*c$ 

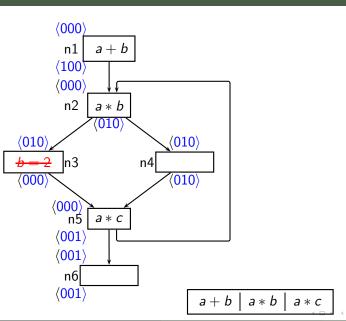


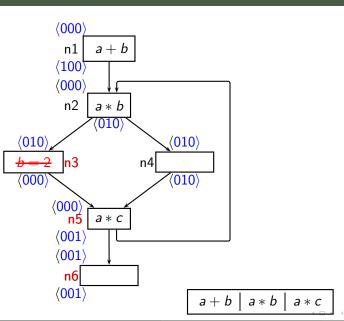


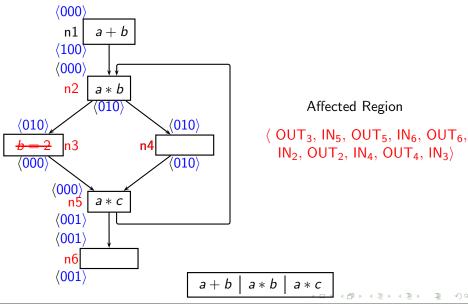


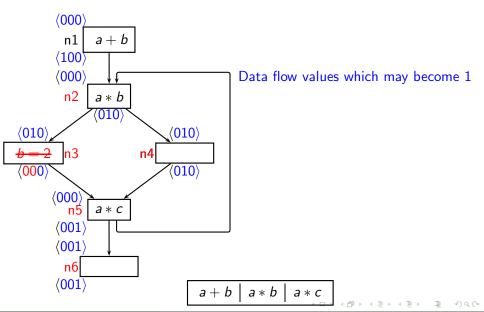
 The data flow values which were 0 and may become 1 due to this change

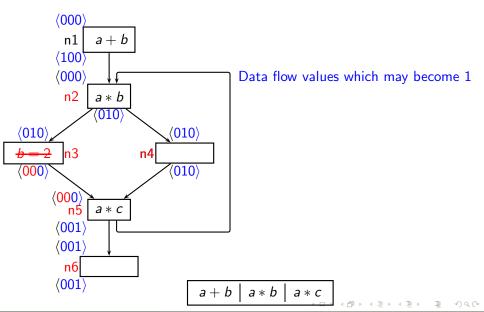
- The data flow values which were 0 and may become 1 due to this change
  - Affected region

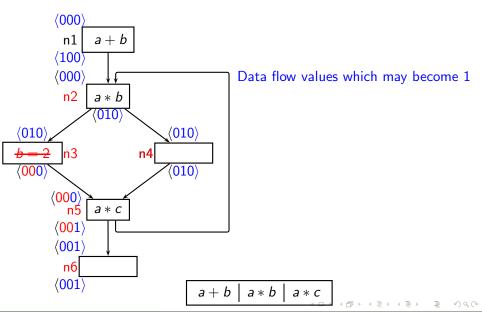


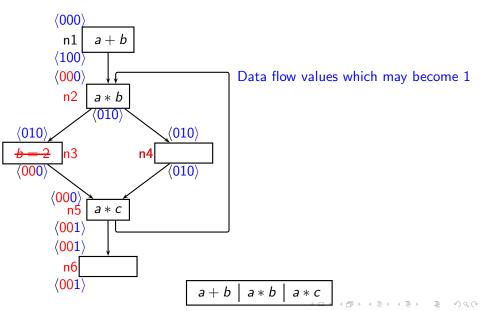


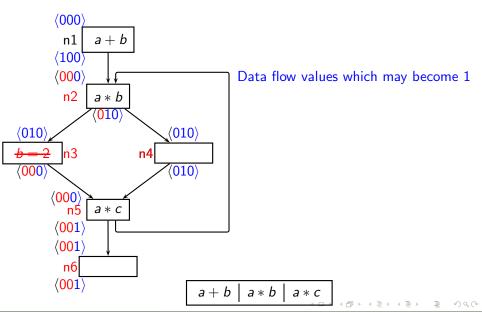


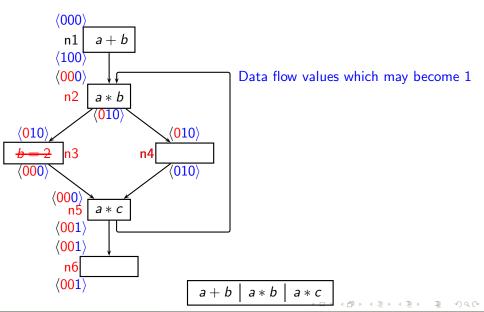


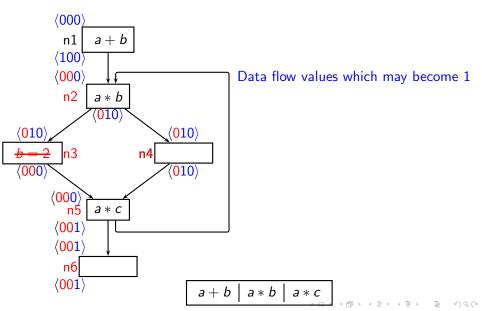


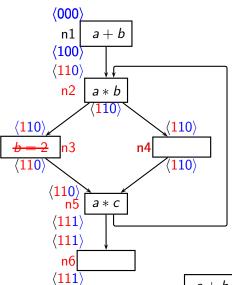












### Data flow values which may become 1

	a + b		a*b		a * c	
Node	In	Out	In	Out	In	Out
1.						
2.	1	1	1			
3.	1	1		1		
4.	1	1				
5.	1	1	1	1		
6.	1	1	1	1		

a \* b

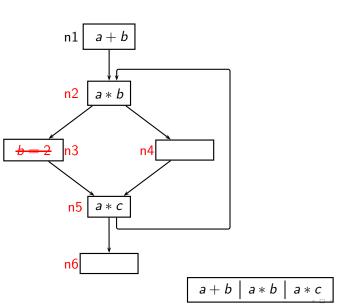
a \* c

• Find out the data flow values which must remain bottom due to the effect of some other property

- Find out the data flow values which must remain bottom due to the effect of some other property
  - Initialize affected region to top.

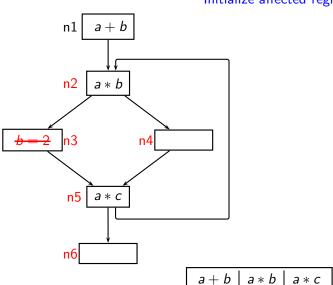
- Find out the data flow values which must remain bottom due to the effect of some other property
  - Initialize affected region to top.
  - Identify boundary nodes

- Find out the data flow values which must remain bottom due to the effect of some other property
  - Initialize affected region to top.
  - Identify boundary nodes
  - Compute values at boundary nodes and propagate them

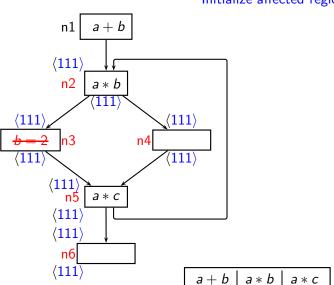


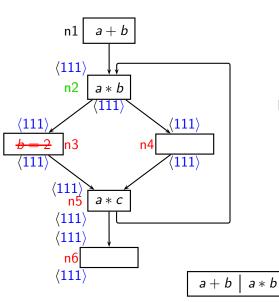
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### Initialize affected region to top



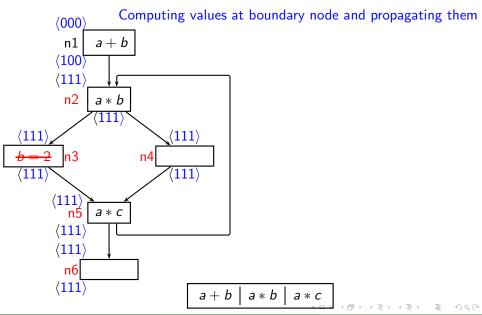
#### Initialize affected region to top

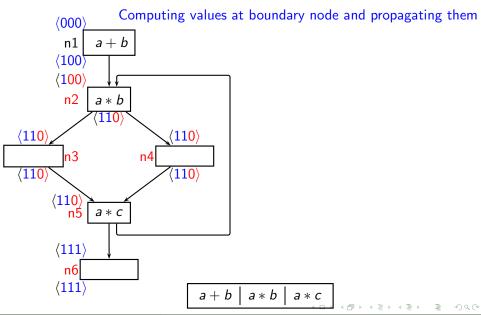


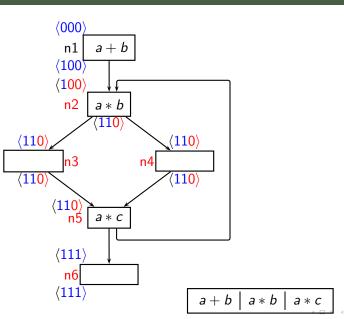


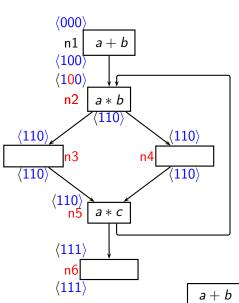
Node 2 is Boundary node

a \* c







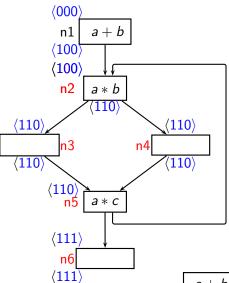


#### Values which must remain 0

	a+b		a * b		a * c	
Node	In	Out	In	Out	In	Out
1.						
2.			0			
3.						
4.						
5.						
6.						

a \* b

a \* c



#### Final values

		a+b		a * b		a * c	
1	lode	In	Out	In	Out	In	Out
	1.	0	1	0	0	0	0
	2.	1	1	0	1	0	0
	3.	1	1	1	1	0	0
	4.	1	1	1	1	0	0
	5.	1	1	1	1	0	1
	6.	1	1	1	1	1	1

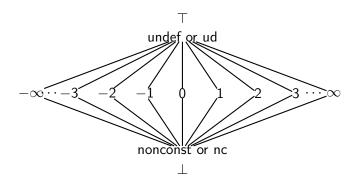
## Part III

Incremental Analysis in General Frameworks

## Incremental Analysis in General Frameworks

Consider constant propagation analysis

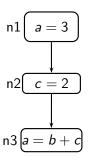
# Component lattice for Constant Propagation



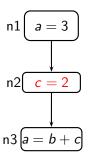
#### Flow functions

- Possible flow functions
  - Top: Similar to raise function
  - Bottom : Similar to lower function
  - Constant : Always produce a constant value
  - Side level: Result depends on the operands of the expression

## Constant functions

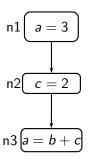


## Constant functions

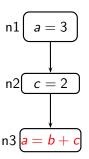


**Produces Constant values** 

## Side level functions

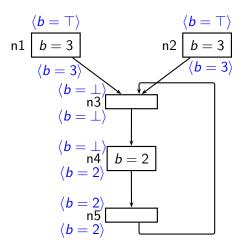


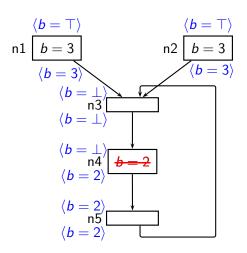
## Side level functions



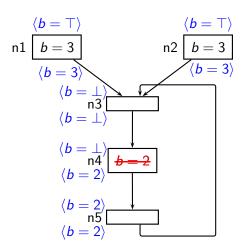
Result depends on the operands

- Unlike bit-vector frameworks, when there is a change to bottom:
  - we cannot propagate the change to its neighbouring nodes

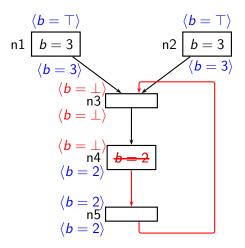




### Change to bottom



We cannot propagate the change



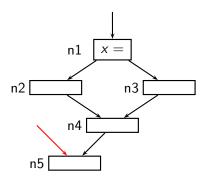
- Unlike bit-vector frameworks, we may need to create an affected region even if there is a change to bottom.
- Solution is to create affected region for all kind of changes.

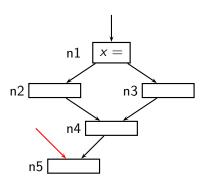
## Part IV

- Based on the observation that some boundary nodes can be characterized by the concept of **Dominance Frontier**.
- Eliminate some boundary nodes from being included in the affected region.

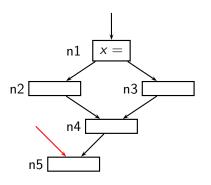
- Let n and m be nodes in CFG. The node n is said to dominate m  $(n \ge m)$ , if every path from **Start** to m passes through n.
- If  $n \neq m$ , then n strictly dominates m, denoted as n > m
- Dominance Frontier:

$$df(n) = \{m \mid \exists p \in pred(m), (n \ge p \text{ and } n \not> m)\}$$
 (1)



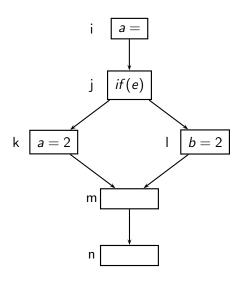


n1 dominates n4

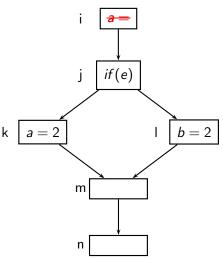


n5 is a dominance frontier of n1

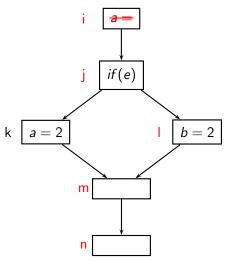
- All Dominance frontier are boundary nodes.
- Vice-versa is not true.



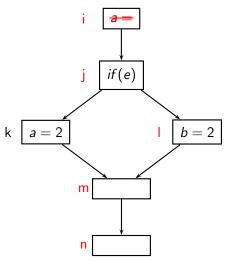
### Affected region: $\langle i, j, l, m, n \rangle$



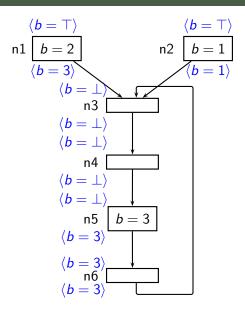
### Affected region: $\langle i, j, l, m, n \rangle$

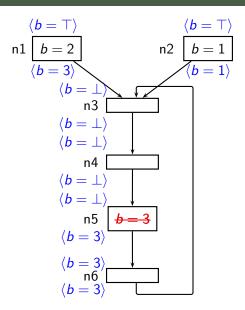


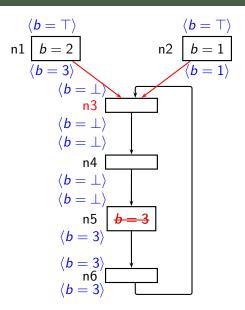
 $\it m$  is a boundary node and is dominated by  $\it i$ 

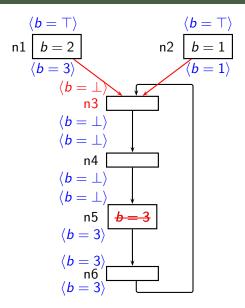


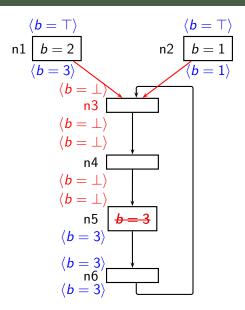
• Possible removal candidates is a dominance frontier of changed node.

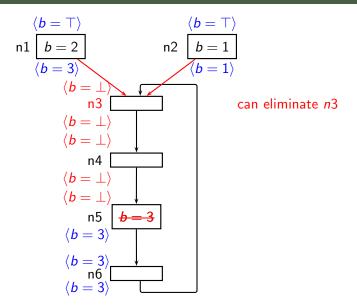












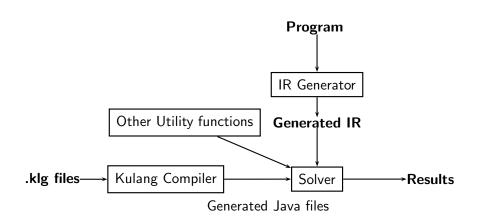
### Part V

# Overview of PRISM

#### **PRISM**

 PRISM is a program analyzer generator developed by TATA Research Development and Design Center (TRDDC)

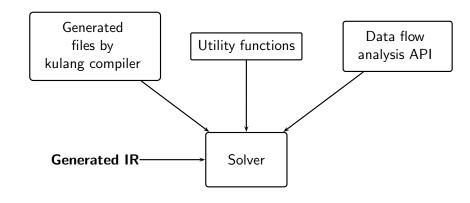
#### Old Architecture of PRISM



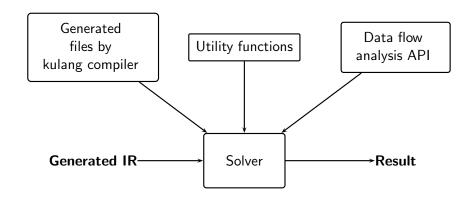
## Architecture of Analyzer Generator

Solver

## Architecture of Analyzer Generator



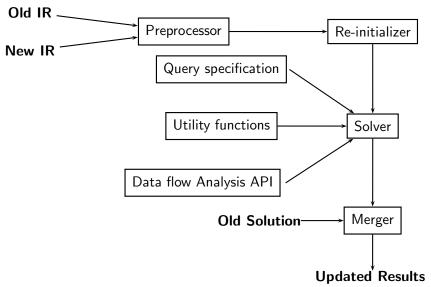
## Architecture of Analyzer Generator



### Part VI

Incremental Solver

### Architecture of Incremental PRISM



### Assumptions

- Pointer information will remain same.
- No change in the context information.
- Declaration of variable haven't change.
- No structural change in the graph.
- A name can refer to a single variable in a program at any given program point.
- past information is stored flow sensitively.

# Part VII

Testing

# Test Case 1 : a is a global variable

n3 n4 n5 n6 n7 n8 n9 n13 n10 n11

### Test Case 2: a is a local variable

Test Case 3: passed as a parameter

### Test Case 4 : context sensitive incremental update

### Part VIII

Thank You!