

By Rana Ramiulzi  
 \$gp 1000 8000<sub>16</sub>

Executable File Header			
	Text Size	400	# total combined text size
	Data Size	80	# total combined data size
	Address		
Text Segment	<u>0040 0000</u> <sub>16</sub> (the memory address for Procedure A's lw instruction)	lw \$a0, X(\$gp) X = <u>8000</u> <sub>16</sub>	# where X is the offset relative to the global pointer register to get to X in the data segment
	<u>0040 0004</u> <sub>16</sub> (the memory address for Procedure A's jal instruction)	jal B <u>0040 0300</u> <sub>16</sub>	# where B is the (pseudo-direct) address of Procedure A in the text segment
	...		
	0040 0300 <sub>16</sub> (the memory address for Procedure B's lw instruction)	lw \$a1, Y(\$gp) Y = <u>8060</u> <sub>16</sub>	# where Y is the offset relative to the global pointer register to get to Y in the data segment
	0040 0304 <sub>16</sub> (the memory address for Procedure A's jal instruction)	jal A <u>0040 0000</u> <sub>16</sub>	# where A is the (pseudo-direct) address of Procedure B in the text segment
	...		
Data Segment	Address		
	<u>1000 0000</u> <sub>16</sub>	X	# the location of X in the data section
	<u>1000 0060</u> <sub>16</sub>	Y	# the location of Y in the data section

A

B