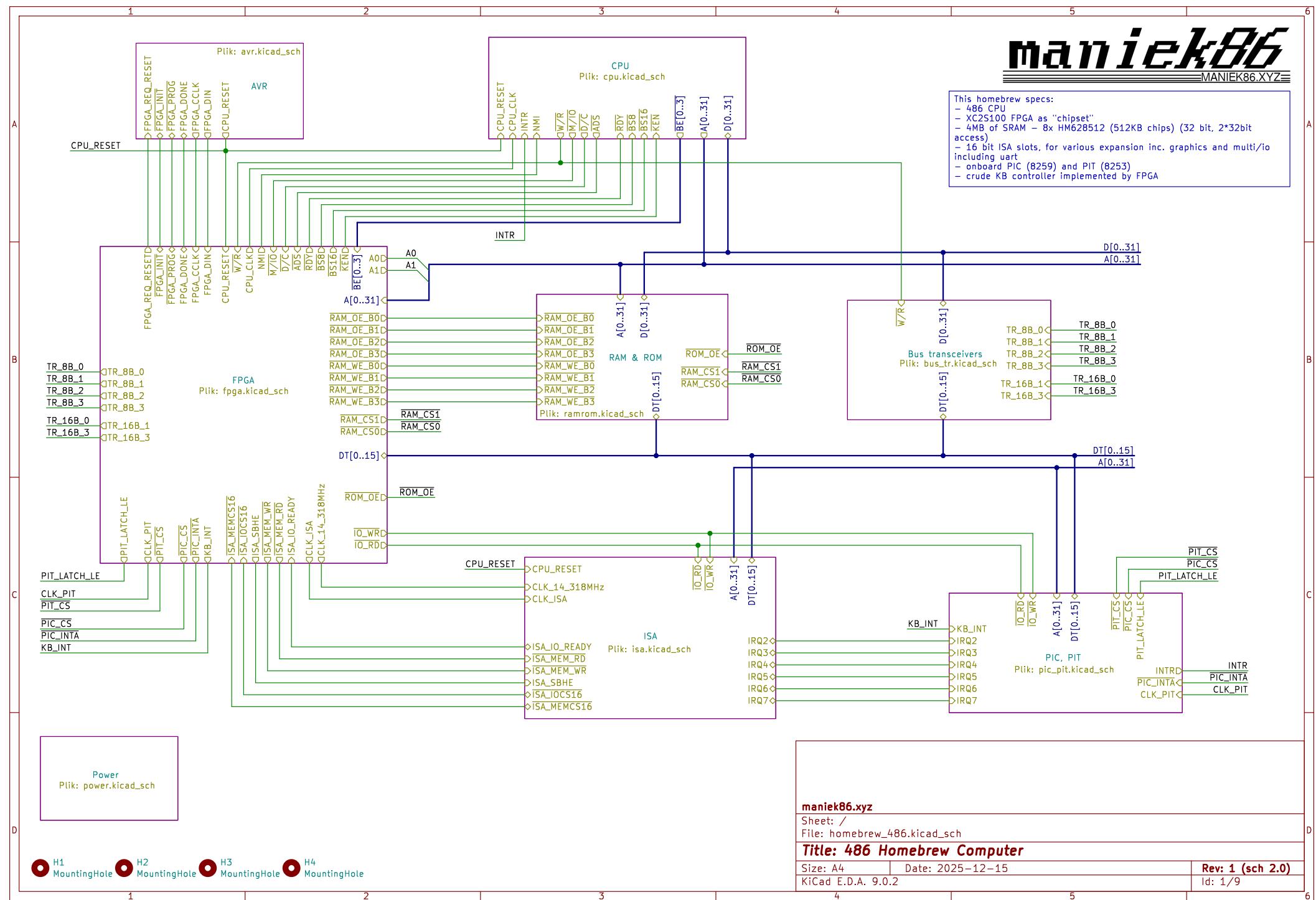
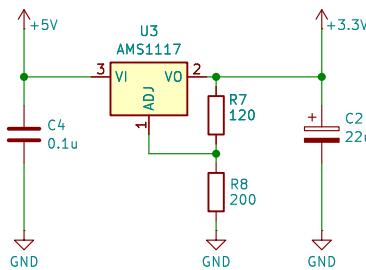
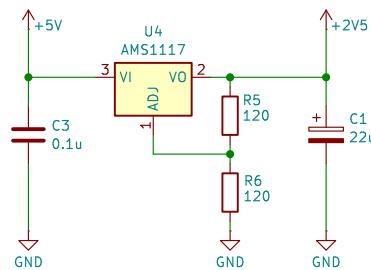
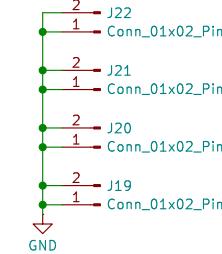
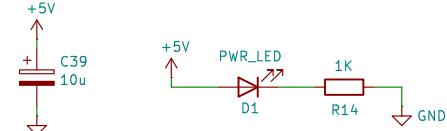
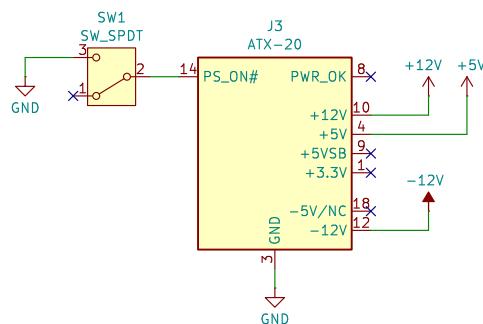


This homebrew specs:

- 486 CPU
- XC25100 FPGA as "chipset"
- 4MB of SRAM - 8x HM628512 (512KB chips) (32 bit, 2*32bit access)
- 16 bit ISA slots, for various expansion inc. graphics and multi/io including uart
- onboard PIC (8259) and PIT (8253)
- crude KB controller implemented by FPGA





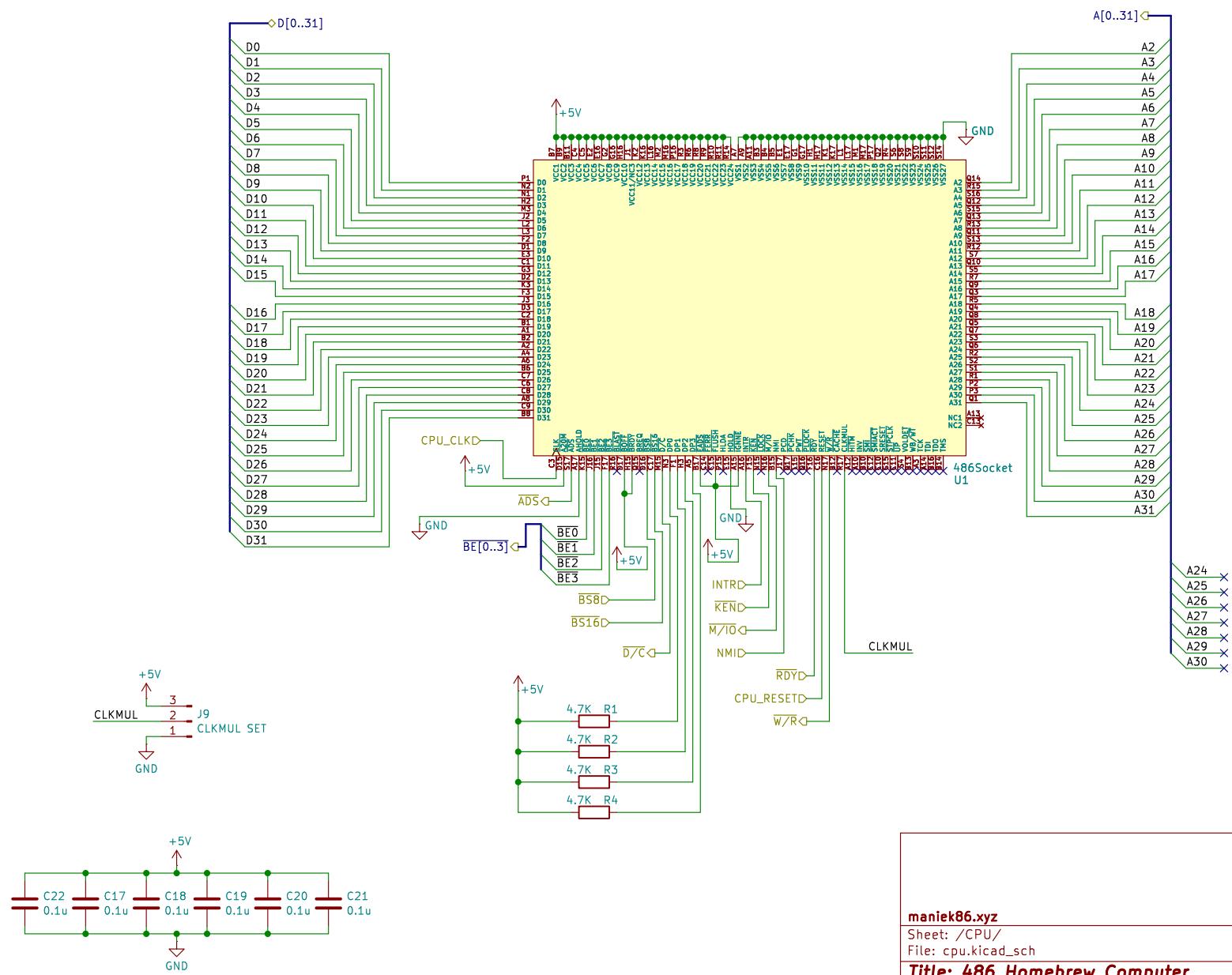
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Size: A4 Date: 2025-12-15
KiCad E.D.A. 9.0.2

Rev: 1 (sch 2.0)
Id: 2/9



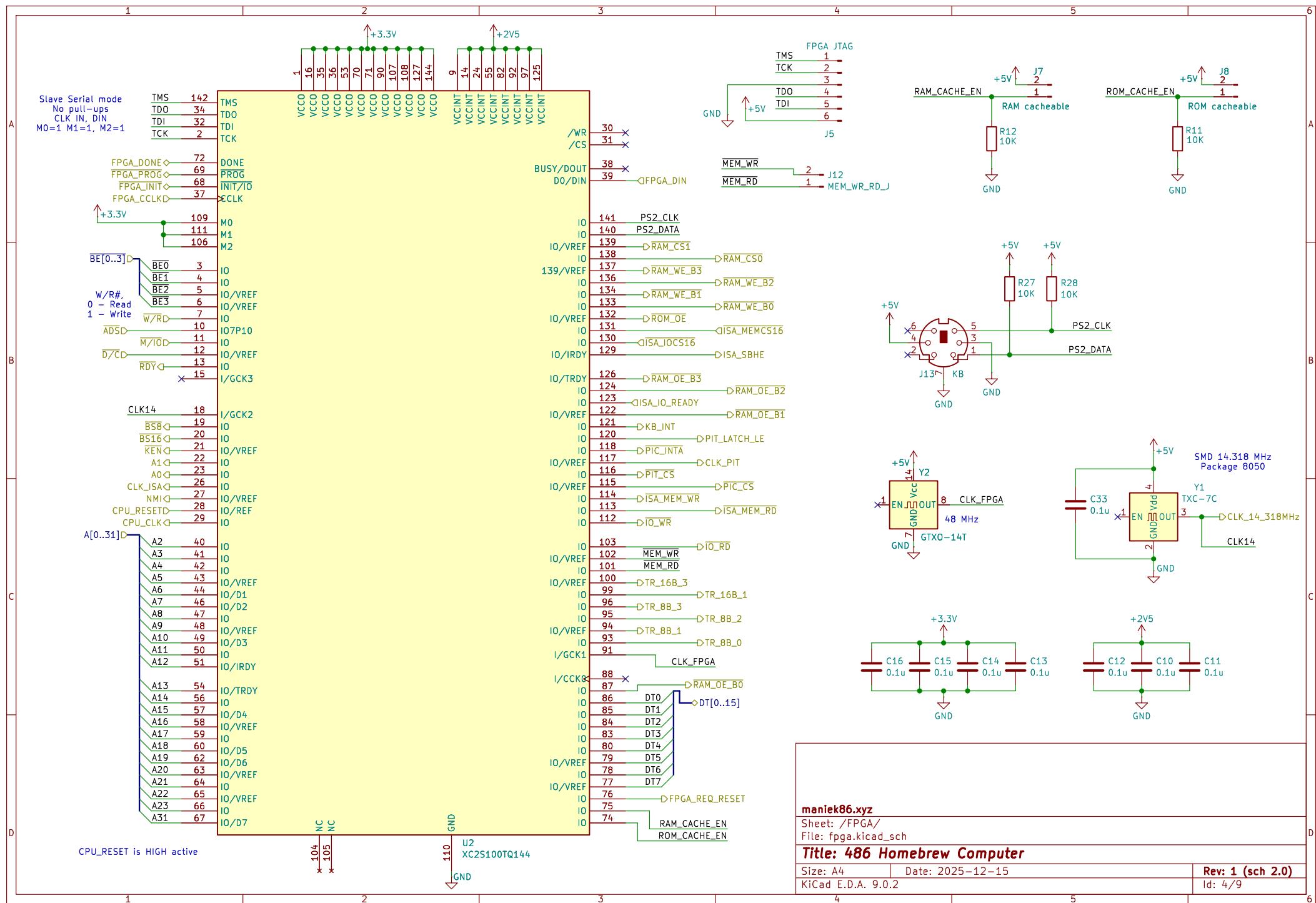
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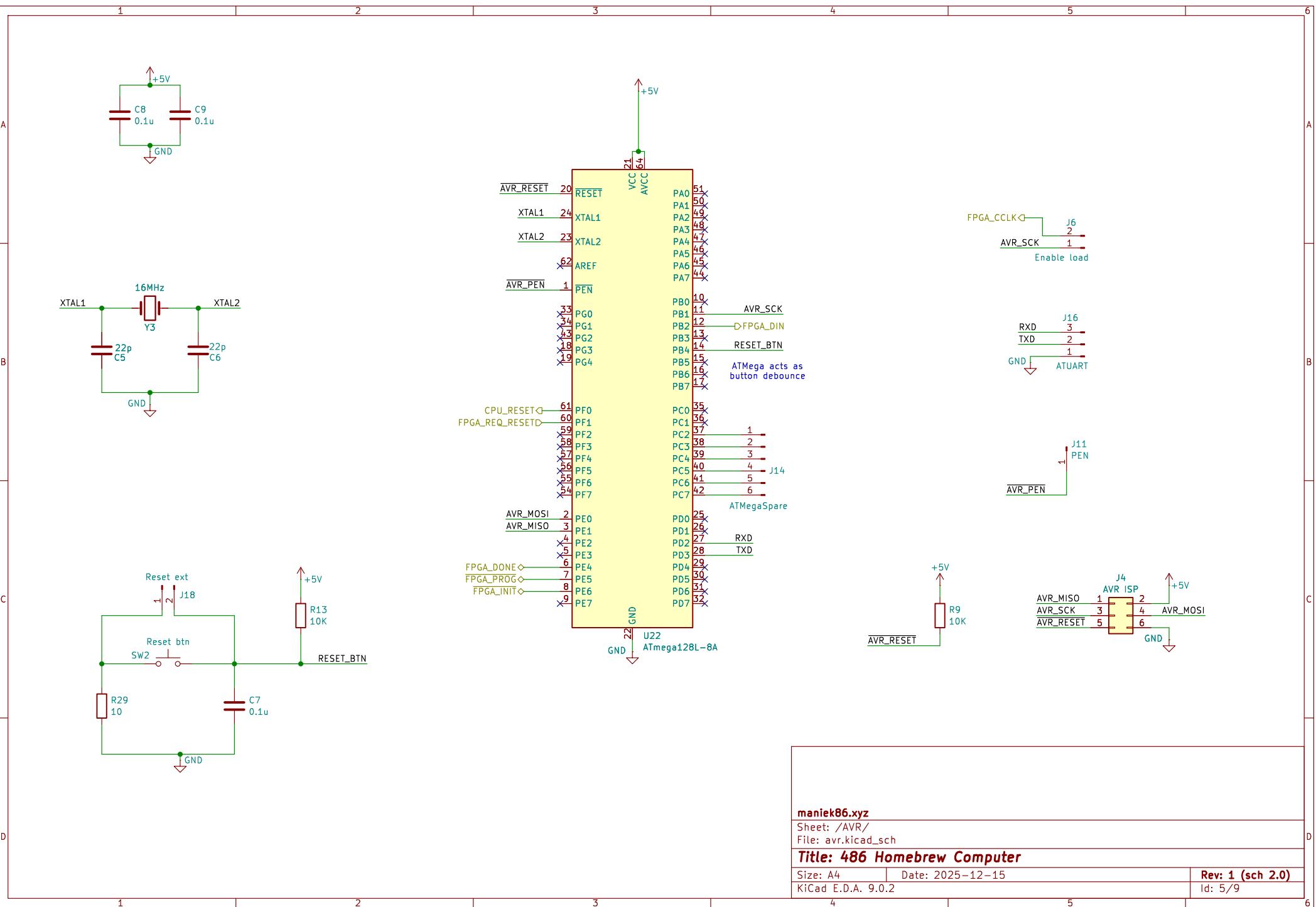
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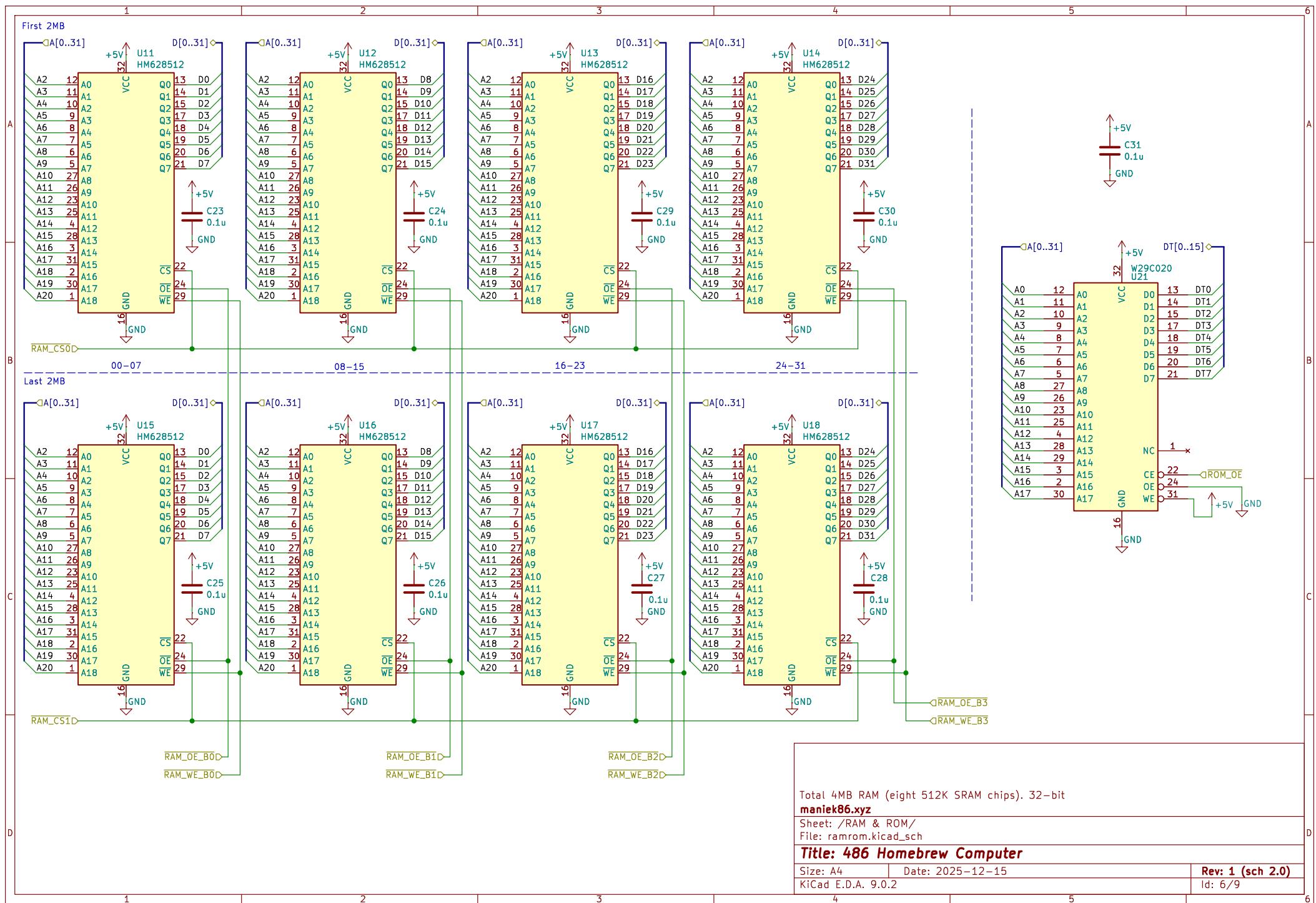
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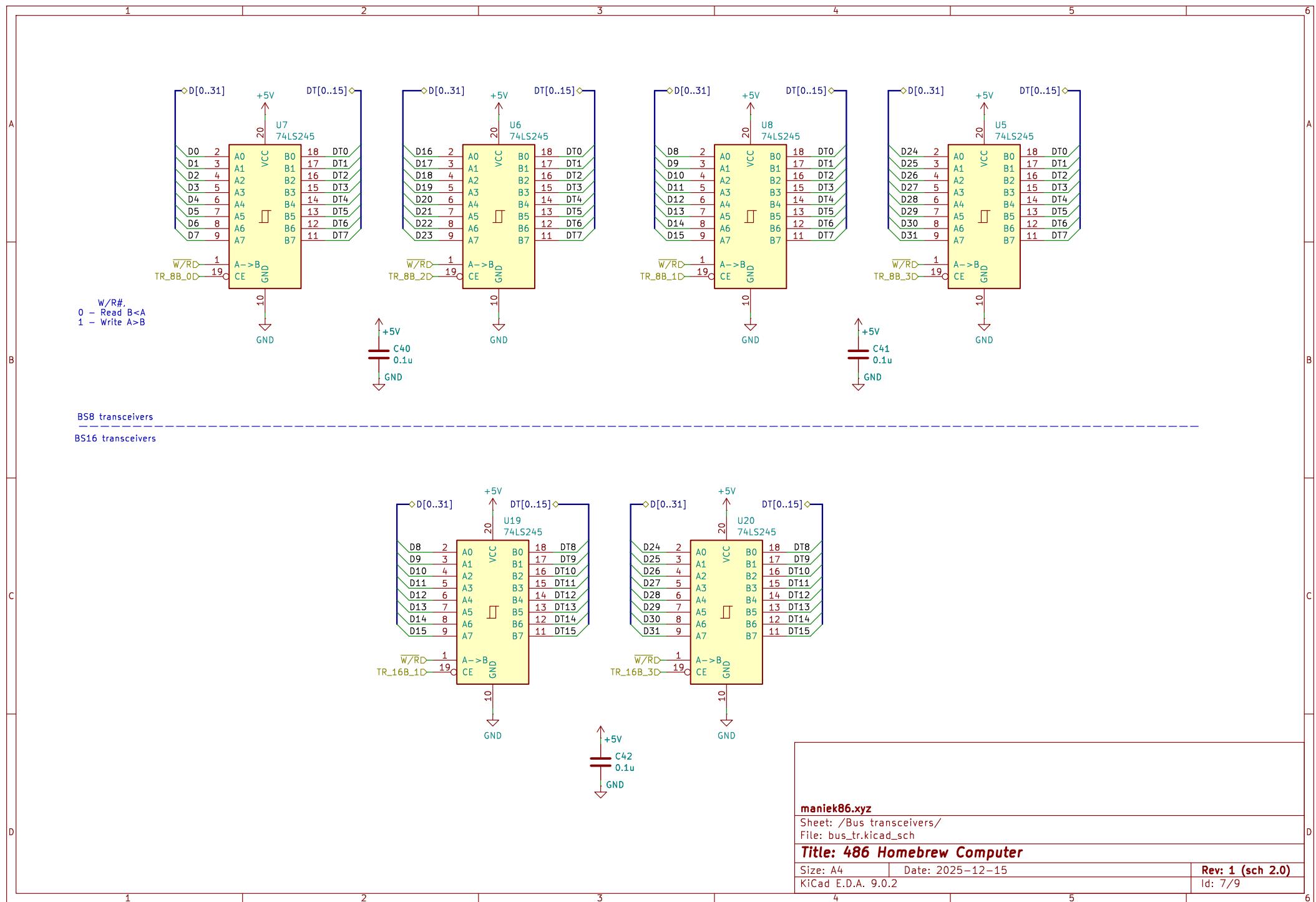
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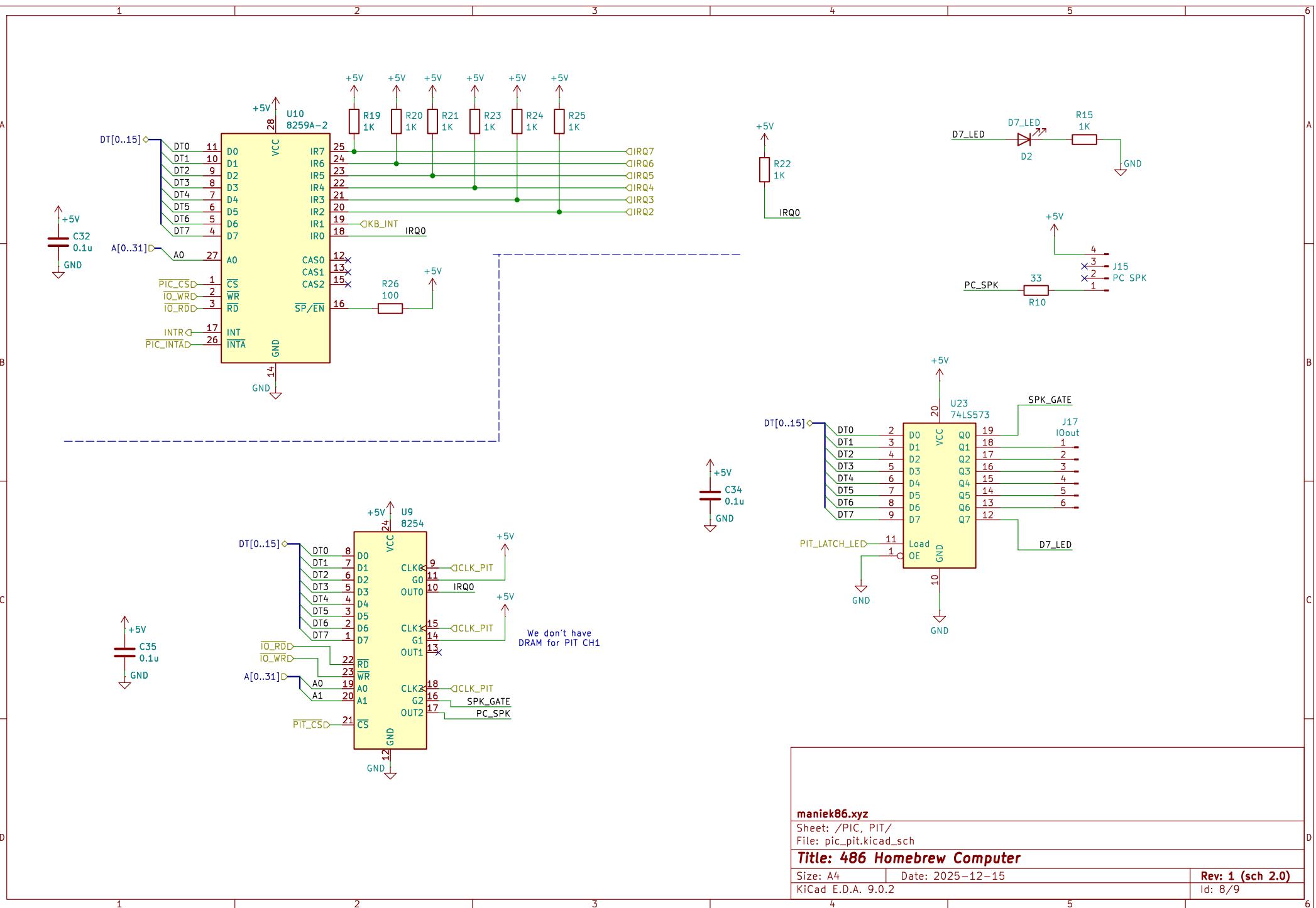
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Size: A4 Date: 2025-12-15
KiCad E.D.A. 9.0.2

Rev: 1 (sch 2.0)
Id: 8/9

