

PREAMP & TONE CONTROL

The circuit diagram illustrates a preamplifier and tone control system. It begins with an **AudioJack2 J1** input connected to a **10uF** capacitor (**C2**) and a **1000k** resistor (**R1**) to ground. The signal then passes through another **10uF** capacitor (**C1**) and a **2k** resistor (**R2**) to the non-inverting input (**V+**) of a **TL071** op-amp (**U1**). The op-amp is configured with a voltage divider (**RV2**, **20K**) and a **100uF** capacitor (**C4**) for biasing. The output of the op-amp is labeled **preamp_output**. This output is then fed into a tone control section, which includes a **47K** resistor (**R3**) and a **6.8nF** capacitor (**C3**) in parallel. The signal then passes through a **22K** resistor (**R4**) and a **47nF** capacitor (**C7**) to the **tone_control_output**. A **10nF** capacitor (**C6**) is also connected to ground. The op-amp is powered by **VCC** and **GND**.

POWER SUPPLY

[illegible]

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