



Devineni Venkata Ramana & Dr.Hima Sekhar  
**MIC College of Technology**

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)

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**AUTONOMOUS**

**NBA-Accredited**  
B.Tech (CSE/ECE/ME)

Accredited by  
**NAAC**  
With Grade **A**

## **ANNUAL REPORT OF EXAMINATIONS FOR A.Y: 2018-2019**

of

**DVR & Dr. HS MIC College of Technology**  
**(Autonomous)**

**Kanchikacherla-521180**





Dr. Devanand Venkata Ramana & Dr. Hima Sekhar  
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### **Annual Report of Examinations for 2018-2019**

#### **I. Students performance in the semester End-examinations:**

a) In the Odd semester end examinations total 2977 students appeared for the examination of UG & PG under autonomous, out of 2977, 1661 students passed in the Odd semester end Examinations. The overall pass percentage is 55.8.

#### **B.Tech. I Sem (2018 Admitted Batch.)**

Examination Starting Date: 24-12-2018

Examination Ending Date: 04-01-2019

Results Declaration Date: 11-01-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	30	14	46.7
2	Electrical and Electronics Engineering	20	11	55.0
3	Mechanical Engineering	45	27	60.0
4	Electronics and Communication Engineering	178	129	72.5
5	Computer Science and Engineering	178	139	78.1
6	Information Technology	57	36	63.2
<b>Overall</b>		<b>508</b>	<b>356</b>	<b>70.1</b>

#### **B.Tech. III Sem (2017 Admitted Batch.) II B.Tech. I Sem Regular / Supple. Oct/Nov – 2018**

Examination Starting Date: 26-10-2018

Examination Ending Date: 08-11-2018

Results Declaration Date: 26-12-2018

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	105	44	41.9
2	Electrical and Electronics Engineering	120	33	27.5
3	Mechanical Engineering	178	49	27.5
4	Electronics and Communication Engineering	199	78	39.2
5	Computer Science and Engineering	172	96	55.8
6	Information Technology	57	27	47.4
<b>Overall</b>		<b>831</b>	<b>327</b>	<b>39.4</b>

#### **B.Tech. V Sem (2016 Admitted Batch.) III B.Tech. I Sem Regular / Supple. Oct/Nov – 2018**

Examination Starting Date: 27-10-2018

Examination Ending Date: 06-11-2018

Results Declaration Date: 15-12-2018

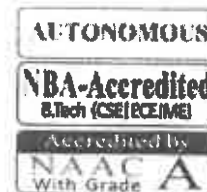
S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	94	50	53.2
2	Electrical and Electronics Engineering	100	46	46.0
3	Mechanical Engineering	160	58	36.3
4	Electronics and Communication Engineering	168	101	60.1
5	Computer Science and Engineering	163	113	69.3
<b>Overall</b>		<b>685</b>	<b>368</b>	<b>53.7</b>



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**B.Tech. VII Sem (2015 Admitted Batch.) IV B.Tech. I Sem Regular / Supple. Oct/Nov – 2018**

Examination Starting Date: 26-10-2018

Examination Ending Date: 08-11-2018

Results Declaration Date: 03-12-2018

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	107	69	64.5
2	Electrical and Electronics Engineering	107	78	72.9
3	Mechanical Engineering	153	67	43.8
4	Electronics and Communication Engineering	201	110	54.7
5	Computer Science and Engineering	164	125	76.2
Overall		732	449	61.3

**M.B.A. I Sem (2018 Admitted Batch.)**

Examination Starting Date: 24-12-2018

Examination Ending Date: 05-01-2019

Results Declaration Date: 11-01-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Business Administration	61	42	68.9

**M.B.A. III Sem (2017 Admitted Batch.) MBA III Sem Regular / Supple. Dec 2018**

Examination Starting Date: 11-12-2018

Examination Ending Date: 24-12-2018

Results Declaration Date: 23-02-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Business Administration	108	90	83.3

**M.C.A. III Sem (2017 Admitted Batch.) MCA III Sem Regular / Supple. Nov 2018**

Examination Starting Date: 22-11-2018

Examination Ending Date: 28-11-2018

Results Declaration Date: 17-01-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Computer Application	34	17	50.0

**M.C.A. V Sem (2016 Admitted Batch.) MCA V Sem Regular / Supple. Oct 2018**

Examination Starting Date: 26-10-2018

Examination Ending Date: 05-11-2018

Results Declaration Date: 01-12-2018

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Computer Application	13	11	84.6

**M.Tech. I Sem (2018 Admitted Batch.)**

Examination Starting Date: 11-01-2019

Examination Ending Date: 25-01-2019

Results Declaration Date: 06-02-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Machine Design	1	1	100
2	VLSI and Embedded Systems	1	0	00
Overall		2	1	50.0



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## M.Tech. III Sem (2017 Admitted Batch.) Seminar-I & Project

S. No.	Branch	Appeared	Passed	% of Pass
1	Machine Design	1	0	0
2	VLSI and Embedded Systems	2	0	0
Overall		3	0	0

b) In the Even semester end examinations total 2944 students appeared for the examination of UG & PG under autonomous, out of 2944, 1899 students passed in the Even semester end Examinations. The overall pass percentage is 64.5.

## B.Tech. II Sem (2018 Admitted Batch.)

Examination Starting Date: 10-06-2019

Examination Ending Date: 21-06-2019

Results Declaration Date: 05-07-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	28	10	35.7
2	Electrical and Electronics Engineering	20	12	60.0
3	Mechanical Engineering	45	25	55.6
4	Electronics and Communication Engineering	174	127	73.0
5	Computer Science and Engineering	177	131	74.0
6	Information Technology	54	30	55.6
Overall		498	335	67.3

## B.Tech. IV Sem (2017 Admitted Batch.) II B.Tech. II Sem Regular / Supple. April 2019

Examination Starting Date: 13-04-2019

Examination Ending Date: 08-05-2019

Results Declaration Date: 19-06-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	103	39	37.9
2	Electrical and Electronics Engineering	116	54	46.6
3	Mechanical Engineering	175	50	28.6
4	Electronics and Communication Engineering	197	112	56.9
5	Computer Science and Engineering	171	102	59.6
6	Information Technology	57	30	52.6
Overall		819	387	47.3

## B.Tech. VI Sem (2016 Admitted Batch.) III B.Tech. II Sem Regular / Supple. April 2019

Examination Starting Date: 15-04-2019

Examination Ending Date: 07-05-2019

Results Declaration Date: 07-06-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	94	50	53.2
2	Electrical and Electronics Engineering	97	62	63.9
3	Mechanical Engineering	159	92	57.9
4	Electronics and Communication Engineering	168	88	52.4



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With Grade

5	Computer Science and Engineering	164	132	80.5
<b>Overall</b>		<b>682</b>	<b>424</b>	<b>62.2</b>

**B.Tech. VIII Sem (2015 Admitted Batch.) IV B.Tech. II Sem Regular / Supple. April / May 2019**

Examination Starting Date: 13-04-2019

Examination Ending Date: 30-04-2019

Results Declaration Date: 25-05-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Civil Engineering	107	76	71.0
2	Electrical and Electronics Engineering	104	88	84.6
3	Mechanical Engineering	150	115	76.7
4	Electronics and Communication Engineering	201	154	76.6
5	Computer Science and Engineering	164	134	81.7
<b>Overall</b>		<b>726</b>	<b>567</b>	<b>78.1</b>

**M.B.A. II Sem (2018 Admitted Batch.)**

Examination Starting Date: 10-06-2019

Examination Ending Date: 24-06-2019

Results Declaration Date: 05-07-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Business Administration	61	49	80.3

**M.B.A. IV Sem (2017 Admitted Batch.) MBA IV Sem Regular / Supple. May 2019**

Examination Starting Date: 07-05-2019

Examination Ending Date: 17-05-2019

Results Declaration Date: 09-07-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Business Administration	108	94	87.0

**M.C.A. IV Sem (2017 Admitted Batch.) MCA IV Sem Regular / Supple. April 2019**

Examination Starting Date: 13-04-2019

Examination Ending Date: 27-04-2019

Results Declaration Date: 09-07-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Computer Application	32	29	90.6

**M.C.A. VI Sem (2016 Admitted Batch)**

Examination Starting Date: 13-04-2019

Examination Ending Date: 27-04-2019

Results Declaration Date: 09-07-2019

S. No.	Branch	Appeared	Passed	% of Pass
1	Master of Computer Application	13	13	100

**M.Tech. II Sem (2018 Admitted Batch.)**

Examination Starting Date: 10-06-2019

Examination Ending Date: 19-06-2019

Results Declaration Date: 05-07-2019

S. No.	Branch	Appeared	Passed	% of Pass
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1	Machine Design	1	1	100
2	VLSI and Embedded Systems	1	0	00
Overall		2	1	50.0

**M.Tech. IV Sem (2017 Admitted Batch.) Seminar-II & Project**

S. No.	Branch	Appeared	Passed	% of Pass
1	Machine Design	1	0	0
2	VLSI and Embedded Systems	2	0	0
Overall		3	0	0

**Controller of Examinations**

**Principal**

**PRINCIPAL**

Devineni Venkata Ramana & Dr. Hima Sekhar  
**MIC College of Technology**  
Kanchikacherla, Krishna District