DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Value Added Courses

A.Y:2018-2019

S.NO	Name of the Value added courses	Course Code	No. Of Hours	Number of Students completing the course in the year
1	Aptitude, Reasoning and verbal Ability	VAC181916		
2	Veda –IIT Training	VAC181917	35	130
3	Coding classes (Phase-I)	VAC181918	28	51
4	Coding classes (Phase-II)	VAC181919	35	46
5	Infosys Training	VAC181920	14	102
6	Internet of Things (Phase-I)"	VAC181921	33	26
7	Internet of Things (Phase-II)"	VAC181922	36	168
8	Workshop on PCB Design	VAC181923	32	168
9	Salesforce Training	VAC181924	14	186
10	Soft Skills Training	VAC181925	21	120
11	WIPRO Training	VAC181926	21	80
			30	100

Signature of Coordinator

Signature of HOD

EDUCATION EDUCATION
INDUSTRY INDUSTRY

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Aptitude, Reasoning & Verbal Ability

By

Training & Placement Cell

Date of the Event: 26.06.2018 - 30.06.2018

Department Coordinators: Mr. A V V Sai Ram - CE   Mr. K Veenanand - ECE
Ms. D Divya - ME   Ms. K Vinaya Sri Bai - CSE

Out Comes:

➢ Enhance the Aptitude Round Clearing ability in interview process
➢ Solve the real-time problems for performing job functions easily
➢ Interpret the concepts of Logical Reasoning Skills
➢ Acquire satisfactory competency in use of Verbal Reasoning

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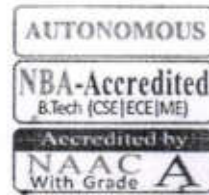Kanchikacherla, NTR Dt
Andhra Pradesh, India - 521180

Devineni Venkata Ramana & Dr.Hima Sekhar

MIC College of Technology

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IOC - Outgoing

DEPARTMENT: Training and Placement   DATE: 18-06-2018

ACADEMIC YEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty and IV B. Tech- (CSE, ECE, MECH,CIVIL) Students

All the HODs, staff and students are here by informed that the Training and Placement Cell is conducting training classes on Aptitude, Reasoning and Verbal Ability for IV B. Tech students from 26-06-2018 to 30-06-2018.In this regard, all the faculty and students are here by informed to utilize this opportunity All the students should attend the training programme without fail. Attendance is mandatory.

Training schedule:

S.No	Students	Timings
1	IV CSE	9.30 -4.10
2	IV ME	9.30 -4.10
3	IV ECE	9.30 -4.10
4	IV CIVIL	9.30 -4.10

Principal

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Department of Electronics and Communication Engineering

Academic Year: 2018-19

Name of the Event: Aptitude, Reasoning and verbal Ability   **Date:** 02.07.2018

Department: ECE   **Academic Year:** 2018-19

Topic: Aptitude, Reasoning and verbal Ability

Date of the Event: 26.06.2018-30.06.2018

Target Audience: B. Tech (CSE, ECE, MECH, CIVIL) Students

Total no of Participants: 130

Description:

The Department of Training & placement cell organized "Aptitude, Reasoning and verbal Ability", and is specially meant for Engineering final students. This program is helpful to create awareness on best innovative practices and also impart technical knowledge among students about recent developments in Engineering and Science. The sole objective of imparting aptitude training is to make students able to critically evaluate various real-life situations by resorting to an analysis of key issues and factors. This Aptitude Training helps them to demonstrate various principles involved in solving mathematical problems and thereby reducing the time taken for performing job functions. Verbal reasoning is a test of a skill rather than a test of learned knowledge. This is used to determine a student's critical thinking skills, as well as their ability to use their own knowledge to solve a problem.

Outcomes of the Event:

After completing this training course, students will be able to

1. Enhance the Aptitude Round Clearing ability in interview process

2. Solve the real-time problems for performing job functions easily

3. Interpret the concepts of Logical Reasoning Skills

4. Acquire satisfactory competency in use of Verbal Reasoning

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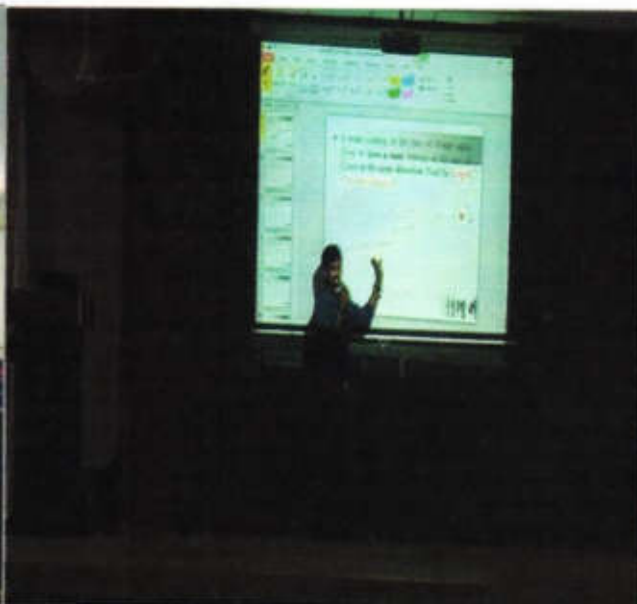Kanchikacherla, NTR Dt.
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Photographs:

Coordinator

HOD

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VEDA IIT TRAINING
BY
TRAINING & PLACEMENT CELL

Date of the Event: 04/09/2018 – 07/09/2018

Department Coordinators: Mr. M Tharun – EEE
Mr. K Veenanand - ECE

Topics of the Program

❑ Basic Electronics
❑ Digital Design & Number Systems
❑ 8086 Microprocessor & 8051
❑ Microcontroller
❑ C Programming
❑ Aptitude & VLSI.

Outcomes of the event:

1 Enhance the Aptitude Round Clearing ability in interview process
2. Solve the real-time problems for performing job functions easily
3. Evaluate the student's knowledge in Digital, Microprocessors, C Programming

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IOC - Outgoing

DEPARTMENT: Training & Placement   DATE: 01.09.2018

ACADEMICYEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty, IV B. Tech –(ECE & EEE)Students and Dept. Notice Board

All the HODs, staff and students are here by informed that the Training and Placement Cell is conducting training classes on Basics of Electronics for IV B. Tech-(ECE &EEE) students from 04.09.2018 to 07.09.2018. In this regard, all the faculty and students are here by informed to utilize this opportunity. All the students should attend the training programme without fail. Attendance is mandatory.
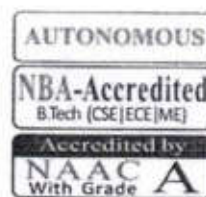
Training schedule:

S.No	Students	Timings
1	IV EEE	9.30 -4.10
2	IV ECE	9.30 -4.10

Principal

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Department of Electronics and Communication Engineering

Academic Year: 2018-19

Name of the Event: Veda –IIT Training

Department: ECE

Topic: Basics of Electronics

Date of the Event: 4.09.2018 to 7.09.2018

Target Audience: B. Tech ECE branch students

Total no of Participants: 51

Date: 10.09.2018

Academic Year: 2018-19

Objective of the event:

The Department of Training & placement cell organized "Veda –IIT Training", and is specially meant for engineering final students of Electronics & communication Engineering & Electrical & Electronics Engineering. This program is helpful to create awareness on best innovative practices and also impart technical knowledge among students about recent developments in Engineering and Science. This program is aimed to familiarize students on Basic Electronics & Communication subjects. The course covers Basic concepts and programming ability.

Topics covered: Basic Electronics, Digital Design & Number Systems, 8086 Microprocessor & 8051 Microcontroller, C Programming, Aptitude & VLSI.

Outcome of event: After completing this training course, students will be able to

1. Enhance the Aptitude Round Clearing ability in interview process

2. Solve the real-time problems for performing job functions easily

3. Evaluate the student's knowledge in Digital, Microprocessors, C Programming

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Photographs:

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CODING CLASSES (PHASE - I)
BY
TRAINING & PLACEMENT CELL

Date of the Event: 24.08.2018 - 28.08.2018

Department Coordinators: Mr. A V V Sai Ram -CE   Mr. M Tharun - EEE

Ms. D Divya - ME   Mr. K Veenanand - ECE

Ms. K Vinaya Sri Bai - CSE

Out Comes:

✓ Techniques for solving problems

✓ Basic computational concepts and elementary data structures

✓ The edit-compile-link-run cycle from a user point of view

✓ Testing strategies

✓ The main activities of software development and their interactions, and some of the major problems of software development

CODING CLASSES (PHASE - II)
BY
TRAINING & PLACEMENT CELL

Date of the Event: 11.09.2018 – 12.09.2018

Department Coordinators: Mr. A V V Sai Ram – CE   Mr. M Tharun - EEE
Ms. D Divya - ME   Mr. K Veenanand - ECE
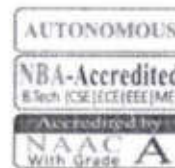Ms. K Vinaya Sri Bai - CSE

Out Comes:

✓ Techniques for solving problems
✓ Basic computational concepts and elementary data structures
✓ The edit-compile-link-run cycle from a user point of view
✓ Testing strategies
✓ The main activities of software development and their interactions, and some of the major problems of software development

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IOC - Outgoing

DEPARTMENT: Training and Placement   DATE: 18-08-2018

ACADEMIC YEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty and IV B. Tech -(CSE, ECE, MECH,CIVIL) Students

All the HODs, staff and students are here by informed that the Training and Placement Cell is conducting training classes on Coding (Phase-I) for IV B. Tech Students from 24-08-2018 to 28-08-2018. In this regard, all the faculty and students are here by informed to utilize this opportunity. All the students should attend the training programme without fail. Attendance is mandatory.

Training schedule:

S.No	Students	Timings
1	IV CSE	9.30 -4.10
2	IV ME	9.30 -4.10
3	IV ECE	9.30 -4.10
4	IV CIVIL	9.30 -4.10

Principal

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IOC - Outgoing

DEPARTMENT: Training and Placement   DATE:10-09-2018

ACADEMIC YEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty and IV B. Tech -(CSE, ECE, MECH,CIVIL) Students

All the HODs, staff and students are here by informed that the Training and Placement Cell is conducting training classes on Coding (Phase-II) for IV B. Tech Students from 11-09-2018 to 12-09-2018. .In this regard, all the faculty and students are here by informed to utilize this opportunity. All the students should attend the training programme without fail. Attendance is mandatory.

Training schedule:

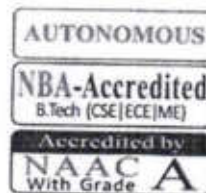| S.No	Students	Timings
1	IV CSE	9.30 -4.10
2	IV ME	9.30 -4.10
3	IV ECE	9.30 -4.10
4	IV CIVIL	9.30 -4.10

Principal

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Department of Electronics and Communication Engineering

Academic Year: 2018-19

Name of the Event: Coding classes (Phase-I)   **Date:** 13.09.2018

Coding classes (Phase-II)   **Academic Year:** 2018-19

Department: ECE

Topic: Coding classes (Phase-I) & Coding classes (Phase-II)

Date of the Event: 24.08.2018-28.08.2018 & 11.09.2018-12.09.2018

Target Audience: B. Tech All branch students

Total no of Participants: Phase-I-46,Phase-II-102

Description:

The Department of Training & placement cell organized "Coding classes (Phase-I & Phase-II)", and is specially meant for engineering final students. During the Coding phase the design of the software system is translated into a programming language format which is used by a computer, this is the main objective of coding. The aim of the coding is to produce high quality system which can be performed in any situation. The programmer removes all the errors related to syntax and format and all the logical errors which find in the programmer during the coding phase.

Outcomes of the Event:

•Techniques for Solving Problems

•Basic Computational Concepts and Elementary Data Structures

•The Edit-Compile-Link-Run Cycle from a User Point Of View

•Testing Strategies

•The Main Activities of Software Development and Their Interactions, and Some of the Major Problems of Software Development

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Coordinator

HOD

Principal

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INFOSYS TRAINING

BY

TRAINING & PLACEMENT CELL

Date of the Event: 07/12/2018 – 13/12/2018

Department Coordinators: Mr. A V V Sai Ram – CE   Mr. M Tharun - EEE

Ms. D Divya - ME   Mr. K Veenanand - ECE

Ms. K Vinaya Sri Bai - CSE

Out Comes:

❖ Enhance the Aptitude Round Clearing ability in interview process

❖ Solve the real-time problems for performing job functions easily

❖ Interpret the concepts of Logical Reasoning Skills

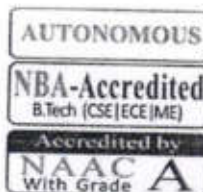❖ Develop effective communication skills (spoken and written)

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DEPARTMENT: Training and Placement   DATE:5-12-2018

ACADEMICYEAR:2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – All Departments

All the HODs, staff and students are here by informed that the Training and Placement Cell is conducting Infosys-specific training classes in collaboration with Talentio Solutions India Pvt Ltd for IV B.Tech Students from 07-12-2018 to 13-12-2018. In this regard, all the faculty and students are here by informed to utilize this opportunity. All the students should attend the training programme without fail. Attendance is mandatory.

Training schedule:

S.No	Students	Timings
1	IV EEE	9.30 -4.10
2	IV CSE	9.30 -4.10
3	IV ME	9.30 -4.10
4	IV ECE	9.30 -4.10
5	IV CIVIL	9.30 -4.10

Principal

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Department of Electronics and Communication Engineering

Academic Year: 2018-19

Name of the Event: Infosys Training

Date of the Event: 07-12-2018 to 13-12-2018

Resource Person: Talentio Solutions India Pvt Ltd

Name of the coordinator: K.Veenanand
Asst Professor, Dept of ECE

Target Audience: IV B. Tech Students

Total no of Participants: 26

Description:

The Department of Training & placement cell organized "**Infosys Training**", and is specially meant for engineering final students. The sole objective of imparting aptitude training is to make students able[a1] to critically evaluate various real-life situations by resorting to an analysis of key issues and factors. This Aptitude Training helps them to demonstrate various principles involved in solving mathematical problems and thereby reducing the time taken for performing job functions. Logical reasoning is a test of a skill rather than a test of learned knowledge. This is used to determine a student's critical thinking skills, as well as their ability to use their own knowledge to solve a problem. Soft Skills (a vital portion of an individual's personality) is an intangible idea in which the qualities like attitude, ability, integrity, reliability, positivity, flexibility, dependability, punctuality, management, cooperation, habits and practices are combined proficiently to capitalize on a person's work efficacy. Soft Skills do the work of combining all these components in accurate share into skills and shaping them into competencies. The aim of the coding is to produce high quality system which can be performed in any situation. The programmer removes all the errors related to syntax and format and all the logical errors which find in the programmer during the coding phase.

Outcome of event: After completing this training course, students will be able to

1. Enhance the Aptitude Round Clearing ability in interview process

2. Solve the real-time problems for performing job functions easily

3. Interpret the concepts of LOGICAL REASONING Skills

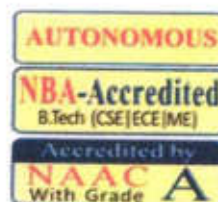4. Develop effective communication skills (spoken and written).

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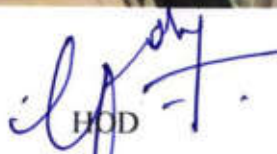e mail: dvrhsmic@mictech.ac.in, Website: www.mictech.ac.in

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Feedback / Suggestions: Students are participated in enthusiastic manne.Planned to organize every year.

Photographs:

Coordinator

HOD

Principal

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IOC - Outgoing

DEPARTMENT: ECE   DATE:21-08-2018

ACADEMIC YEAR: 2018-19

ORIGINATOR	HOD,ECE
CIRCULATED TO	Faculty, III B. Tech Students and Dept. Notice Board

It is here by informed that the Department of ECE is organizes **Internet of Things (Phase-I)** training for III B. Tech students in collaboration with Andhra Pradesh State Skill Development Corporation along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, from 23rd August 2018 to 1st September 2018. In this regard, all the faculty and students are here by informed to utilize this opportunity

HOD,ECE

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IOC - Outgoing

DEPARTMENT: ECE   DATE: 30-11-2018

ACADEMIC YEAR: 2018-19

ORIGINATOR	HOD,ECE
CIRCULATED TO	Faculty, III B. Tech Students and Dept. Notice Board

It is here by informed that the Department of ECE is organizes **Internet of Things (Phase-II)** training for III B. Tech students in collaboration with Andhra Pradesh State Skill Development Corporation along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, from 03rd December 2018 to 12th December 2018. In this regard, all the faculty and students are here by informed to utilize this opportunity

HOD, ECE

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Department of Electronics & Communication Engineering

WORKSHOP

Name of the event: **Workshop on "Internet of Things (Phase-I)"**

Date of the event: 23rd August 2018 to 1st September 2018

    Andhra Pradesh State Skill Development Corporation organized this workshop along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla.

- ❖ The Program was inaugurated by our college Vice Principal and Head of the Department Dr.A.Guruva Reddy.
- ❖ Each Section is allotted three days for Phase-I of IoT Fundamentals Workshop.
- ❖ Three days training includes Forenoon and Afternoon Sessions, which includes hands on experience.
- ❖ Each section has 57 students, who are beneficial by this workshop and a total of 168 students are trained during these 9 days of workshop.
- ❖ This workshop is aimed at imparting coding techniques of ardiuno based embedded-c code development.

Resource person:

1. **Mr. B.Nagaraju**
2. **Mr. T.Anil kumar**

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Workshop on "Internet of Things" -Photo Gallery:

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Andhra Pradesh. India - 521180

Department of ECE
Electronic Society for Proactive and Innovative Communication Engineers

WORKSHOP ON

Internet of Things(IOT)

By

Andhra Pradesh State Skill Development Corporation

From 23rd August 2018 to 1st September 2018

MIC College of Technology

Devineni Venkata Ramana & Dr.Hima Sekhar
MIC College of Technology
(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)
Kanchikacherla - 521180, Krishna Dist, A.P. India.
Phone : 08678 - 273535, 273623, Fax: 08678 - 273569
e-mail: dvrhsmic@mictech.ac.in, Website: www.mictech.ac.in

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NAAC A
With Grade

Department of Electronics & Communication Engineering

WORKSHOP

Name of the event: **Workshop on "Internet of Things (Phase-II)"**

Date of the event: 03rd December 2018 to 12th December 2018

Andhra Pradesh State Skill Development Corporation organized this workshop along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla.

❖ The Program was inaugurated by our college Vice Principal and Head of the Department Dr.A.Guruva Reddy.

❖ Each section has 57 students, who are beneficial by this workshop and a total of 168 students are trained during these 9 days of workshop.

❖ This workshop is aimed at imparting coding techniques of Raspberry-pi based embedded-c code development.

Resource person:

1. **Ms.P.Prabhu Kumari**
2. **Ms. Swetha sri Thota**

Workshop on "Internet of Things" -Photo Gallery:

Coordinator   HOD   Principal

Devineni Venkata Ramana & Dr. Hima Sekhar

MIC College of Technology

Kanchikacherla, Krishna Dist, Andhra Pradesh

DEPARTMENT OF ECE

Workshop on PCB Design

28th Feb To 1st Mar 2019

1. Name: _____
2. Designation: _____
3. Qualification: _____
4. Institution: _____
5. Professional Experience: _____
7. Address: _____

_____

8. Email Id: _____
9. Mobile No. : _____

Declaration:

The information provided is true to the best of my knowledge and belief. If selected, I agree to abide by the rules and regulations of the pro-gram.

Signature of the Applicant

Place:

Date:

Signature of the
Head of Organization
(With seal)

N.B.: Brochure & registration form can also be downloaded from our Website: http:// www.mictech.ac.in

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Chief Patron
Dr. M. V. RamanaRao, Chairman

Patron
Sri N. SrinivasaRao, Vice Chairman
Sri D. PandurangaRao, CEO
Sri M. SrinivasaRao, Director (P&D)
Prof. N. Krishna, Director (Academic)
Dr.K.B.K.Rao, Principal

Convener
Dr. A.Gurva Reddy, Head, Dept. of ECE

Coordinator
Mr.B.R.K.Singh,
Asst.Professor, Dept. of ECE
Mrs. T.M.S.Bharadwaj, Asst Professor.

Organizing Committee
Dr.Savitesh M.Sharma,.Prof.
Mr.CH.Pullano Assoc Prof
Mr. M.Gopala Krishna, Asst. Prof.
Mrs.T.Sridevi, Asst. Prof.
Mr. D.Rahul, Asst. Prof.
Mr. K.Veenanand. Asst.Prof.

Workshop on PCB Design
28th Feb To 1st Mar 2019

Organized by
Department of ECE

Devineni Venkata Ramana & Dr.Hima sekhar MIC College of Technology
Kanchikacherla, Krishna Dist.
A.P - 521180.Ph: 08678-273535.
Fax: 08678-273569
Website: www.mictech.ac.in

About the College:

Devineni Venkata Ramana & Dr. Hima Sekhar MIC College of Technology was established in 2002, at Kanchikacherla, 35km away from Vijayawada on NH-9. It was started by Dr.M.V.RamanaRao, CMD, MIC Electronics Ltd., an MNC, pioneered in making the giant LED screens, Solar Eco friendly LED lighting sys-tems 3D Hologram and offering the solutions to global telecom industry. The institute has the mission to pro-mote quality technical education, give the nation self-reliant engineers & entrepreneurs and bridge the gap between the industry and institution. The institute is located in sylvan surroundings with excellent infrastructure, state of art labs, well stocked library, sports facilities, modern class rooms, seminar halls and other facilities.

The College is affiliated to JNTUK Kakinada, approved by AICTE and also an ISO 9001:2008 certified institution. The institute is offering 5 UG programmes in engineering with 780 intake, 6 PG programmes M.Tech, MBA & MCA with 456 intake and Diploma programmes in EEE, Mech, ECE, Civil engineering with an intake of 240.

About the Department:

The Electronics & Communication Engineering department was started with UG programme in 2002 with an annual intake of 60. Now, the department is offering UG programme with 180 intake and PG programme with two specializations Digital Electronics and Communication systems and VLSI & Embedded Systems with an intake of 18 and 36 respectively. The vision of the department is to train the students with latest updates in the field of Electronics & Communication Engineering and is striving for the quality of education.

The department has well qualified, experienced & dynamic faculty along with experienced & skilled technical supporting staff who spearhead the process of achieving the vision of the department. The department has well equipped labs & infrastructure. Till now 9 batches have passed out with more than 95 pass percentage. Faculty R&D having DST sponsored project worth of 18.3lacs and many published IEEE papers and International Journals. Many of our students' technical papers have been published in various National & international technical symposiums. A good number of students are being placed through campus placements in major IT and core MNC companies every year.

About the Programme:

The scope of the workshop ranges from the fundamental properties Design and making of printed circuit board, soldering components on the PCB and verification of circuit function etc. software session has been done by PCB wizard software practical session has been conducted which includes etching,drilling,cleaning,soldering of components on pcd as per the circuit diagram and verifying the functionality of the circuit.

Important Dates

Workshop On : 28th Feb To 1st Mar 2019

Resource Persons:

Experts from APSSDC

Eligibility Criteria:

This programme is open to all AICTE approved engineering college Teaching Staff members and students pursuing their UG & PG courses, R&D scholars &Professionals from R&D; Industry. Selection of participants will be on first come first serve basis.

Address for Correspondence:

Dr.A.Gurva Reddy ., Professor & Head, Dept. of ECE,

DVR & Dr. HS MIC College of Technology, Kanchikacherla, Krishna Dist., AP -521180.

PRINCIPAL
DVR & Dr HS MIC College of Technology
Kanchikacherla, NTR Dt.
Andhra Pradesh, India - 521180.

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MIC College of Technology
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Kanchikacherla - 521180, Krishna Dist. A.P.

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Accredited by
NAAC 'A' Grade

Workshop
on
PCB Design
by APSSDC

28-02-2019 - 1-03-2019

Organized by
Department of Electronics and Communication Engineering

PRINCIPAL
DVR & Dr HS MIC College of Technology
Kanchikacherla, NTR Dt.
Andhra Pradesh, India - 521180

IOC - Outgoing

DEPARTMENT: ECE   **DATE: 26-02-2018**

ACADEMICYEAR: 2018-19

ORIGINATOR	HOD,ECE
CIRCULATED TO	Faculty, II B. Tech(ECE) Students and Dept. Notice Board

It is here by informed that the Department of ECE is organizing PCB Design training for II B. Tech students in collaboration with Andhra Pradesh State Skill Development Corporation along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, from 28-02-2019 to 1-03-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity

HOD/ECE

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Andhra Pradesh, India - 521180

Devineni Venkata Ramana & Dr.Hima Sekhar

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NAAC
With Grade A

Department of Electronics and Communication Engineering

Academic Year: 2018-19

Date:04-03-2019

Academic Year: 2018-19

Name of the event: *Workshop on PCB Design*

Date of the event: 28-02-2019 -1-03-2019

Name of the coordinators:

      1.Mr B R K Singh

      2.MrTMS Bharadwaj

Description:

- ❖ Andhra Pradesh State Skill Development Corporation along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, conducted the PCB Design workshop on Designing and making of Printed Circuit Board, Soldering components on the PCB and verification of circuit function etc.

- ❖ Three days training includes Forenoon and Afternoon Sessions, which includes hands on experience.

- ❖ PCB wizard Software training,

- ❖ Circuit simulation and two electronic circuits designing.

- ❖ PCB making, screen printing, etching of PCB, Soldering of components on to the PCB are conducted.

- ❖ Each section has 62 students, who are beneficial by this workshop and a total of 186 students are trained during these 9 days of workshop.

Target Audience: II B.Tech – ECEA, B, C students.

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Photos:

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Workshop
on
PCB Design
by APSSDC

28-02-2019 - 1-03-2019

Organized by
Department of Electronics and Communication Engineering

Coordinator

HOD

Principal

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SALES FORCE TRAINING
BY
TRAINING & PLACEMENT CELL

Date of the Event: 10/12/2018 – 12/12/2018

Department Coordinators: Mr. A V V Sai Ram – CE   Mr. M Tharun - EEE
       Mr. K Veenanand - ECE   Ms. K Vinaya Sri Bai - CSE

Objectives:

❖ Sales force CRM and its features
❖ Sales force objects, field types, and validation rules
❖ Data modeling and management
❖ Setup and user management
❖ Security and data access
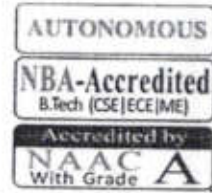❖ Workflow automation
❖ Sales and service cloud configuration

PRINCIPAL
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Andhra Pradesh, India - 521180

IOC - Outgoing

DEPARTMENT: Training & Placement   DATE:08-12-2018

ACADEMICYEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty, IV B. Tech Students and Dept. Notice Board

It is here by informed that the Training and Placement Cell is organizing Sales force training for IV B. Tech students in collaboration with Zaran Tech Pvt Ltd from 10-12-2018 to 12-12-2018. In this regard, all the faculty and students are here by informed to utilize this opportunity.

Principal

PRINCIPAL
DVR & Dr HS MIC College of Technology
Kanchikacheria, NTR Dt.
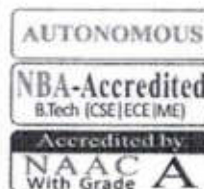Andhra Pradesh. India - 521180

Devineni Venkata Ramana & Dr.Hima Sekhar

MIC College of Technology

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Accredited by:

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With Grade A

Department of Electronics & Communication Engineering

Academic Year: 2018-19

Name of the Event: Salesforce Training

Date of the Event: 10-12-2018 to 12-12-2018

Resource Person: ZaranTech Pvt Ltd

Name of the coordinator: K.Veenanand

Target Audience: IV B. Tech Students

Total no of Participants: 120

Description:
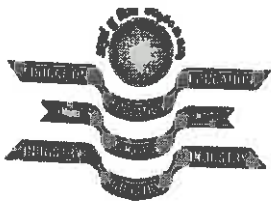
Objectives – Salesforce Administrator

This Salesforce Developer training for App Builder and Administrator is specifically designed to give you hands-on experience in the following topics:

- Salesforce CRM and its features
- Salesforce objects, field types, and validation rules
- Data modeling and management
- Setup and user management
- Security and data access
- Workflow automation
- Sales and service cloud configuration
- Lightning components installation
- Salesforce interface
- Application deployment and Force.com platform change management
- User interface configuration
- Preparing for Salesforce Administration (ADM 201) and App Builder certification exams
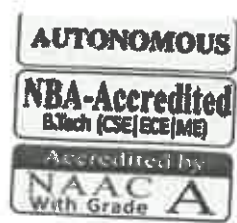
Prerequisites

There are no specific prerequisites for joining this Salesforce Developer training course. Anybody can opt for this Salesforce Admin certification. However, prior experience as a developer or administrator will definitely give a boost to your learning path here.

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Topics Covered:

Module 01: Salesforce Fundamentals

Module 02: Quote Templates – Preview, Create, and Activate

Module 03: Field Types and Salesforce Objects

Module 04: Creation of Custom Buttons, Link units, and Actions

Module 05: Salesforce Workflow, Schema Builder, and Process Builder

Module 06: Validation and Workflow Rules

Module 07: Creation of Role Hierarchy

Module 08: Security Data Security Model

Module 09: Salesforce User Profiles

Module 10: Salesforce Reporting and Dashboards

Module 11: Data Management in Salesforce

Module 12: AppExchange

Module 13: Deploying a Change Set

Module 14: User Interface in Salesforce

Module 15: UI component dynamic update

Module 16: Event handling

Module 17: Server- side controller

Module 18: Hands on example of Server-side controller

Module 19: Mobile

Module 20: APP DEVELOPMENT

Feedback / Suggestions: Students are participated in enthusiastic manner.
Planned to organize every year.

Coordinator

HOD

Principal

SOFT SKILLS TRAINING
BY
TRAINING & PLACEMENT CELL

Date of the Event: 24/09/2018 – 26/09/2018

Resource Person: Wicky David Training Company

Department Coordinators: Mr. A V V Sai Ram – CE   Mr. M Tharun - EEE
Ms. D Divya - ME   Mr. K Veenanand - ECE
Ms. K Vinaya Sri Bai - CSE

Contents

Qualities of Body Language   Curriculum Vitae
Emotional Intelligence   Public Speaking
Team Building   Conflict Resolution
Attitude   Group Discussion
Leadership   Negotiation Skills
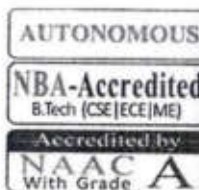Stress Management and Time Management

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IOC - Outgoing

DEPARTMENT: Training and Placement   DATE: 20-09-2018

ACADEMICYEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty and IVB. Tech Students – All Departments

All the HODs, staff and students are here by informed that the training and placement section is conducting training classes on soft skills for IV B. Tech Students from 24-09-2018 to 26-09-2018. In this regard, all the faculty and students are here by informed to utilize this opportunity. All the students should attend the training programme without fail. Attendance is mandatory.

Training schedule:

S.No	Students	Timings
1	IV EEE	9.30 -4.10
2	IV CSE	9.30 -4.10
3	IV ME	9.30 -4.10
4	IV ECE	9.30 -4.10
5	IV CIVIL	9.30 -4.10

Principal

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Andhra Pradesh, India - 521186,

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Department of Electronics and Communication Engineering

Academic Year: 2018-19

Name of the Event: Soft Skills Training

Date: *24.09.2018-26.09.2018*

Department: ECE

Topic: Soft Skills Training

Resource Person: Wicky David Training Company

Name of the coordinator: K.Veenanand
Asst Professor, Dept of ECE

Target Audience: B. Tech ECE branch students

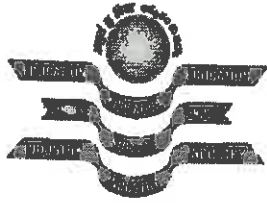**Total no of Participants:** 80

Description:

The Department of Training & placement cell organized "**Soft skills Training**", and is specially meant for Engineering final students. Soft Skills (a vital portion of an individual's personality) is an intangible idea in which the qualities like attitude, ability, integrity, reliability, positivity, flexibility, dependability, punctuality, management, cooperation, habits and practices are combined proficiently to capitalize on a person's work efficacy. Soft Skills do the work of combining all these components in accurate share into skills and shaping them into competencies.

Topics covered: Qualities of Body Language, Emotional Intelligence, Team Building, Attitude, Group Discussion, Know Thyself, Leadership, Public Speaking, Conflict Resolution, Curriculum Vitae, Negotiation Skills, Stress Management and Time Management.

Outcome of event: After completing this training course, students will be able to
- Develop effective communication skills (spoken and written).
- Develop effective presentation skills.
- Become self-confident individuals by mastering inter-personal skills, team management skills, and leadership skills.

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Date: 27.09.2018

Academic Year: 2018-19

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- Develop all-round personalities with a mature outlook to function effectively in different circumstances.

Feedback / Suggestions: Students are participated in enthusiastic manner. Planned to organize every year.
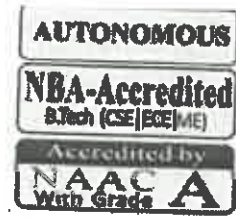
Coordinator

HOD

Principal

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WIPRO TRAINING
BY
TRAINING & PLACEMENT CELL

Date of the Event: 09/11/2018 – 14/11/2018

Resource Person: Talentio Solutions India Pvt Ltd

Department Coordinators: Mr. A V V Sai Ram – CE   Mr. K Veenanand - ECE
Ms. D Divya - ME   Ms. K Vinaya Sri Bai - CSE

Topics of the Program

❖ Wipro Turbo Quants
❖ Wipro Turbo Verbal English
❖ Wipro Turbo Logical Reasoning
❖ Wipro Turbo Technical Computer coding

Outcomes of the event:

1. Enhance the Aptitude Round Clearing ability in interview process
2. Solve the real-time problems for performing job functions easily
3. Interpret the concepts of LOGICAL REASONING Skills
4. Develop effective communication skills (spoken and written)

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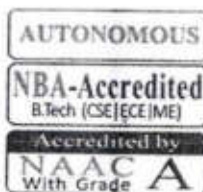Kanchikacherla, NTR Dt
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With Grade

DEPARTMENT: Training and Placement   DATE: 07-11-2018

ACADEMIC YEAR: 2018-19

ORIGINATOR	Principal
CIRCULATED TO	HODs, Faculty and IV B. Tech-(CSE, ME, CIVIL, ECE) Students.

All the HODs, staff and students are here by informed that the training and placement section is conducting training classes on WIPRO Specific Training for IV B.Tech- (CSE, ME, CIVIL, ECE) students from 09-11-2018 to 14-11-2018. In this regard, all the faculty and students are here by informed to utilize this opportunity. All the students should attend the training programme without fail. Attendance is mandatory.

Training schedule:

S.No	Students	Timings
1	IV CSE	9.30 -4.10
2	IV ME	9.30 -4.10
3	IV ECE	9.30 -4.10
4	IV CIVIL	9.30 -4.10

Principal

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Department of Electronics and Communication Engineering

Academic Year: 2018-19

Name of the Event: WIPRO Training

Date: 15.11.2018

Date of the Event: 09.11.2018-14.11.2018

Resource Person: Talentio Solutions India Pvt Ltd

Name of the coordinator: K.Veenanand
Asst Professor, Dept of ECE
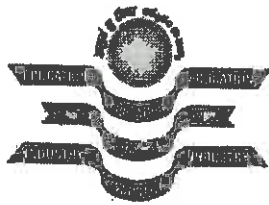
Target Audience: IV B. Tech Students

Total no of Participants: 100

Description:

The Department of Training & placement cell organized "**WIPRO Training**", and is specially meant for engineering final students. The sole objective of imparting aptitude training is to make students able to critically evaluate various real-life situations by resorting to an analysis of key issues and factors. This Aptitude Training helps them to demonstrate various principles involved in solving mathematical problems and thereby reducing the time taken for performing job functions. Logical reasoning is a test of a skill rather than a test of learned knowledge. This is used to determine a student's critical thinking skills, as well as their ability to use their own knowledge to solve a problem. Soft Skills (a vital portion of an individual's personality) is an intangible idea in which the qualities like attitude, ability, integrity, reliability, positivity, flexibility, dependability, punctuality, management, cooperation, habits and practices are combined proficiently to capitalize on a person's work efficacy. Soft Skills do the work of combining all these components in accurate share into skills and shaping them into competencies. The aim of the coding is to produce high quality system which can be performed in any situation. The programmer removes all the errors related to syntax and format and all the logical errors which find in the programmer during the coding phase.

Topics covered: Wipro Turbo Quants, Wipro Turbo Verbal English, Wipro Turbo Logical Reasoning and Wipro Turbo Technical Computer coding.
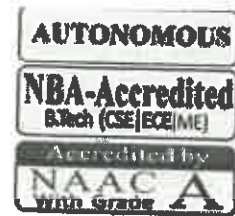
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Outcome of event: After completing this training course, students will be able to

1. Enhance the Aptitude Round Clearing ability in interview process
2. Solve the real-time problems for performing job functions easily
3. Interpret the concepts of Logical Reasoning Skills
4. Develop effective communication skills (spoken and written).

Feedback / Suggestions: Students are participated in enthusiastic manner.
Planned to organize every year.

Coordinator

HOD

Principal