



Devineni Venkata Ramana & Dr.Hima Sekhar  
**MIC College of Technology**

(Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada)

Kanchikacherla - 521180, Krishna Dist, A.P, India.  
Phone : 08678 - 273535, 273623. Fax: 08678 - 273569  
e mail- dyvrmic@micotech.ac.in, Website- www.micotech.ac.in



**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**List of Value Added Courses**

**A.Y:2019-20**

S.NO	Name of the Value added courses	Course Code	No. Of Hours	Number of Students completing the course in the year
1	Aptitude, Reasoning and verbal Ability	VAC192013	70	106
2	Coding classes (Phase-I)	VAC192014	70	51
3	Coding classes (Phase-II)	VAC192015	28	28
4	Workshop on Fundamentals of Embedded Systems	VAC192016	21	115
5	In House Training	VAC192017	40	62
6	Workshop on PCB Design	VAC192018	33	192
7	PCB Hardware Training	VAC192019	35	192
8	PYTHON PROGRAMMING	VAC192020	30	100
9	WIPRO Training	VAC192021	30	110

  
Signature of Coordinator

  
Signature of HOD



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e mail: dvrmic/mictech.ac.in, Website: www.mictech.ac.in



**Outcomes of the event:**

1. Enhancement the Aptitude round clearing ability in interview process
2. Solve the real time problems for performing job functions easily
3. Interpret the concept of LOGICAL REASONING skills
4. Acquire satisfactory competency in use of VERBAL REASONING

## Training on Aptitude, Reasoning and Verbal Ability



Date: 13-06-2019 to 22-06-2019

**REGISTRATION  
FREE**

**Principal:**

**Dr. Y. Sudheer Babu**

M.Tech, Ph.D

**Coordinators:**

- Mr. M. Tharun, Assistant Professor-EEE
- Mr. K. Veenanand, Assistant Professor-ECE
- Mr. K. Vinay Sri Bai, Assistant Professor-CSE

**For : IV B.Tech Students**

**ORGANIZED BY:**  
Training & Placement Cell

**PRINCIPAL**  
DVR & Dr HS MIC College of Technology  
Kanchikacherla, NTR Dt  
Andhra Pradesh, India - 521180



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**IOC-Outgoing**

DEPARTMENT: Training and Placement

DATE: 10-06-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	PRINCIPAL
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – ECE, CSE & EEE

All the HOD's, staff and students are here by informed that the training and placement cell is conducting training program on Aptitude, Reasoning and Verbal Ability for IV Year B.Tech students from 13-06-2019 to 22-06-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity. The training schedule is given below.

**Timings:** 09.15 AM-12.30 PM (Fore noon)

01.15 PM-04.30PM (After noon)

**Venues:** CSE seminar Hall- CSE & ECE Batch 1

ECE seminar Hall- CSE & ECE Batch 2

EEE seminar Hall- EEE

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## Department of Electronics & Communication Engineering

**Academic Year: 2019-20**

**Name of the Event:** Aptitude, Reasoning and verbal Ability

**Topic:** Aptitude, Reasoning and verbal Ability

**Date of the Event:** 13-06-2019 to 22-06-2019

**Name of the coordinator:** Mr. K.Veenanand

Asst Professor, Dept of ECE

**Target Audience:** IV B. Tech Students

**Total no of Participants:** 106

**Description:** The Department of Training & placement cell organized “**Aptitude, Reasoning and verbal Ability**”, and is specially meant for Engineering final students. This program is helpful to create awareness on best innovative practices and also impart technical knowledge among students about recent developments in Engineering and Science. The sole objective of imparting aptitude training is to make students able to critically evaluate various real-life situations by resorting to an analysis of key issues and factors. This Aptitude Training helps them to demonstrate various principles involved in solving mathematical problems and thereby reducing the time taken for performing job functions. Verbal reasoning is a test of a skill rather than a test of learned knowledge. This is used to determine a student's critical thinking skills, as well as their ability to use their own knowledge to solve a problem.

**Outcomes of the Event:** After completing this training course, students will be able to

1. Enhance the Aptitude Round Clearing ability in interview process
2. Solve the real-time problems for performing job functions easily
3. Interpret the concepts of LOGICAL REASONING Skills
4. Acquire satisfactory competency in use of VERBAL REASONING

  
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Photographs:



  
Coordinator

  
HOD

  
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Andhra Pradesh, India - 521180



Dr. Y. Venkatesh Ramana & Dr. Hima Sekhar  
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e mail: dyrhmica@micttech.ac.in, Website: www.micttech.ac.in



## CODING CLASSES

By

**MADHU TECH SKILLS**

### Outcomes of the event:

1. Techniques for solving Problems
2. Basic Computational Concepts and Elementary Data Structures
3. The Edit, Compile, Link, Run cycle from a User point of view
4. Testing Strategies
5. The Main Activities of Software Development and their Interaction

Date:

24-06-2019 to 28-06-2019 PHASE-I

01-07-2019 to 04-07-2019 PHASE-II



Principal:

**Dr. Y. Sudheer Babu**

M.Tech, Ph.D

Coordinators:

**For : IV B.Tech Students**

- Mr. M. Tharun, Assistant Professor-EEE
- Mr. K. Veenanand, Assistant Professor-ECE
- Mr. K. Vinay Sri Bai, Assistant Professor-CSE

ORGANIZED BY  
TRAINING & PLACEMENT CELL





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### IOC-Outgoing

DEPARTMENT: Training and Placement

DATE: 20-06-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	PRINCIPAL
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – ECE, CSE & EEE

All the HOD's, staff and students are here by informed that the Training and Placement Cell is conducting training classes on Coding (Phase-I) for IV B. Tech Students from 24-06-2019 to 28-06-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity. The training schedule is given below.

**Timings:** 09.15 AM-12.30 PM (Fore noon)

01.15 PM-04.30PM (After noon)

**Venues:** CSE seminar Hall- CSE & ECE Batch 1

ECE seminar Hall- CSE & ECE Batch 2

EEE seminar Hall- EEE



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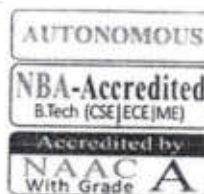
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e mail: dvhsmic@mictect.ac.in, Website: www.mictect.ac.in



**IOC-Outgoing**

DEPARTMENT: Training and Placement

DATE: 28-06-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	PRINCIPAL
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – ECE, CSE & EEE

All the HOD's, staff and students are here by informed that the Training and Placement Cell is conducting training classes on Coding (Phase-II) for IV B. Tech Students from 01-07-2019 to 04-07-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity. The training schedule is given below.

**Timings:** 09.15 AM-12.30 PM (Fore noon)

01.15 PM-04.30PM (After noon)

**Venues:** CSE seminar Hall- CSE & ECE Batch 1

ECE seminar Hall- CSE & ECE Batch 2

EEE seminar Hall- EEE

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## Department of Electronics & Communication Engineering

**Academic Year: 2019-20**

**Name of the Event:** Coding Classes (Phase - I and Phase -II)

**Date of the Event:** 24-06-2019 to 28-06-2019 (Phase – I) &  
01-07-2019 to 04-07-2019 (Phase – II)

**Number of Hours:** 30Hrs + 15Hrs

**Name of Resource Persons:** Madhu Tech Skills

**Name of the coordinator:** Mr. K.Veenanand,  
Asst Professor, Dept of ECE

**Target Audience:** IV B. Tech Students

**Total no of Participants:** Phase-I-51, Phase-II-28

### Description:

The Department of Training & placement cell organized “**Coding classes (Phase-I and Phase-II)**”, and is specially meant for Engineering final year students. During the Coding phase the design of the software system is translated into a programming language format which is used by a computer, this is the main objective of coding. The aim of the coding is to produce high quality system which can be performed in any situation. The programmer removes all the errors related to syntax and format and all the logical errors which find in the programmer during the coding phase.

### Outcomes of the Event:

- Techniques For Solving Problems
- Basic Computational Concepts And Elementary Data Structures
- The Edit-Compile-Link-Run Cycle From A User Point Of View
- Testing Strategies
- The Main Activities Of Software Development And Their Interactions, And Some Of The Major Problems Of Software Development

  
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**Photographs:**



*Devineni Venkata Ramana*  
Coordinator

*HOB*  
HOB

*Dr. Hima Sekhar*  
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*Dr. HS*

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## Workshop on Fundamentals of Embedded Systems



Date: 05-07, Sep 2019

e-spice  
The heart of technological excellence



Resource Persons: sri **M.MANOJ KUMAR**, Founder, **TINKER SPACE**  
**Engineers, BANGLORE**

Department of Electronics and Communication Engineering

  
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**IOC - Outgoing**

DEPARTMENT: ECE


DATE: 01-09-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	HOD,ECE
CIRCULATED TO	Faculty, III B. Tech Students (ECE) and Dept. Notice Board

It is here by informed that the Department of ECE is conducting Training on Fundamentals of ES for III B. Tech students (ECE) in collaboration with Tinker Space from 05-09-2019 to 07-09-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity.

  
HOD,ECE

  
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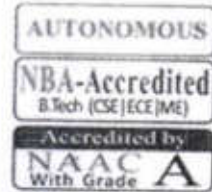




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## Department of Electronics & Communication Engineering

Academic Year: 2019-20

Academic Year: 2019-2020

Type of the event: **Workshop on Fundamentals of Embedded Systems**

Date of the event: 05 to 07, Sep 2019

Name of the coordinators:

Mr B R K Singh, Assoc professor, Dept of ECE

Description:

- ❖ TINKER SPACE Engineers, BANGLORE along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, conducted the **Fundamentals of Embedded Systems** workshop on Basic Knowledge of Embedded Systems, Basic Knowledge of Processor, Programming in Embedded Systems. Application in Embedded Systems.
- ❖ This training includes Forenoon and Afternoon Sessions, which includes hands on experience.
- ❖ System • Embedded System • Components • Classifications • Processors • Other Hardware • Software • Applications
- ❖ who are beneficial by this workshop and a total of 115 students are trained during these 3 days of workshop.

Target Audience: III B.Tech – ECE A,B students.

  
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Phone : 08678 - 273535, 273623, Fax: 08678 - 273569

e mail: dvvrmic@mictch.ac.in, Website: www.mictch.ac.in




**Photographs:**

## Workshop on Fundamentals of Embedded Systems



**Date: 05-07, Sep 2019**




**Resource Persons: sri M.MANOJ KUMAR, Founder, TINKER SPACE Engineers, BANGLORE**

**Department of Electronics and Communication Engineering**

  
Coordinator

  
HOD

  
Principal

  
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# IN HOUSE TRAINING

By

**Resource Person: Sri. D. Panduranga Rao, CEO**

Date: 05-07-2019 to 30-07-2019

**For : IV B.Tech Students**

**Contents:**

1. Structure of C Programs
2. Usage Of Different Data Types
3. Information On Decision
4. Control Statements
5. The Concept Of Recursion
6. The Concept of Matrix

**Principal:**

**Dr. Y. Sudheer Babu**

M.Tech, Ph.D

**Coordinators:**

- Mr. M. Tharun, Assistant Professor-EEE
- Mr. K. Veenanand, Assistant Professor-ECE
- Mr. K. Vinay Sri Bai, Assistant Professor-CSE

ORGANIZED BY:  
**Training & Placement Cell**

  
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**REGISTRATION  
FREE**



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**IOC-Outgoing**

DEPARTMENT: Training and Placement

DATE: 02-07-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	PRINCIPAL
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – ECE, CSE & EEE

All the HOD's, staff and students are here by informed that the training and placement cell is conducting training program on In house Training for IV Year student's from 05-07-2019 to 30-07-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity. The training schedule is given below.

**Timings:** 09.15 AM-12.30 PM (Fore noon)

01.15 PM-04.30PM (After noon)

**Venues:** CSE seminar Hall- CSE & ECE Batch 1

ECE seminar Hall- CSE & ECE Batch 2

EEE seminar Hall- EEE

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## Department of Electronics & Communication Engineering

**Academic Year: 2019-20**

**Name of the Event:** In House Training

**Date of the Event:** 05.07.2019 to 30.07.2019

**Resource Person:** Sri D.Panduranga Rao, CEO  
 MIC COLLEGE OF TECHNOLOGY

**Name of the coordinator:** Mr. K.Veenanand,  
 Asst Professor, Dept of ECE

**Target Audience:** IV B. Tech Students

**Total no of Participants:** 62

**Objective of the Course:** To help students improve their C programming skills and also to help develop logic for the given problem. It will help students to face placement.

**Outcome of the Course:** Students will be able to develop C programs for the given problem.

### CONTENTS:

- Information about basic concepts of the C programming language such as structure of C programs, usage of different data types and their variables followed by usage of operators with the variables was imparted to students.
- Information on decision control statements such simple if, if- else and if-else-if ladder and go to statement was imparted to students.
- Information on loops such as while loop, do-while loop and for loop and break and continue was imparted to students. Students were made to do the programs on them and some prominent problems were solved. Instruction on usage of functions in the C programming language was given to students. Also the different parameter passing techniques i.e. pass by value and pass by reference methods were discussed. Students were made to execute programs on these techniques.
- The concept of recursion was introduced to students. Programs on some of the most prominent problems on recursion were taught to students. Followed by introduction to arrays in C which covered utilization of array as function parameter.
- The concepts of Matrix were instructed to students. Operations on arrays and matrix were taught. Students were made to execute the programs on arrays and matrices.
- Concept of Strings was introduced to students. Different operations on strings using inbuilt functions and without using in-built functions were taught to students.
- Concept of pointers was introduced to students. Operations using pointers was taught. Usage of pointers as



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function parameters was told to students.


- Students practiced the programs on these concepts. Advanced concept in C i.e. dynamic memory allocation using pointers and little introduction to data structures was given to the students.
- Questions related to placement were practiced with explanation to the solutions.

**Feedback / Suggestions:** Students are participated in enthusiastic manner. Planned to organize every year.

  
Coordinator

  
HOD

  
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Devineni Venkata Ramana & Dr. Hima Sekhar  
**MIC College of Technology**  
Kanchikacherla, Krishna Dist, Andhra Pradesh  
DEPARTMENT OF ECE  
Workshop on PCB Design  
09<sup>th</sup> Sep To 21<sup>st</sup> Sep 2019

1. Name: \_\_\_\_\_
2. Designation: \_\_\_\_\_
3. Qualification: \_\_\_\_\_
4. Institution: \_\_\_\_\_
5. Professional Experience: \_\_\_\_\_
7. Address: \_\_\_\_\_
- \_\_\_\_\_
8. Email Id: \_\_\_\_\_
9. Mobile No. : \_\_\_\_\_

Declaration:

The information provided is true to the best of my knowledge and belief. If selected, I agree to abide by the rules and regulations of the pro-gram.

Signature of the Applicant

Place:

Date:

Signature of the  
Head of Organization  
(With seal)

N.B: Brochure & registration form can also be downloaded from our Website: <http://www.mictech.ac.in>

**Chief Patron**

Dr. M. V. RamanaRao, Chairman

**Patron**

Sri N. SrinivasaRao, Vice Chairman  
Sri D. PandurangaRao, CEO  
Sri M. SrinivasaRao, Director (P&D)  
Prof. N. Krishna, Director (Academic)  
Dr.K.B.K.Rao, Principal

**Convener**

Dr. A.Gurva Reddy, Head, Dept. of ECE

**Coordinator**

Mr.B.R.K.Singh,  
Asst.Professor, Dept. of ECE  
Mrs. T.M.S.Bharadwaj, Asst Professor.

**Organizing Committee**

Dr.Savitesh M.Sharma, Prof.  
Mr.CH.Pullarao Assoc Prof  
Mr. M.Gopala Krishna, Asst. Prof.  
Mrs.T.Sridevi, Asst. Prof.  
Mr. D.Rahul, Asst. Prof.  
Mr. K.Veenanand, Asst.Prof.



Organized by  
Department of ECE

Devineni Venkata Ramana & Dr.Hima  
sekhar MIC College of Technology  
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**About the College:**

Devineni Venkata Ramana & Dr. Hima Sekhar MIC College of Technology was established in 2002, at Kanchikacherla, 35km away from Vijayawada on NH-9. It was started by Dr.M.V.RamanaRao, CMD, MIC Electronics Ltd., an MNC, pioneered in making the giant LED screens, Solar Eco friendly LED lighting systems 3D Hologram and offering the solutions to global telecom industry. The institute has the mission to promote quality technical education, give the nation self-reliant engineers & entrepreneurs and bridge the gap between the industry and institution. The institute is located in sylvan surroundings with excellent infrastructure, state of art labs, well stocked library, sports facilities, modern class rooms, seminar halls and other facilities.

The College is affiliated to JNTUK Kakinada, approved by AICTE and also an ISO 9001:2008 certified institution. The institute is offering 5 UG programmes in engineering with 780 intake, 6 PG programmes M.Tech, MBA & MCA with 456 intake and Diploma programmes in EEE, Mech, ECE, Civil engineering with an intake of 240.

**About the Department:**

The Electronics & Communication Engineering department was started with UG programme in 2002 with an annual intake of 60. Now, the department is offering UG programme with 180 intake and PG programme with two specializations Digital Electronics and Communication systems and VLSI & Embedded Systems with an intake of 18 and 36 respectively. The vision of the department is to train the students with latest updates in the field of Electronics & Communication Engineering and is striving for the quality of education.

The department has well qualified, experienced & dynamic faculty along with experienced & skilled technical supporting staff who spearhead the process of achieving the vision of the department. The

department has well equipped labs & infrastructure. Till now 9 batches have passed out with more than 95 pass percentage. Faculty R&D having DST sponsored project worth of 18.3lacs and many published IEEE papers and International Journals. Many of our students' technical papers have been published in various National & international technical symposiums. A good number of students are being placed through campus placements in major IT and core MNC companies every year.

**About the Programme:**

The scope of the workshop ranges from the fundamental properties Design and making of printed circuit board, soldering components on the PCB and verification of circuit function etc. software session has been done by PCB wizard software.practical session has been conducted which includes etching,drilling,cleaning,soldering of components on pcb as per the circuit diagram and verifying the functionality of the circuit.

**Important Dates**

Workshop On : 09<sup>th</sup> Sep To 21<sup>st</sup> Sep 2019

**Eligibility Criteria:**

This programme is open to all AICTE approved engineering college Teaching Staff members and students pursuing their UG & PG courses, R&D scholars & Professionals from R&D; Industry. Selection of participants will be on first come first serve basis.

**Address for Correspondence:**

Dr.A.Gurva Reddy ,Professor & Head,  
Dept. of ECE,

DVR & Dr. HS MIC College of Technology,  
Kanchikacherla, Krishna Dist., AP -521180.

  
PRINCIPAL

DVR & Dr. HS MIC College of Technology  
Kanchikacherla, NTR Dt  
Andhra Pradesh, India - 521180





Devineni Venkata Ramana & Dr.Hima Sekhar  
**MIC College of Technology**

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With Grade **A**

**IOC - Outgoing**

DEPARTMENT: ECE

DATE: 06-09-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	HOD,ECE
CIRCULATED TO	Faculty, II B. Tech Students(ECE) and Dept. Notice Board

It is here by informed that the Department of ECE is Conducting PCB Design training for II B.Tech students (ECE) in collaboration with Andhra Pradesh State Skill Development Corporation along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, from 09-09-2019 to 21-09-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity

  
HOD,ECE

  
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Phone : 08678 - 273535, 273623, Fax: 08678 - 273569

e mail: dvrhsmic@mictech.ac.in, Website: www.mictech.ac.in



## Department of Electronics and Communication Engineering

**Academic Year: 2019-20**

Academic Year: 2019-2020

Name of the event: *Workshop on PCB Design*

Date of the event: 09th Sep To 21<sup>th</sup> Sep 2019

Name of the coordinators:

1.Mr B R K Singh

2.MrTMS Bharadwaj

Description:

- ❖ Andhra Pradesh State Skill Development Corporation along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, conducted the PCB Design workshop on Designing and making of Printed Circuit Board, Soldering components on the PCB and verification of circuit function etc.
- ❖ Three days training includes Forenoon and Afternoon Sessions, which includes hands on experience.
- ❖ PCB wizard Software training.
- ❖ Circuit simulation and two electronic circuits designing.
- ❖ PCB making, screen printing, etching of PCB, Soldering of components on to the PCB are conducted.
- ❖ Each section has 64 students, who are beneficial by this workshop and a total of 192 students are trained during these 9 days of workshop.

Target Audience: II B.Tech – ECEA, B, C students.

  
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Phone : 08678 - 273535, 273623, Fax: 08678 - 273569

e mail: dvrbmic@micttech.ac.in, Website: www.micttech.ac.in



Photos:



  
**Coordinator**

  
**HOD**

  
**Principal**

  
**PRINCIPAL**  
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## 3 Day Workshop on *PCB* *Hardware Training*

**II B. Tech Students**

02/02/2020 to 08/02/2020

### *Objectives*

PCB WIZARD SOFTWARE TRAINING

CIRCUIT SIMULATION

TWO ELECTRONIC CIRCUITS DESIGNING

PCB MAKING

SCREEN PRINTING

ETCHING OF PCB

### **Coordinators:**

1.Mr B R K Singh

2.Mr TMS Bharadwaj



  
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With Grade

**IOC - Outgoing**

DEPARTMENT: ECE

DATE:31/01/2020

ACADEMIC YEAR: 2019-20

ORIGINATOR	HOD,ECE
CIRCULATED TO	Faculty, II B. Tech Students(ECE) and Dept. Notice Board

It is here by informed that the Department of ECE is Conducting PCB Hardware training for II B.Tech students (ECE) in collaboration with Micro Link Technologies, from 02/02/2020 to 08/02/2020. In this regard, all the faculty and students are here by informed to utilize this opportunity

  
 HOD, ECE

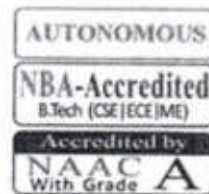
  
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Kanchikacherla - 521180, Krishna Dist, A.P. India.  
Phone : 08678 - 273535, 273623, Fax: 08678 - 273569  
e mail: dvhrmic@micotech.ac.in, Website: www.micotech.ac.in



## Department of Electronics and Communication Engineering

**Academic Year: 2019-20**

Academic Year: 2019-2020

Name of the event: **PCB Hardware Training**

Date of the event: 02/02/2020 to 08/02/2020

Name of the coordinators:

- 1.Mr B R K Singh
- 2.Mr TMS Bharadwaj

Description:

- ❖ Micro Link Technologies along with Electronic society for proactive and innovative communication engineers (espice) of MIC College of Technology, Kanchikacherla, conducted the PCB hardware training.
- ❖ The one week training includes Forenoon and Afternoon Sessions, which includes hands on experience.
- ❖ On first day, PCB wizard Software training.
- ❖ On other day ,circuit simulation and two electronic circuits designing.
- ❖ Remaining days, PCB making, screen printing, etching of PCB, Soldering of components on to the PCB are conducted.
- ❖ Each section has 64 students, who are beneficial by this workshop and a total of 192 students are trained during these 7 days of workshop.

Target Audience: II B.Tech – ECEA,B,C students.

  
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Andhra Pradesh, India - 521180.



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Kanchikacherla - 521180, Krishna Dist, A.P, India.

Phone : 08678 - 273535, 273623, Fax: 08678 - 273569

e mail: dvrhsmic@micttech.ac.in, Website: www.micttech.ac.in



Photos:



  
Coordinator

  
HOD

  
Principal

  
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## TRAINING PROGRAMME ON PYTHON

```
def dds(x):  
    turn = 0  
  
    def otwrite(ast):  
        dename = getNodename()  
        bel=symbol.sym_name.get(int(ast[0])  
        int    % (label="x" % (nodename  
        isinstance(ast[1], str):  
            if ast[1].strip():  
                print " %s" % ast[1]  
            else:  
                print ""  
        else:  
            print ""  
        children = []  
        for n, child in enumerate(ast[1]):  
            children.append(otwrite(child))  
        print " %s" % (nodename,  
        for name in children:  
            print " %s" % name,  
            print " %s" % name,
```

### Outcomes of the event:

1. Structure of Python Programming
2. Information on Decision Control Statements
3. Information on Loops
4. The Concept of Recursion
5. The Concept of Matrix
6. Concept of Strings
7. Concept of Pointers

**For : IV B.Tech Students**

**Date:19-07-2019 to 27-07-2019**

Principal:

**Dr. Y. Sudheer Babu**


M.Tech, Ph.D

**REGISTRATION**

**FREE**

**ORGANIZED BY**

**TRAINING & PLACEMENT CELL**

  
PRINCIPAL  
DVR & Dr HS MIC College of Technology  
Kanchikacherla, NTR Dt.  
Andhra Pradesh, India - 521180

Coordinators:

- Mr. M. Tharun, Assistant Professor-EEE
- Mr. K. Vinay Sri Bai, Assistant Professor-CSE
- Mr. K. Veenanand, Assistant Professor - ECE





Devineni Venkata Ramana & Dr.Hima Sekhar  
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### IOC-Outgoing

DEPARTMENT: Training and Placement

DATE: 16-07-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	PRINCIPAL
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – ECE, CSE & EEE

All the HOD's, staff and students are here by informed that the training and placement cell is conducting training program on Python Programming Training for IV Year student's from 19-07-2019 to 27-07-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity. The training schedule is given below.

**Timings:** 09.15 AM-12.30 PM (Fore noon)

01.15 PM-04.30PM (After noon)

**Venues:** CSE seminar Hall- CSE & ECE Batch 1

ECE seminar Hall- CSE & ECE Batch 2

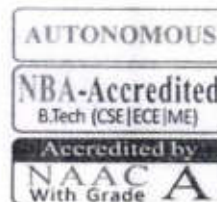
EEE seminar Hall- EEE

  
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Phone : 08678 - 273535, 273623. Fax: 08678 - 273569  
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## Department of Electronics & Communication Engineering

• Academic Year: 2019-20 •

**Name of the Event:** PYTHON PROGRAMMING

**Date of the Event:** 19-07-2019 to 27-07-2019

**Resource Person:** Talentio Solutions India Pvt Ltd

**Name of the coordinator:** Mr. K.Veenanand

Asst Professor, Dept of ECE

**Target Audience:** IV B. Tech Students

**Total no of Participants:** 100

**OBJECTIVE:** The course is designed to provide basic knowledge of Python. Python programming is intended for software engineers, system analysts, program managers and user support personnel who wish to learn the Python programming language.

**OVERVIEW:** Python is a widely used general-purpose, high level programming language. It was created by Guido van Rossum in 1991 and further developed by the Python Software Foundation. It was designed with an emphasis on code readability, and its syntax allows programmers to express their concepts in fewer lines of code.

**WHY TAKE THIS COURSE:** Python has been voted for the most favorite programming language of all time. It is undoubtedly beating other programming languages. It has been used for developing almost every kind of applications whether it is web applications or game applications. Numerous programmers have increased the use of Python programming languages and it is certainly used worldwide. Python programmers would be the most demandable in the future of IT industries which makes Python future brighter.

  
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## LEARNING OUTCOMES

After completing this course the students will be able to demonstrate the knowledge and ability:

- To understand why Python is a useful scripting language for developers.
- To learn how to design and program Python applications.
- To learn how to use lists, tuples and dictionaries in Python programs.
- To learn how to identify Python object types.
- To learn how to use indexing and slicing to access data in Python programs.

Photographs:



  
Coordinator

  
HOD

  
PRINCIPAL

  
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Phone : 08678 - 273535, 273623; Fax: 08678 - 273569  
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# WIPRO TRAINING

By

TALENTIO SOLUTIONS INDIA Pvt. Ltd.



**For : IV B.Tech Students**

Date: 09-09-2019 to 16-09-2019

### Outcomes of the event:

1. Enhancement the Aptitude round clearing ability in interview process
2. Solve the real time problems for performing job functions easily
3. Interpret the concept of LOGICAL REASONING skills
4. Develop effective communication skills

**Principal:**

**Dr. V. Sudheer Babu**

M.Tech, Ph.D

**Coordinators:**

- Mr. M. Tharun, Assistant Professor -EEE
- Mr. K. Veenanand, Assistant Professor-ECE
- Mr. K. Vinay Sri Bal, Assistant Professor-CSE

**ORGANIZED BY:**

Placement Cell

**REGISTRATION  
FREE**

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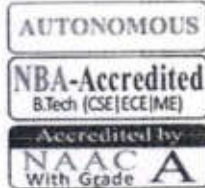




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**IOC-Outgoing**

DEPARTMENT: Training and Placement

DATE: 06-09-2019

ACADEMIC YEAR: 2019-20

ORIGINATOR	PRINCIPAL
CIRCULATED TO	HODs, Faculty and IV B. Tech Students – ECE, CSE & EEE

All the HOD's, staff and students are here by informed that the training and placement cell is conducting training program on WIPRO Specific Training for IV Year student's from 09-09-2019 to 16-09-2019. In this regard, all the faculty and students are here by informed to utilize this opportunity. The training schedule is given below.

**Timings:** 09.15 AM-12.30 PM (Fore noon)

01.15 PM-04.30PM (After noon)

**Venues:** CSE seminar Hall- CSE & ECE Batch 1

ECE seminar Hall- CSE & ECE Batch 2

EEE seminar Hall- EEE

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## Department of Electronics & Communication Engineering

**Academic Year: 2019-20**

**Name of the Event:** WIPRO Training

**Date of the Event:** 09-09-2019 to 16-09-2019

**Resource Person:** Talentio Solutions India Pvt Ltd

**Name of the coordinator:** Mr. K.Veenanand  
Asst Professor, Dept of ECE

**Target Audience:** IV B. Tech Students

**Total no of Participants:** 110

### **Description:**

The Department of Training & placement cell organized “WIPRO Training”, and is specially meant for engineering final year students. The sole objective of imparting aptitude training is to make students able to critically evaluate various real-life situations by resorting to an analysis of key issues and factors. This Aptitude Training helps them to demonstrate various principles involved in solving mathematical problems and thereby reducing the time taken for performing job functions. Logical reasoning is a test of a skill rather than a test of learned knowledge. This is used to determine a student's critical thinking skills, as well as their ability to use their own knowledge to solve a problem. Soft Skills (a vital portion of an individual's personality) is an intangible idea in which the qualities like aptitude, ability, integrity, reliability, positivity, flexibility, dependability, punctuality, management, cooperation, habits and practices are combined proficiently to capitalize on a person's work efficacy. Soft Skills do the work of combining all these components in accurate share into skills and shaping them into competencies.

The aim of the coding is to produce high quality system which can be performed in any situation. The programmer removes all the errors related to syntax and format and all the logical errors which find in the programmer during the coding phase.

**Topics covered:** Wipro Turbo Quants, Wipro Turbo Verbal English, Wipro Turbo Logical Reasoning and Wipro Turbo Technical Computer coding.

  
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**Outcome of event:** After completing this training course, students will be able to

1. Enhance the Aptitude Round Clearing ability in interview process
2. Solve the real-time problems for performing job functions easily
3. Interpret the concepts of LOGICAL REASONING Skills
4. Develop effective communication skills (spoken and written).


Photographs:



  
Coordinator

  
HOD

  
PRINCIPAL

  
DVR & Dr. HS MIC College of Technology,  
Kanchikacherla, NTR Dt.  
Andhra Pradesh, India - 521180