

GPIO Configuration
Structure and Enumerations

GPIO Pin Modes



```
graph LR; A[GPIO Pin Modes] --> B[GPIO Configuration Structure and Enumerations]
```

The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'GPIO Configuration Structure and Enumerations'. The box on the right is gray with a black border and contains the text 'GPIO Pin Modes'. A black arrow points from the right side of the gray box to the left side of the white box, indicating a relationship or flow from the pin modes to the configuration structure.