

## Ratish Puduppully

Third year PhD student at School of Informatics, University of Edinburgh

phone: +44 7384168825 ◊ email: r.puduppully@sms.ed.ac.uk ◊ webpage: <https://ratishsp.github.io/>

### EDUCATION

PhD in Informatics  
University of Edinburgh  
Advisor: Prof. Mirella Lapata  
Sep 2017 - Ongoing

MS in CSE by Research  
IIIT Hyderabad  
Grade: 9.33 CGPA  
Dec 2014 - Feb 2017

B.E in Electronics and Telecommunications  
Mumbai University, Mumbai  
Grade: equiv. to 9 CGPA  
Jul 2001 - Jun 2005

### PAPERS

University of Edinburgh's submission to the Document-level Generation and Translation Shared Task, **Ratish Puduppully**, Jonathan Mallinson, and Mirella Lapata. Proceedings of Workshop on Neural Generation and Translation, ACL 2019.

Data-to-text Generation with Entity Modeling - **Ratish Puduppully**, Li Dong, and Mirella Lapata. Proceedings of ACL. 2019.

Data-to-Text Generation with Content Selection and Planning - **Ratish Puduppully**, Li Dong, and Mirella Lapata. Proceedings of AAAI. 2019.

Transition-Based Deep Input Linearization - **Ratish Puduppully**, Yue Zhang and Manish Shrivastava. Proceedings of EACL. 2017.

Transition based Syntactic Linearization with Lookahead Features - **Ratish Puduppully**, Yue Zhang and Manish Shrivastava. Proceedings of NAACL-HLT. 2016.

Brahmi-Net - An online system for transliteration and script conversion for Indian languages - Anoop Kunchukuttan, **Ratish Puduppully** and Pushpak Bhattacharyya. Proceedings of NAACL-HLT System Demonstrations. 2015.

Merging Verb Senses of Hindi WordNet using Word Embeddings - Sudha Bhingardive, **Ratish Puduppully**, Dharendra Singh and Pushpak Bhattacharyya. Proceedings of ICON. 2014.

The IIT Bombay SMT System for ICON 2014 Tools Contest - Anoop Kunchukuttan, **Ratish Puduppully**, Rajen Chatterjee, Abhijit Mishra, Pushpak Bhattacharyya. Proceedings of ICON Tools Contest. 2014.

### PATENTS

Method and system of sharing content - Ben Willis, Sunil Natraj, Abhijeet Shinde, Tarun Agarwal, **Ratish Puduppully**, Santhi Swaroop Pulagala, Shu-Hua Chang. US Patent 9,256,695. 2016.

### HONORS

Edinburgh Global Research Scholarship and Principal's Career Development Scholarship for pursuing PhD studies at University of Edinburgh  
May 2017

Travel Grant from ACM India for presenting paper at EACL conference in Valencia, Spain  
Apr 2017

Travel Grant from Microsoft for presenting paper at NAACL conference in San Diego, US  
Jun 2016

JRD Tata Scholarship for Academic Excellence for undergraduate studies 2002-04

**TEACHING AND ADVISING** Tutor for Accelerated Natural Language Processing. 2018.

Mentored a BS student from Stanford University during the students' internship at University of Edinburgh in 2018.

**RESEARCH EXPERIENCE** Google Research London Jun 2019 - Oct 2019  
Intern with the text summarization team at Google Research London under the mentorship of Shashi Narayan. I worked on researching recurrent neural network grammar based approaches to jointly generate summary and its parse tree.

Singapore University of Technology and Design Mar 2017 - Aug 2017  
Research Assistant at NLP lab under guidance of Prof. Yue Zhang. I worked in the area of financial market prediction using text data.

IIT Bombay May 2014 - Dec 2014  
Research Engineer at Center for Indian Language Technology (CFILT) lab under guidance of Prof. Pushpak Bhattacharyya. Worked in areas of MT and WSD.

**INDUSTRY EXPERIENCE** Saba Software, Mumbai May 2008 – May 2014  
Technical Architect

Infosys Technologies Ltd Sep 2005 – May 2008  
Software Engineer

**PERSONAL INFORMATION** Languages: English fluent. Malayalam native. Hindi and Marathi fluent.  
Address: Room no 4.23, Informatics Forum, 10 Crichton Street, Edinburgh EH89AB