Ratish Puduppully

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EDUCATION

PhD in Informatics
University of Edinburgh   Sep 2017 - Sep 2020 (Expected)
Advisor: Prof. Mirella Lapata

MS in CSE by Research
IIIT Hyderabad   Dec 2014 - Feb 2017
Grade: 9.33 CGPA

B.E in Electronics and Telecommunications
Mumbai University, Mumbai   Jul 2001 - Jun 2005
Grade: equiv. to 9 CGPA

PAPERS

Data-to-Text Generation with Content Selection and Planning - **Ratish Puduppully**, Li Dong, and Mirella Lapata. Proceedings of AAAI. 2019.

Transition-Based Deep Input Linearization - **Ratish Puduppully**, Yue Zhang and Manish Shrivastava. Proceedings of EACL. 2017.

Transition based Syntactic Linearization with Lookahead Features - **Ratish Puduppully**, Yue Zhang and Manish Shrivastava. Proceedings of NAACL-HLT. 2016.

Brahmi-Net - An online system for transliteration and script conversion for Indian languages - Anoop Kunchukuttan, **Ratish Puduppully** and Pushpak Bhattacharyya. Proceedings of NAACL-HLT System Demonstrations. 2015.

Merging Verb Senses of Hindi WordNet using Word Embeddings - Sudha Bhingardive, **Ratish Puduppully**, Dhirendra Singh and Pushpak Bhattacharyya. Proceedings of ICON. 2014.

The IIT Bombay SMT System for ICON 2014 Tools Contest - Anoop Kunchukuttan, **Ratish Puduppully**, Rajen Chatterjee, Abhijit Mishra, Pushpak Bhattacharyya. Proceedings of ICON Tools Contest. 2014.

PATENTS

Method and system of sharing content - Ben Willis, Sunil Natraj, Abhijeet Shinde, Tarun Agarwal, **Ratish Puduppully**, Santhi Swaroop Pulagala, Shu-Hua Chang. US Patent 9,256,695. 2016.

HONORS

Edinburgh Global Research Scholarship and Principal's Career Development Scholarship for pursuing PhD studies at University of Edinburgh   May 2017

Travel Grant from ACM India for presenting paper at EACL conference in Valencia, Spain   Apr 2017

Travel Grant from Microsoft for presenting paper at NAACL conference in San Diego, US   Jun 2016

JRD Tata Scholarship for Academic Excellence   2002-04

TEACHING AND ADVISING	Tutor for Accelerated Natural Language Processing. 2018.
RESEARCH EXPERIENCE	Singapore University of Technology and Design Mar 2017 - Aug 2017 Research Assistant at NLP lab under guidance of Prof. Yue Zhang. I worked in the area of financial market prediction using text data. IIT Bombay May 2014 - Dec 2014 Research Engineer at Center for Indian Language Technology (CFILT) lab under guidance of Prof. Pushpak Bhattacharyya. Worked in areas of MT and WSD.
INDUSTRY EXPERIENCE	Saba Software, Mumbai May 2008 – May 2014 Technical Architect Infosys Technologies Ltd Sep 2005 – May 2008 Software Engineer
PERSONAL INFORMATION	Languages: English fluent. Malayalam native. Hindi and Marathi fluent. Address: Room no 4.23, Informatics Forum, 10 Crichton Street, Edinburgh EH89AB
REFERENCES	Mirella Lapata, Professor, Institute for Language, Cognition and Computation, School of Infomatics, University of Edinburgh. mlap@inf.ed.ac.uk Adam Lopez, Reader, Institute for Language, Cognition, and Computation, School of Infomatics, University of Edinburgh. alopez@inf.ed.ac.uk Yue Zhang, Associate Professor, Westlake University. yue.zhang@wias.org.cn